

Single-chip Type with Built-in FET Switching Regulator Series

1.0A, Step-down Switching regulators with Built-in Power MOSFET

BU90054GWZ

●General Description

The BU90054GWZ are a high efficiency 6MHz synchronous step-down switching regulator with ultra low current PFM mode. It provides up to 1.0A load current and an input voltage range from 3.0V to 5.5V, optimized for battery powered portable applications. BU90054GWZ has a mode control pin that allows the user to select Forced PWM(Pulse Width Modulation) mode or PFM(Pulse Frequency Modulation) and PWM auto change mode utilized power save operation at light load current.

●Features

- Fast transient response
- Automatic PFM/PWM operation
- Forced PWM operation
- Internal Soft Start
- Under voltage lockout
- Over current protection
- Thermal shutdown

●Applications

Smart phones, Cell phones, Portable applications and Micro DC/DC modules, USB accessories

●Package(s)
UCSP30L1

W(Typ.) x D(Typ.) x H(Max.)
1.30mm x 0.90mm x 0.33mm

●Typical Application Circuit(s)

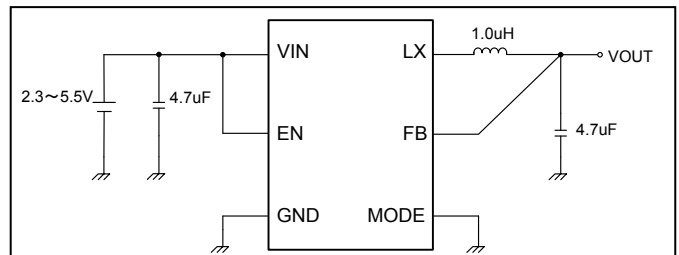


Figure 1. Typical Application Circuit(s)

●Lineup

Part No.	Output voltage	Input voltage	Switching frequency	Operating mode	
				MODE=L	MODE=H
BU90054GWZ	1.80V	2.3V to 5.5V	4.8MHz to 6.0MHz	Automatic PFM/PWM	Forced PWM

●Pin Configuration(s)

(BOTTOM VIEW)

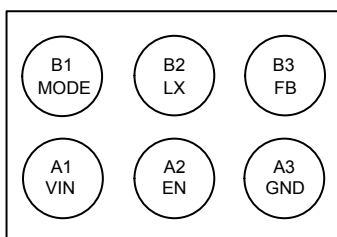


Figure 2. Pin Configuration(s)

●Pin Description(s)

Pin No.	Symbol	Function
A1	VIN	Power supply input pin
A2	EN	Enable pin
A3	GND	GND pin
B1	MODE	Forced PWM mode pin
B2	LX	Inductor connection pin
B3	FB	Feedback voltage input pin

●Block Diagram(s)

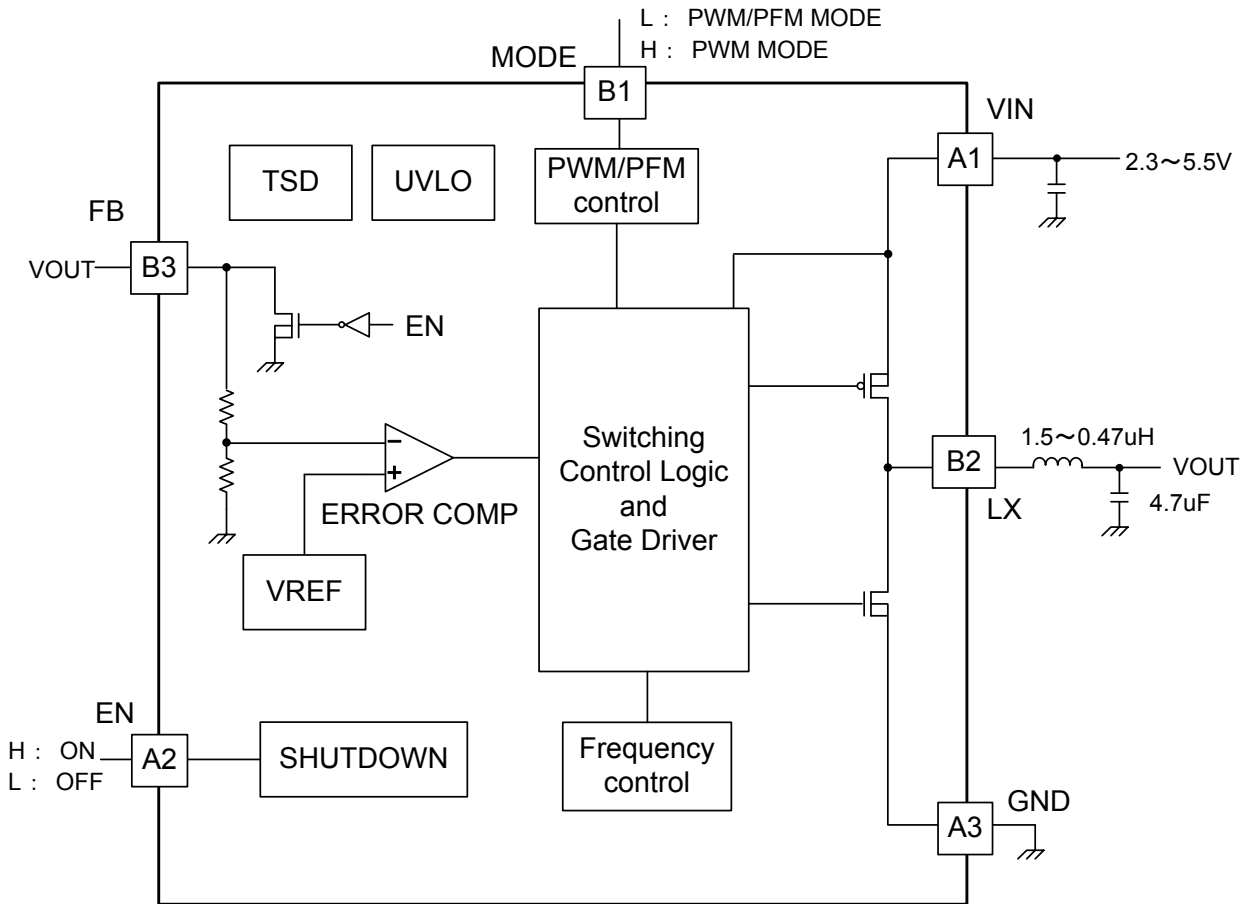


Figure 3. Block Diagram(s)

●Description of Block(s)

The BU90054GWZ are a synchronous step-down DC/DC converter that achieves fast transient response from light load to heavy load by hysteretic PWM control system and current constant PFM control system.

OPWM control

BU90054GWZ operates by hysteretic PWM control. This scheme ensures fast switching, high efficiency, and fast transient response.

When the output voltage is below the VREF voltage, the error comparator output is low to high and turning on P-channel MOSFET until above the VREF voltage and minimum on time.

OPFM control

At light load the regulator and MODE=low, the regulator operates with reduced switching frequency and improves the efficiency. During PFM operation, the output voltage slightly higher than typical output voltage.

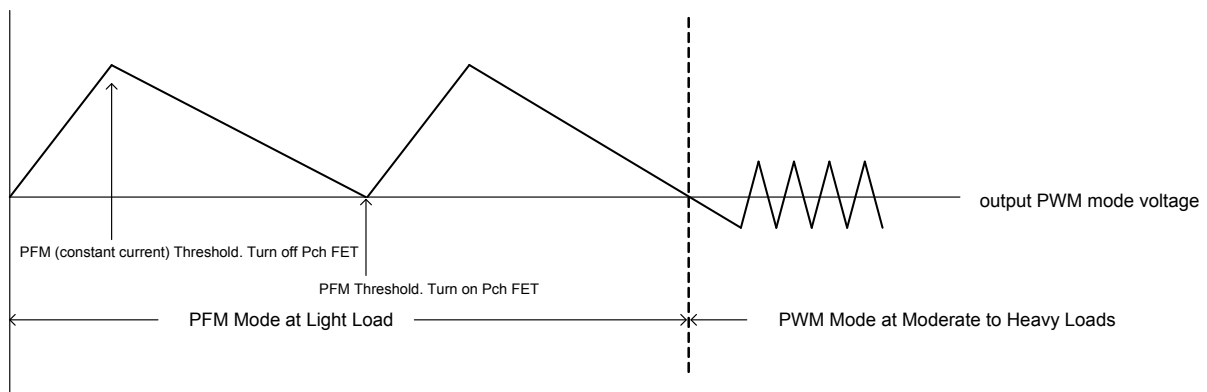


Figure 4. Operation of PFM mode and PWM mode

●Description of operations

1) Shutdown

If the EN input pin set to low (<0.4V), all circuit are shut down and the regulator is standby mode. Do not leave the EN pin floating.

2) Soft start function

The regulator has a soft start circuit that reduces in-rush current at start-up. Typical start up times with a 4.7uF output capacitor is 120usec.

3) Current limit

The BU90054GWZ has a current limit circuit that protects itself and external components during overload condition.

4) UVLO

The BU90054GWZ has an Under Voltage Lock Out circuit that turn off device when $V_{IN} > 2.05V$ (typ.)

5) FORCED PWM MODE

Setting MODE pin high (>1.4V) places the regulator in forced PWM. This control provides noise reduction and output stability. Do not leave the MODE pin floating.

6) TSD

The BU90054GWZ has a thermal shutdown feature to protect the device if the junction temperature exceeds 150°C.

In thermal shutdown, the DRIVER is disabled.

This circuit is only to cut off the IC from thermal runaway, and has not been design to protect or guarantee the IC.

Therefore, the user should not plan to activate this circuit with continued operation in mind.

●Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Maximum input power supply voltage	VIN	7	V
Maximum voltage at EN, FB, LX, MODE	VEN, VFB, VLX, VMODE	7	V
Power dissipation	Pd	0.39 ^{*1}	W
Operating temperature range	Topr	-40 to +85	°C
Storage temperature range	Tstg	-55 to +125	°C
Junction temperature	Tjmax	+125	°C

(*1) When mounted on the specified PCB (55mm x 63mm), Deducted by 3.9m W/c when used over Ta=25c

●Recommended Operating Rating(s)

Parameter	Symbol	Rating			Unit
		Min.	Typ.	Max.	
Input voltage	VIN	2.3	-	5.5	V

●Electrical Characteristic(s) (unless otherwise specified VIN=3.6V, Ta=25°C)

Item	Symbol	Rating			Unit	Condition	
		Min.	Typ.	Max.			
【Switching regulator】							
Output voltage accuracy	VOUTA	1.764	1.800	1.836	V	MODE:H(PWM Operation)	
		1.764	1.800	1.854		MODE:L(PFM Operation)	
Maximum load current	IoutMAX1	-	-	1.0	A	3.0V ≤ VIN < 5.5V	
	IoutMAX2	-	-	0.8	A	2.7V ≤ VIN < 3.0V	
	IoutMAX3	-	-	0.6	A	2.3V ≤ VIN < 2.7V	
【Soft start】							
Soft start time	Tss	65	120	240	usec		
【Frequency control】							
Switching frequency	fosc	4.8	5.4	6.0	MHz	No load, MODE:H	
【Driver】							
PchFET on resistance	RonP1	-	250	400	mOhm	VIN=5.0V	
	RonP2		300	450	mOhm	VIN=3.6V	
NchFET on resistance	RonN1	-	220	350	mOhm	VIN=5.0V	
	RonN2		250	380	mOhm	VIN=3.6V	
【Control】							
EN pin control voltage	Operation	VENH	1.4	-	VIN	V	
	Non Operation	VENL	0	-	0.4	V	
MODE pin control voltage	Operation	VMODEH	1.4	-	VIN	V	Forced PWM
	Non Operation	VMODEL	0	-	0.4	V	Automatic PFM/PWM
【UVLO】							
Protect threshold voltage	Uvth	1.95	2.05	2.15	V		
Hysteresis	Uvhy	50	100	150	mV		
【Current limit】							
Current limit threshold	ILIMIT	1.5	1.7	1.9	A	PMOS current detect, Open loop	
【Output discharge】							
Output discharge resistance	DRES	55	110	220	Ohm	EN=0V	
【Circuit current】							
Operating quiescent current	IINS1	-	45	65	uA	EN:H, MODE:L, VOUT=3.6V forced Not switching	
Shutdown current	SHD	-	0	1	uA	EN=0V	

●Electrical Characteristic curves (Reference data)

Parts

L:LQM21MPN1R0NG0 (2.0mm×1.6mm×1.0mm Murata)

COUT:GRM155R60J475M(1.0mm×0.5mm×0.5mm Murata)

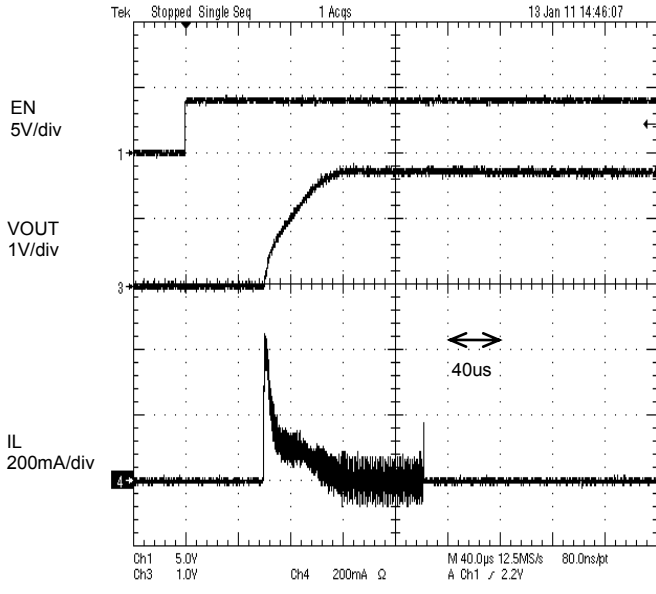


Figure 5. Start up

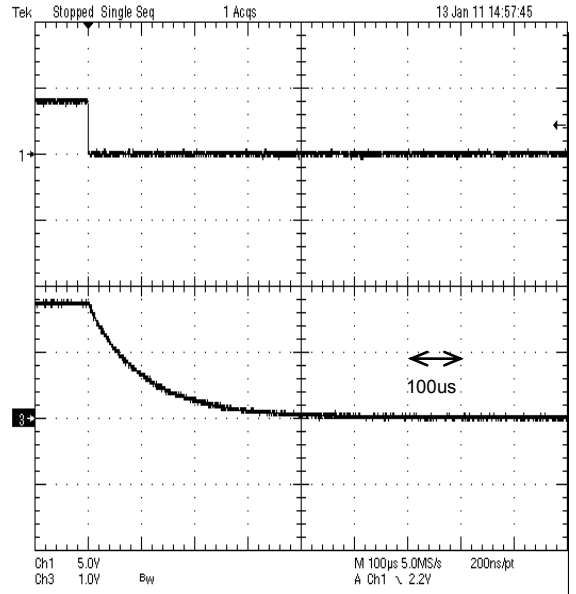


Figure 6. Shut down

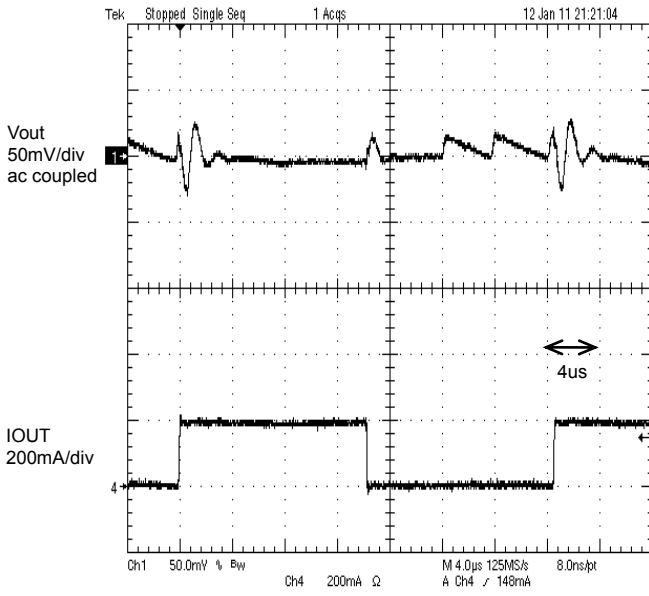


Figure 7. Load transient response 5mA to 200mA
tr=tf=100ns, Mode : Low

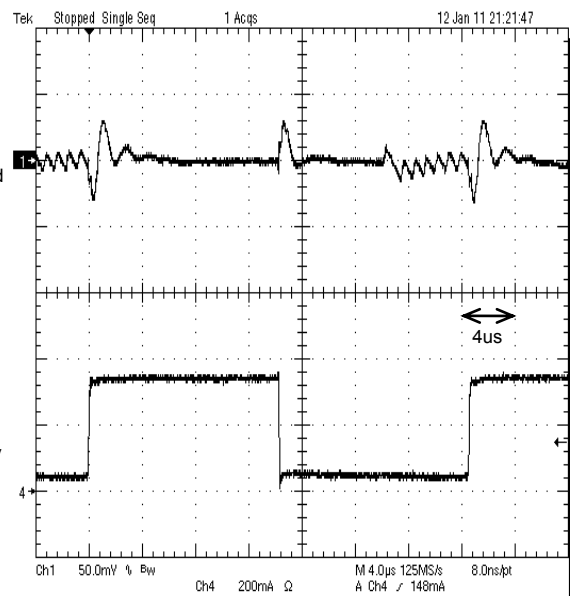


Figure 8. Load transient response 50mA to 350mA
tr=tf=100ns, Mode :Low

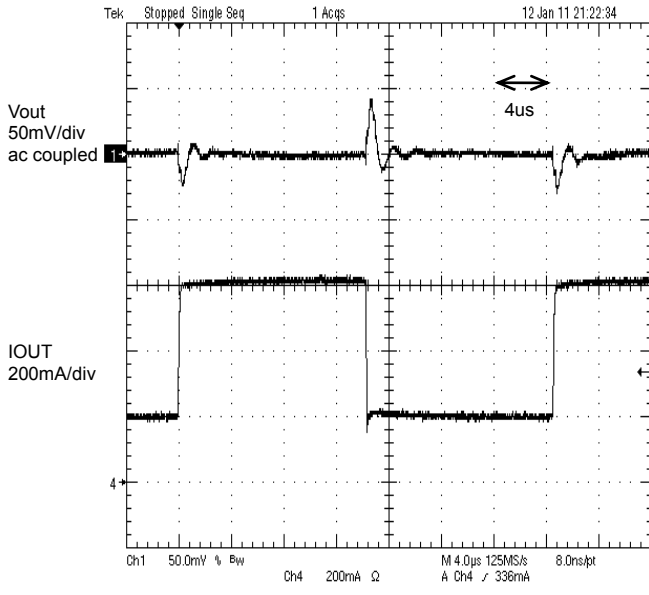


Figure 9. Load transient response 200mA to 600mA
tr=tf=100ns, MODE : Low

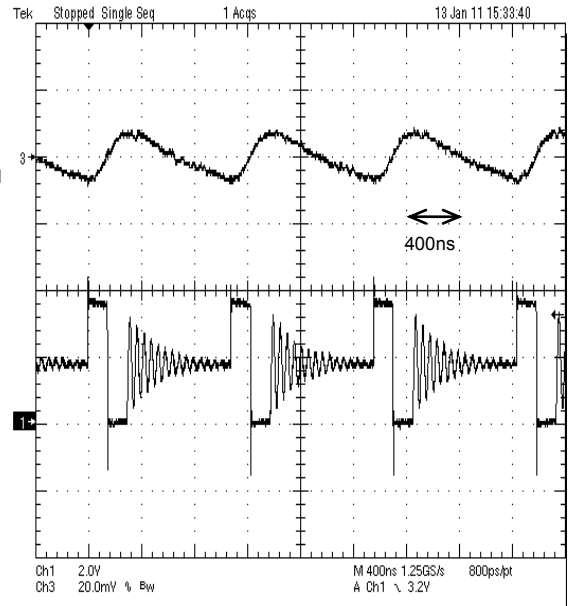


Figure 10. PFM mode Operation, Iout=50mA

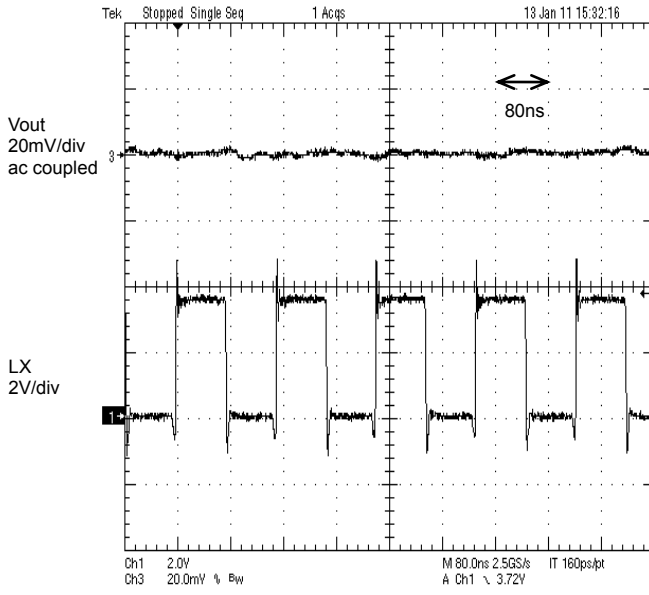


Figure 11. PWM mode Operation, Iout=100mA

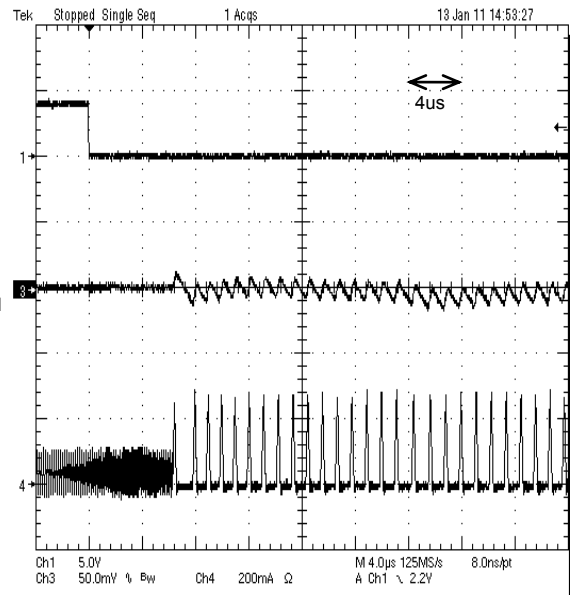


Figure 12. Mode Change Response
MODE : High to Low

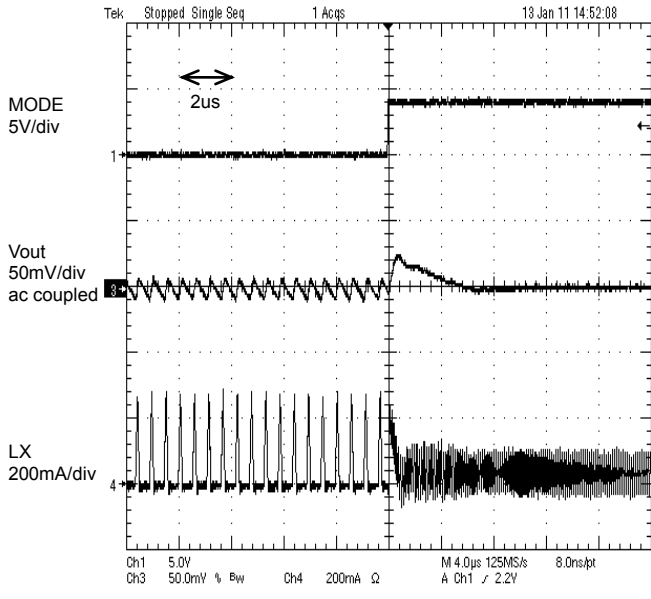


Figure 13. Mode Change Response
MODE : Low to High

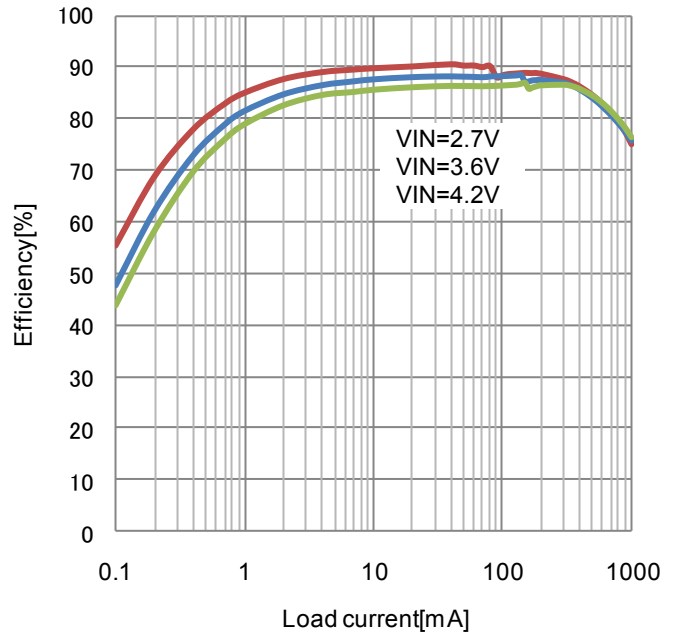


Figure 14. Efficiency vs Load current
PWM/PFM Auto mode

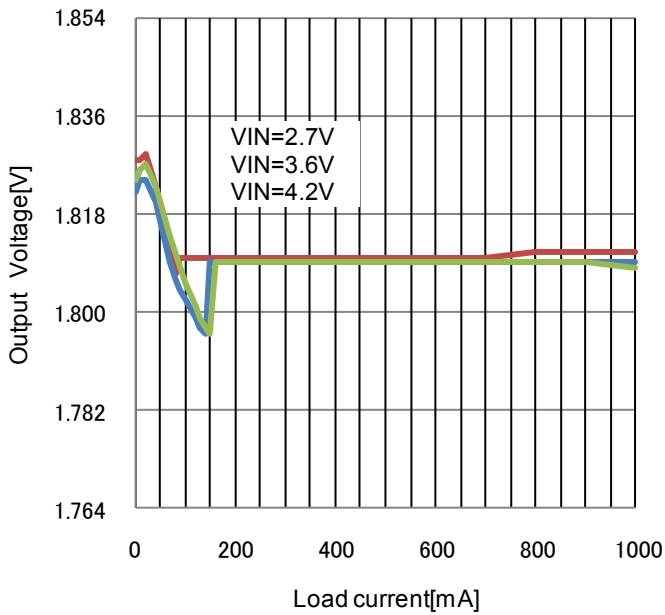


Figure 15. Load regulation
PWM/PFM Auto mode

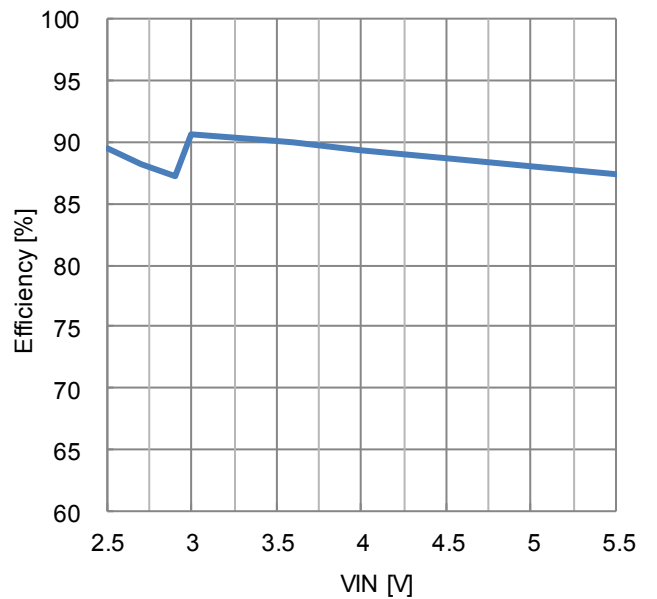


Figure 16. Efficiency vs VIN
PWM/PFM Auto mode, Iout=125mA
L : DFE252012C1R0

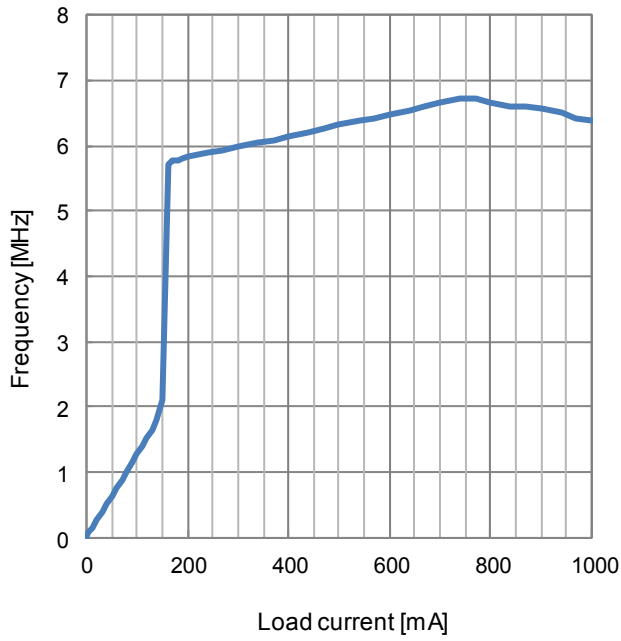


Figure 17. Frequency vs Load current
PWM/PFM Auto mode

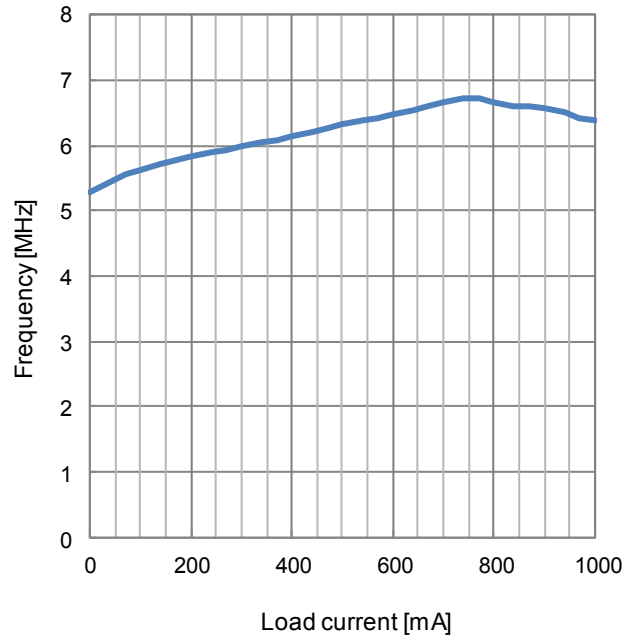


Figure 18. Frequency vs Load current
PWM mode Operation

●PC Board layout

The suggested PCB layout for the BU90054GWZ are shown in Figure. The following guidelines should be used to ensure a proper layout.

- 1) The input capacitor CIN should be connect as closely possible to VIN pin and GND pin.
- 2) From the output voltage to the FB pin line should be as separate as possible.
- 3) COUT and L should be connected as closely as possible. The connection of L to the LX pin should be as short as possible.

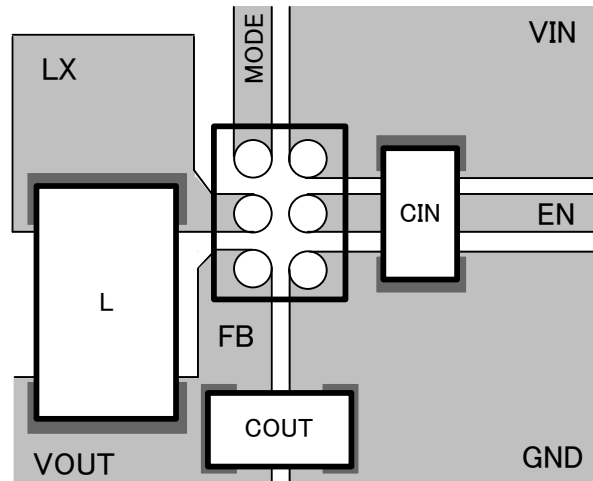


Figure 19. PCB layout

●External parts selection

Inductor selection

The inductance significantly depends on output ripple current. As shown by following equation, the ripple current decreases as the inductor and/or switching frequency increase.

$$\Delta I_L = \frac{(V_{IN} - V_{OUT}) \times V_{OUT}}{L \times V_{IN} \times f}$$

f: switching frequency L: inductance ΔI_L : inductor current ripple

As a minimum requirement, the DC current rating of the inductor should be equal to the maximum load current plus half of the inductor current ripple as shown by the following equation.

$$I_{LPEAK} = I_{OUTMAX} + \frac{\Delta I_L}{2}$$

1) Recommended inductor selection

• $I_{out} \leq 1A$

LQM2MPN1R0NG0 (2.0mm × 1.6mm × 1.0mm Murata)
MIPSZ2016D1R0FH (2.0mm × 1.6mm × 1.0mm FDK)
DFE252012C1R0 (2.5mm × 2.0mm × 1.2mm TOKO)
DFE252010C1R0 (2.5mm × 2.0mm × 1.0mm TOKO)

• $I_{out} \leq 0.6A$

LQM21PN1R0NGC (2.0mm×1.2mm×1.0mm Murata)
MIPSZ2012D1R0 (2.0mm × 1.2mm × 1.0mm FDK)
MIPSTZ1608D1R0 (1.6mm × 0.8mm × 0.8mm FDK)
MLP2012H1R0M (2.0mm×1.2mm×1.0mm TDK)
CKP2012N1R0N (2.0mm×1.2mm×1.0mm Taiyo Yuden)

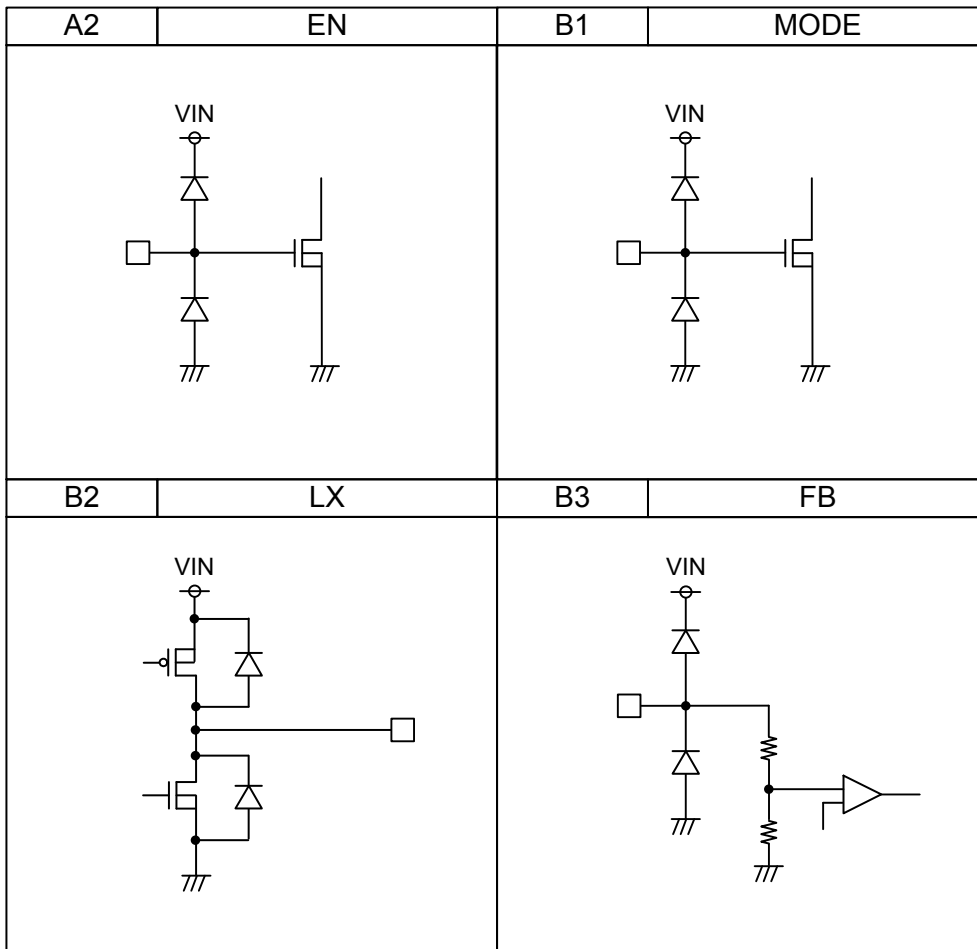
2) Recommended input capacitor(CIN) selection

GRM155R60J225M (1.0mm × 0.5mm × 0.5mm Murata)
GRM155R60J475M (1.0mm × 0.5mm × 0.5mm Murata)
GRM155R60G106M (1.0mm × 0.5mm × 0.5mm Murata)

3) Recommended output capacitor(COUT) selection

GRM155R60J475M (1.0mm × 0.5mm × 0.5mm Murata)
GRM155R60G106M (1.0mm × 0.5mm × 0.5mm Murata)

● I/O equivalence circuit(s)



● Caution of use

1) Absolute maximum ratings

An excess in the absolute maximum rating, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.

2) GND voltage

The potential of GND pin must be minimum potential in all condition. As an exception, the circuit design allows voltages up to -0.3 V to be applied to the IC pin.

3) Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

4) Inter-pin shorts and mounting errors

Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.

5) Actions in strong electromagnetic field

Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.

6) Mutual impedance

Power supply and ground wiring should reflect consideration of the need to lower mutual impedance and minimize ripple as much as possible (by making wiring as short and thick as possible or rejecting ripple by incorporating inductance and capacitance).

7) Thermal shutdown Circuit (TSD Circuit)

This model IC has a built-in TSD circuit. This circuit is only to cut off the IC from thermal runaway, and has not been design to protect or guarantee the IC. Therefore, the user should not plan to activate this circuit with continued operation in mind.

8) Regarding input pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated.

P-N junctions are formed at the intersection of these P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, as shown in the figures below, the relation between each potential is as follows:

When $GND > Pin A$ and $GND > Pin B$, the P-N junction operates as a parasitic diode.

When $GND > Pin B$, the P-N junction operates as a parasitic transistor.

Parasitic diodes can occur inevitable in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Accordingly, methods by which parasitic diodes operate, such as applying a voltage that is lower than the GND (P substrate) voltage to an input pin, should not be used.

9) Disturbance light

In a device where a portion of silicon is exposed to light such as in a WL-CSP, IC characteristics may be affected due to photoelectric effect. For this reason, it is recommended to come up with countermeasures that will prevent the chip from being exposed to light.

Status of this document

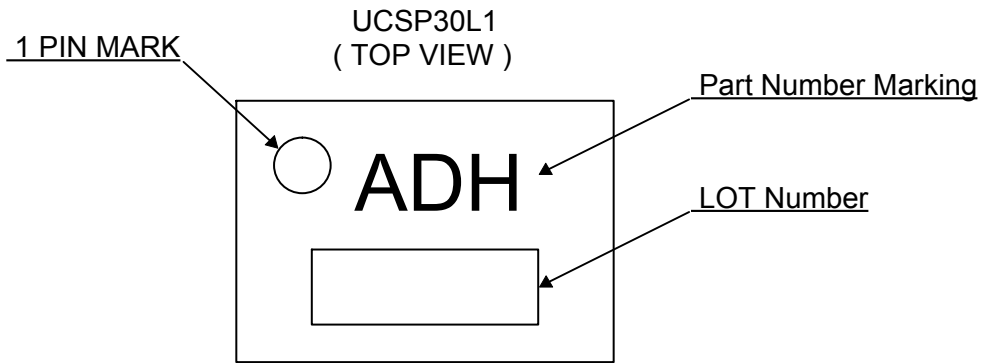
The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority

●Ordering Information

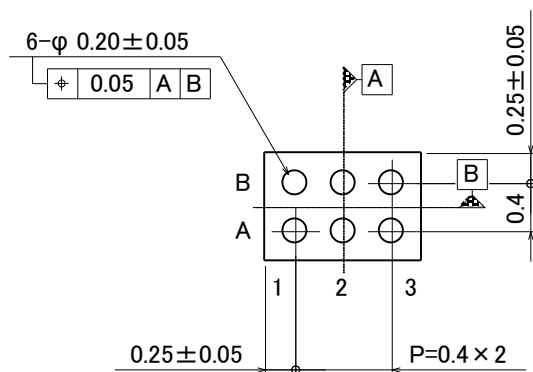
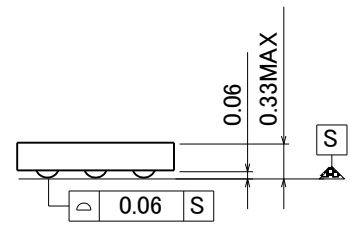
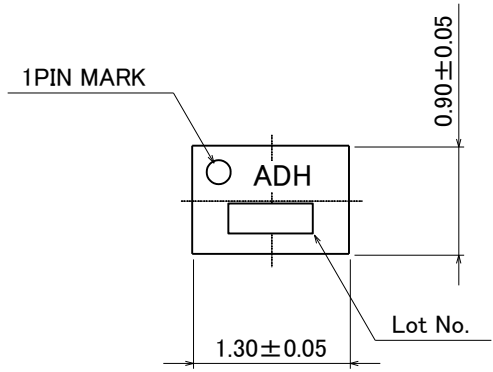


●Marking Diagram(s)(TOP VIEW)



●Physical Dimension Tape and Reel Information

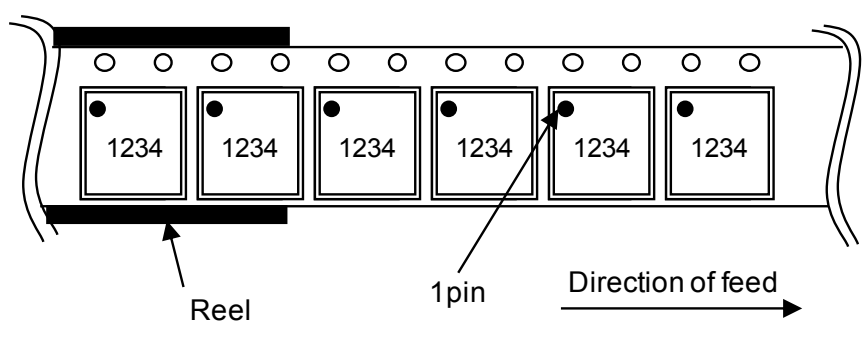
Package Name	UCSP30L1 (BU90054GWZ)
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(Unit : mm)

< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	6000pcs
Direction of feed	E2 The direction is the pin 1 of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand



●Revision History

Date	Revision	Changes
29.Jul.2014	001	New Release.
8.Dec.2014	002	Page10 Recommended inductor selection DFE252010C1R0 added. Page12 Caution of use 9) Disturbance light added.

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
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1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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