

## General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

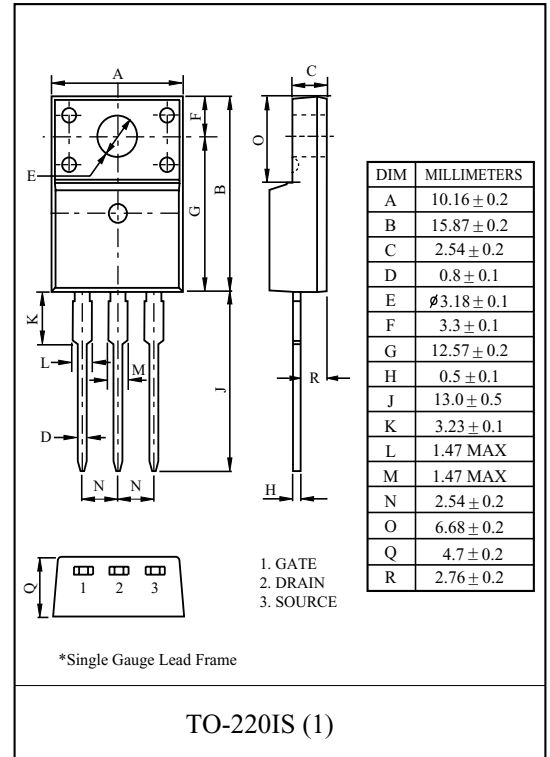
## FEATURES

- $V_{DSS}=680V$ ,  $I_D=7A$
- Drain-Source ON Resistance :  
 $R_{DS(ON)}(Max)=1.25\Omega$  @  $V_{GS}=10V$
- $Q_g(\text{typ.})=24nC$

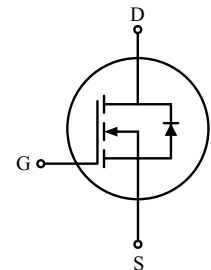
## MAXIMUM RATING (Tc=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		$V_{DSS}$	680	V
Gate-Source Voltage		$V_{GSS}$	± 30	V
Drain Current	@T <sub>c</sub> =25°C	$I_D$	7*	A
	@T <sub>c</sub> =100°C		4.2*	
	Pulsed (Note1)	$I_{DP}$	18*	
Single Pulsed Avalanche Energy (Note 2)		$E_{AS}$	212	mJ
Repetitive Avalanche Energy (Note 1)		$E_{AR}$	1.6	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Drain Power Dissipation	T <sub>c</sub> =25°C	$P_D$	44.6	W
	Derate above 25°C		0.36	W/°C
Maximum Junction Temperature		T <sub>j</sub>	150	°C
Storage Temperature Range		T <sub>stg</sub>	-55 ~ 150	°C
<b>Thermal Characteristics</b>				
Thermal Resistance, Junction-to-Case		R <sub>thJC</sub>	2.8	°C/W
Thermal Resistance, Junction-to-Ambient		R <sub>thJA</sub>	62.5	°C/W

\* : Drain current limited by maximum junction temperature.



## PIN CONNECTION



# KF7N68F

## ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	680	-	-	V
Breakdown Voltage Temperature Coefficient	Δ BV <sub>DSS</sub> /Δ T <sub>j</sub>	I <sub>D</sub> =250μA, Referenced to 25 °C	-	0.65	-	V/°C
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> =680V, V <sub>GS</sub> =0V,	-	-	10	μA
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.5	-	4.5	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =± 30V, V <sub>DS</sub> =0V	-	-	± 100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A	-	1.0	1.25	Ω
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =520V, I <sub>D</sub> =7.0A V <sub>GS</sub> =10V (Note4,5)	-	24	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	5.4	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	9.5	-	
Turn-on Delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =325V I <sub>D</sub> =7.0A R <sub>G</sub> =25Ω (Note4,5)	-	30	-	ns
Turn-on Rise time	t <sub>r</sub>		-	30	-	
Turn-off Delay time	t <sub>d(off)</sub>		-	50	-	
Turn-off Fall time	t <sub>f</sub>		-	25	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	1010	-	pF
Output Capacitance	C <sub>oss</sub>		-	111	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	7.0	-	
Gate Resistance	R <sub>g</sub>		-	2.5	5	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	I <sub>S</sub>	V <sub>GS</sub> <V <sub>th</sub>	-	-	7	A
Pulsed Source Current	I <sub>SP</sub>		-	-	28	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =7.0A, V <sub>GS</sub> =0V	-	-	1.4	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =7.0A, V <sub>GS</sub> =0V, dI <sub>S</sub> /dt=100A/μs	-	350	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	3.6	-	μC

Note 1) Repetivity rating : Pulse width limited by junction temperature.

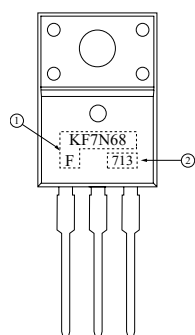
Note 2) L = 8.1mH, I<sub>S</sub>=7.0A, V<sub>DD</sub>=50V, R<sub>G</sub>=25Ω, Starting T<sub>j</sub>=25 °C.

Note 3) I<sub>S</sub> ≤ 7.0A, dI<sub>S</sub>/dt ≤ 200A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>j</sub>=25 °C.

Note 4) Pulse Test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%.

Note 5) Essentially independent of operating temperature.

## Marking



① PRODUCT NAME  
② LOT NO

# KF7N68F

Fig1.  $I_D - V_{DS}$

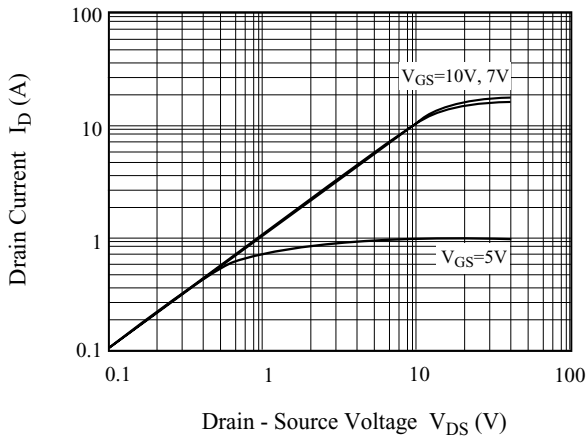


Fig2.  $I_D - V_{GS}$

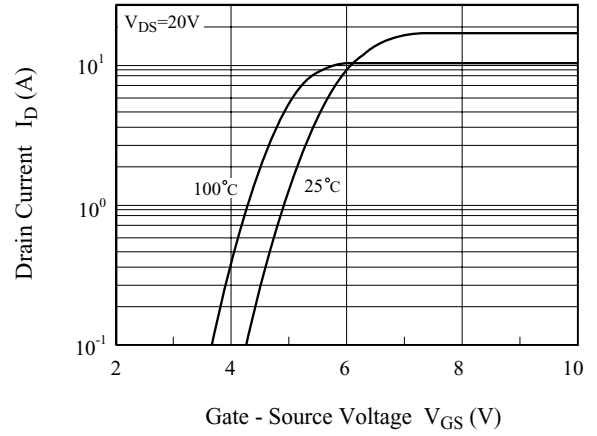


Fig3.  $BV_{DSS} - T_j$

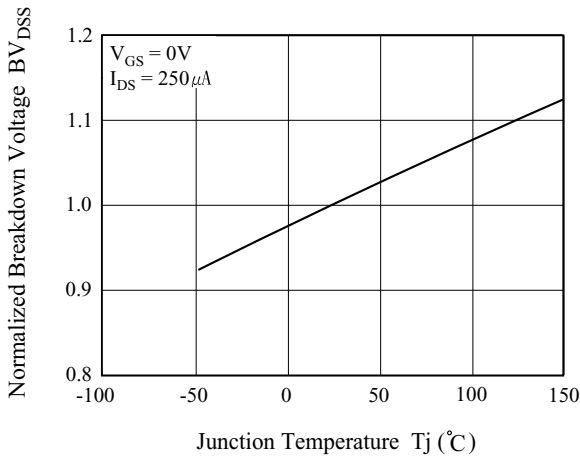


Fig4.  $R_{DS(ON)} - I_D$

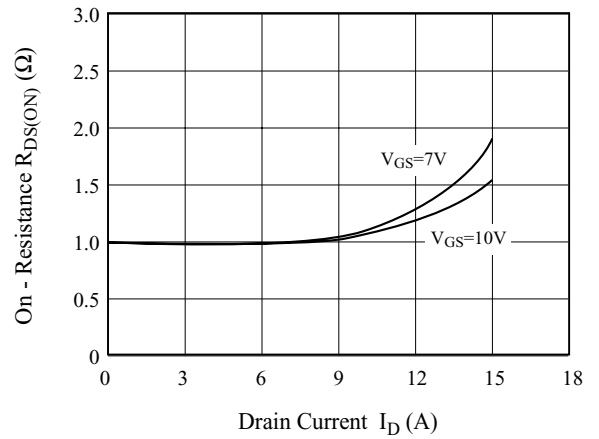


Fig5.  $I_S - V_{SD}$

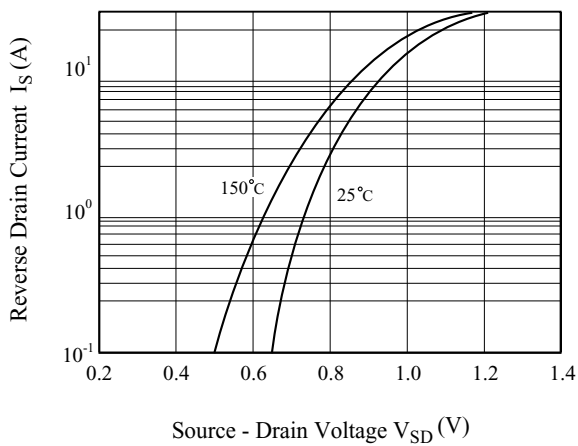
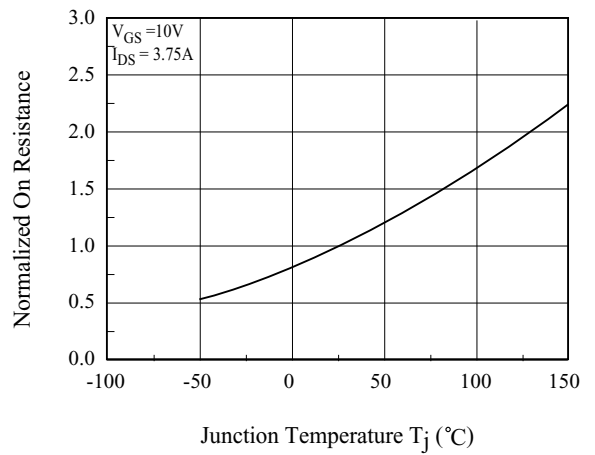


Fig6.  $R_{DS(ON)} - T_j$



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Fig 7. C -  $V_{DS}$

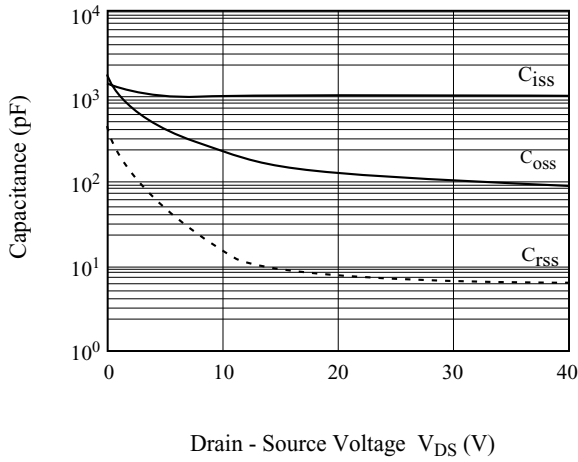


Fig8.  $Q_g$ -  $V_{GS}$

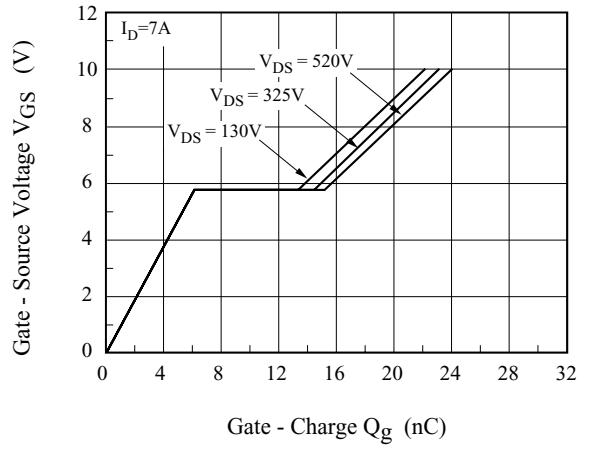


Fig9. Safe Operation Area

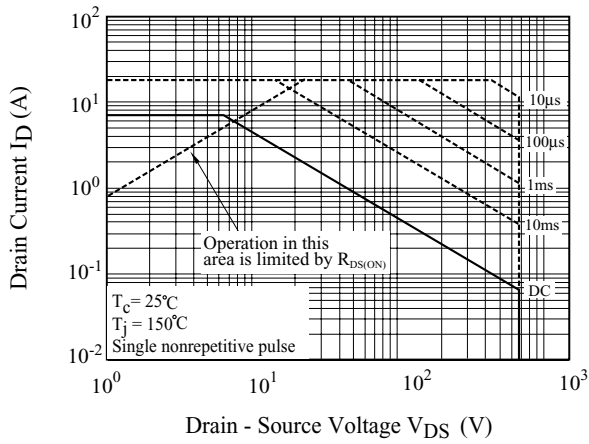


Fig10. Transient Thermal Response Curve

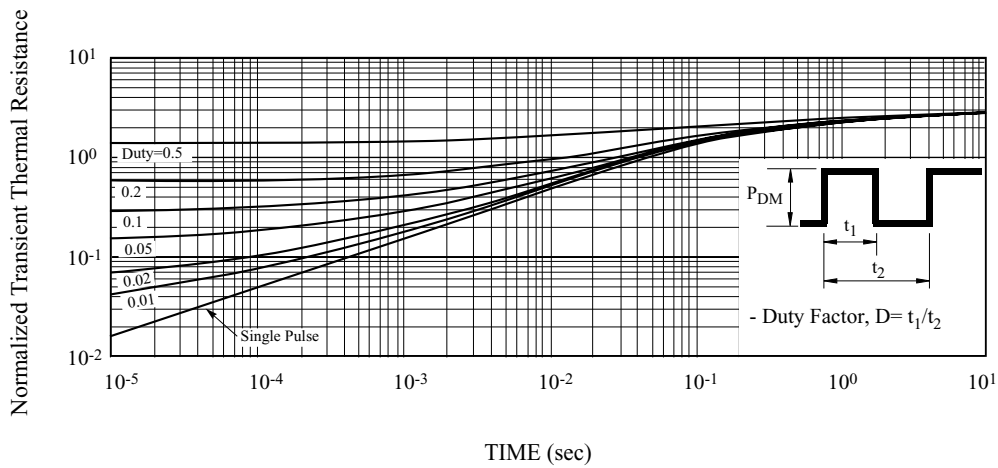


Fig11. Gate Charge

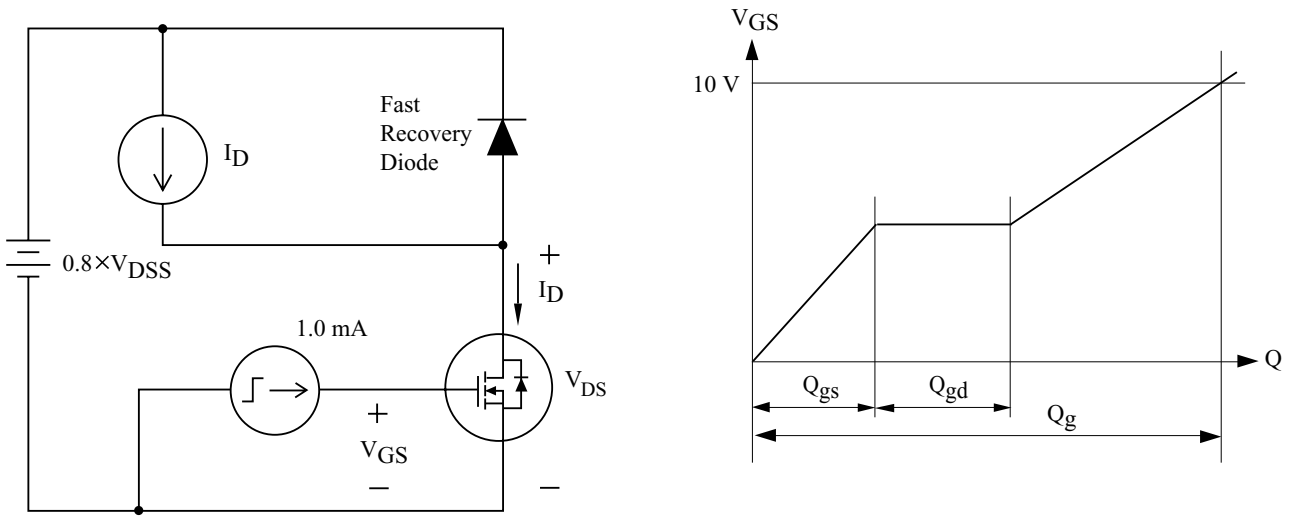


Fig12. Single Pulsed Avalanche Energy

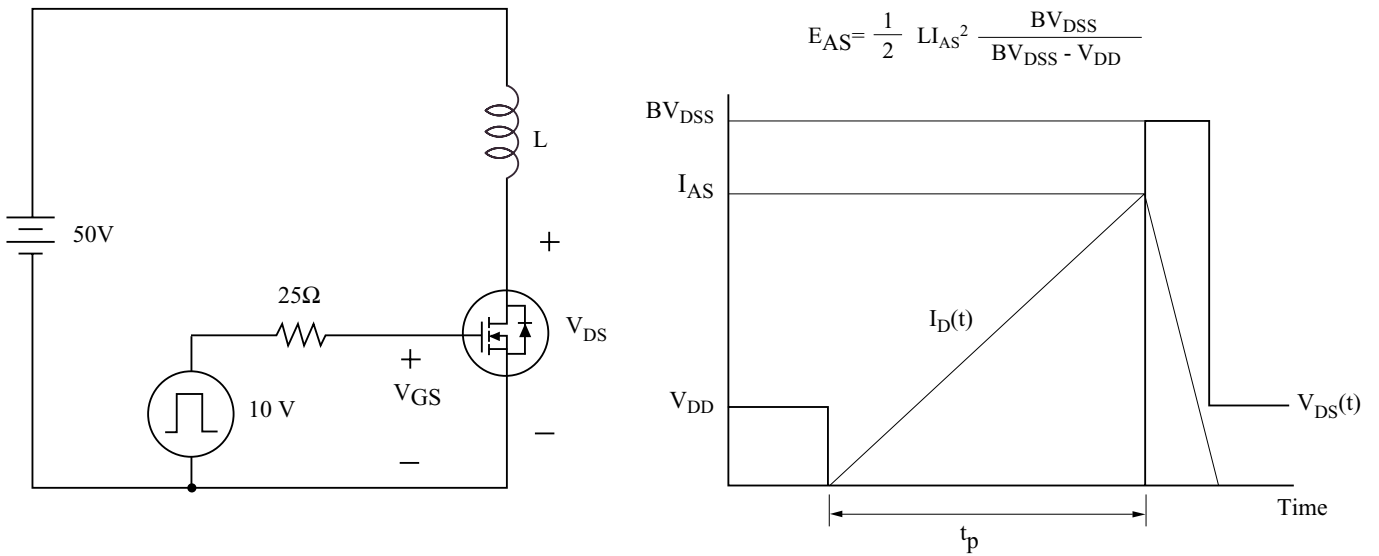


Fig13. Resistive Load Switching

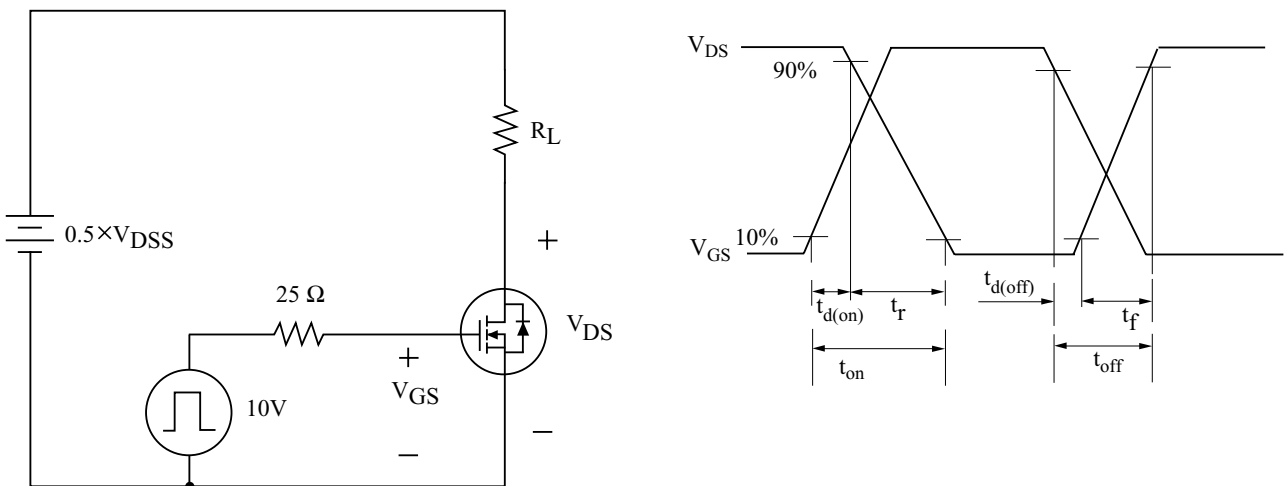


Fig14. Source - Drain Diode Reverse Recovery and  $dv/dt$

