

FDP023N08B_F102

N-Channel PowerTrench® MOSFET

75V, 242A, 2.35mΩ

Features

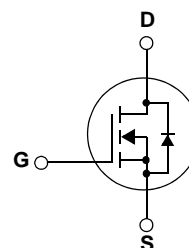
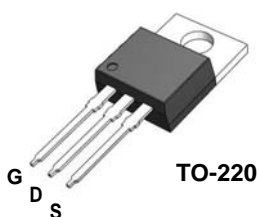
- $R_{DS(on)} = 1.96m\Omega$ (Typ.) @ $V_{GS} = 10V, I_D = 75A$
- Low FOM $R_{DS(on)} * Q_G$
- Low reverse recovery charge, Q_{rr}
- Soft reverse recovery body diode
- Enables highly efficiency in synchronous rectification
- Fast Switching Speed
- 100% UIL Tested
- RoHS Compliant

Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advance PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Application

- Synchronous Rectification
- Battery Charger and Battery Protection circuit
- DC motor drives and Uninterruptible Power Supplies
- Micro Solar Inverter



MOSFET Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted*

Symbol	Parameter	FDP023N08B_F102	Units
V_{DSS}	Drain to Source Voltage	75	V
V_{GSS}	Gate to Source Voltage	±20	V
I_D	Drain Current	-Continuous ($T_C = 25^\circ C$, Silicon Limited)	242*
		-Continuous ($T_C = 100^\circ C$, Silicon Limited)	171*
		-Continuous ($T_C = 25^\circ C$, Package Limited)	120
I_{DM}	Drain Current	- Pulsed (Note 1)	968
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	961
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6
P_D	Power Dissipation	($T_C = 25^\circ C$)	245
		- Derate above $25^\circ C$	1.64
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ C$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	$^\circ C$

* Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDP023N08B_F102	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.61	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	

Package Marking and Ordering Information

Device Marking	Device	Package	Description	Quantity
FDP023N08B	FDP023N08B_F102	TO-220	F102: Trimmed Leads	50

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}, T_C = 25^\circ\text{C}$	75	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$, Referenced to 25°C	-	0.35	-	$\text{V}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$ $V_{DS} = 60\text{V}, T_C = 150^\circ\text{C}$	-	-	1 500	μA
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	2.0	-	3.8	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{V}, I_D = 75\text{A}$	-	1.96	2.35	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 75\text{A}$	-	185	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 37.5\text{V}, V_{GS} = 0\text{V}$ $f = 1\text{MHz}$	-	10350	13765	pF
C_{oss}	Output Capacitance		-	1855	2465	pF
C_{rss}	Reverse Transfer Capacitance		-	46.8	-	pF
$C_{oss(er)}$	Energy Related Output Capacitance	$V_{DS} = 37.5\text{V}, V_{GS} = 0\text{V}$	-	3290	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 37.5\text{V}, I_D = 100\text{A}$ $V_{GS} = 10\text{V}$	-	150	195	nC
Q_{gs}	Gate to Source Gate Charge		-	50.3	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	31.7	-	nC
$V_{plateau}$	Gate Plateau Voltage		(Note 4)	-	4.9	-
Q_{sync}	Total Gate Charge Sync.	$V_{DS} = 0\text{V}, I_D = 50\text{A}$ (Note 5)	-	127.4	-	nC
Q_{oss}	Output Charge	$V_{DS} = 37.5\text{V}, V_{GS} = 0\text{V}$	-	146.2	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 37.5\text{V}, I_D = 100\text{A}$ $V_{GS} = 10\text{V}, R_{GEN} = 4.7\Omega$	-	41	92	ns
t_r	Turn-On Rise Time		-	71	151	ns
$t_{d(off)}$	Turn-Off Delay Time		-	111	232	ns
t_f	Turn-Off Fall Time		(Note 4)	-	56	122
ESR	Equivalent Series Resistance (G-S)	$f = 1\text{MHz}$	-	2.23	-	Ω

Drain-Source Diode Characteristics

I_S	Maximum Continuous Drain to Source Diode Forward Current	-	-	242*	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	968	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_{SD} = 75\text{A}$	-	-	1.3	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{V}, V_{DD} = 37.5\text{V}, I_{SD} = 100\text{A}$	-	79.3	-	ns
Q_{rr}	Reverse Recovery Charge	$di_F/dt = 100\text{A}/\mu\text{s}$	-	114	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L = 3\text{mH}, I_{AS} = 25.32\text{A}$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 100\text{A}, di/dt \leq 200\text{A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics
5. See the test circuit in page 8

Typical Performance Characteristics

Figure 1. On-Region Characteristics

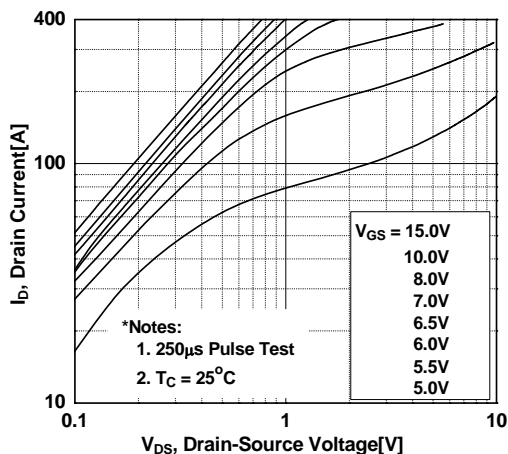


Figure 2. Transfer Characteristics

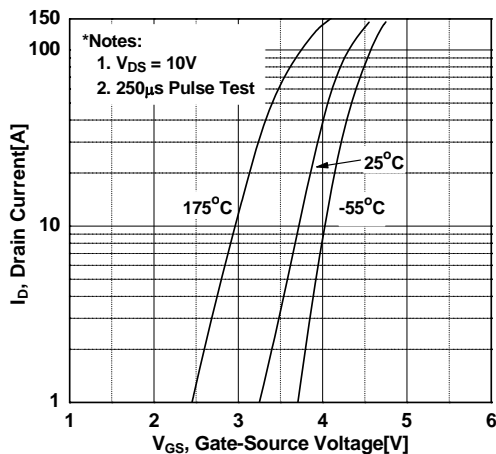


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

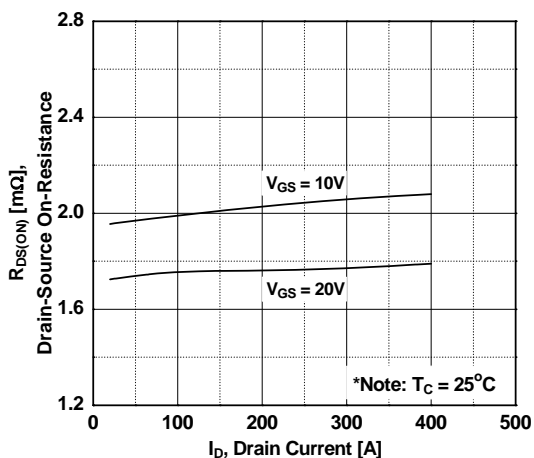


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

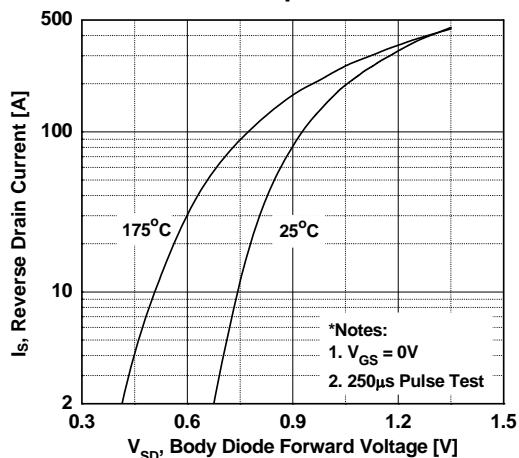


Figure 5. Capacitance Characteristics

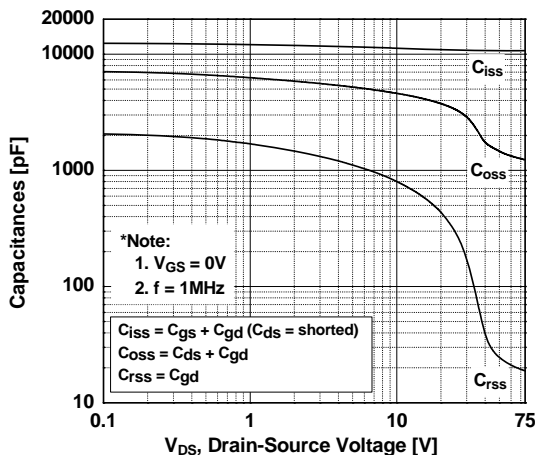
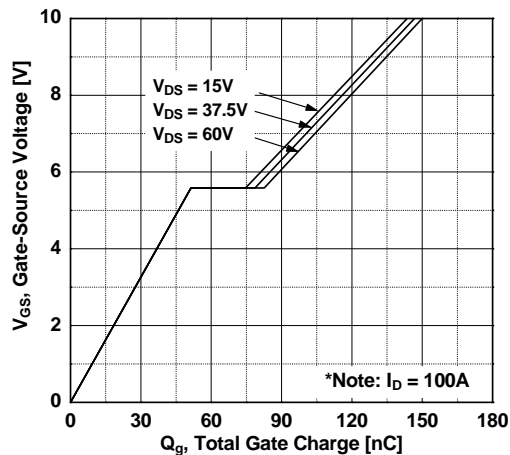


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

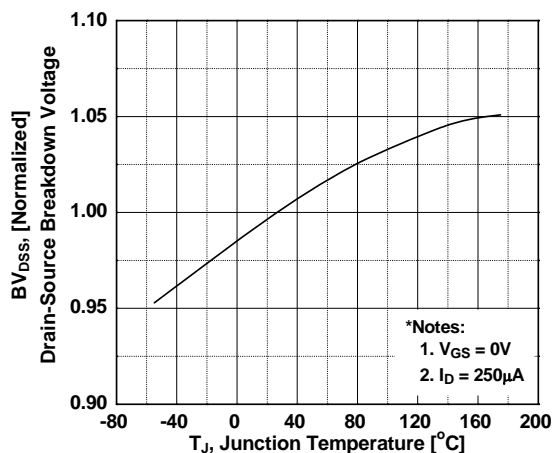


Figure 8. On-Resistance Variation vs. Temperature

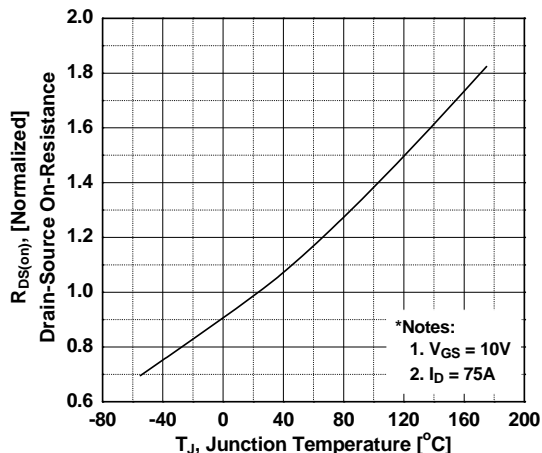


Figure 9. Maximum Safe Operating Area vs. Case Temperature

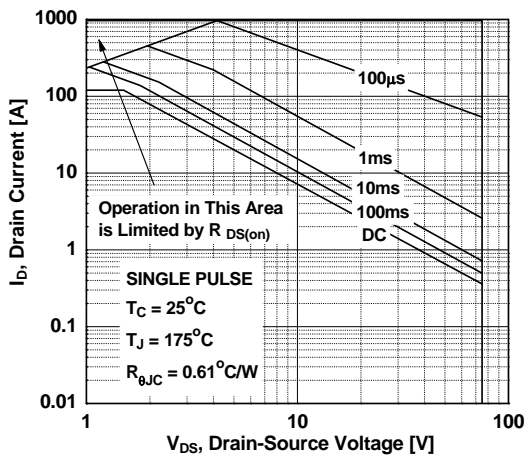


Figure 10. Maximum Drain Current vs. Case Temperature

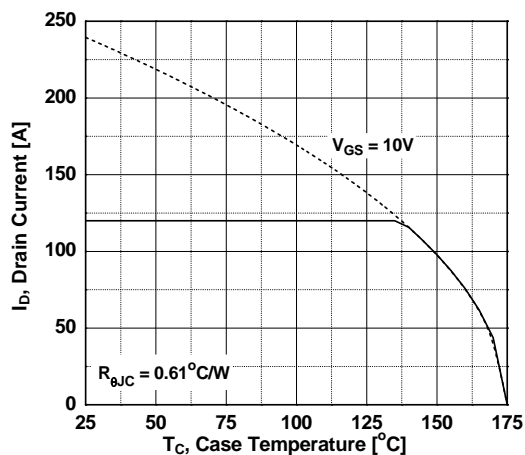


Figure 11. E_oss vs. Drain to Source Voltage

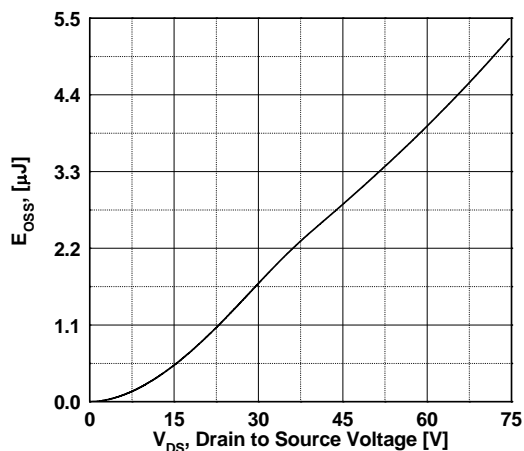
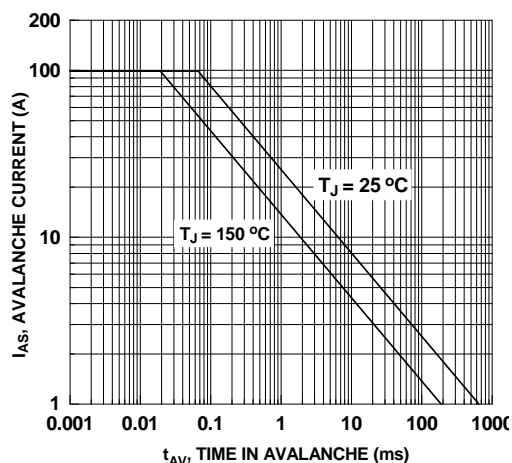
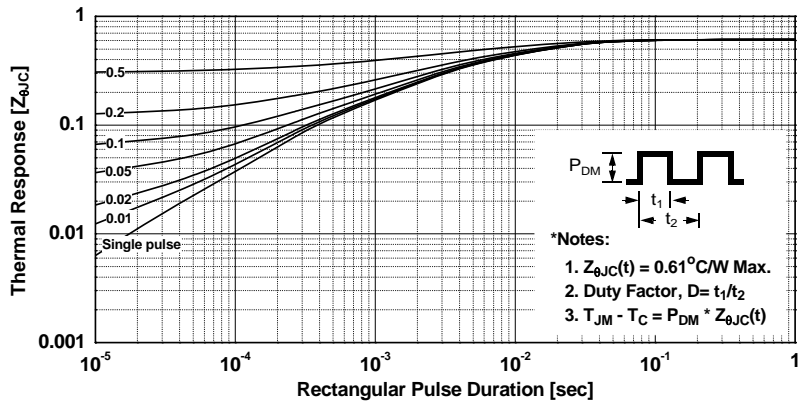


Figure 12. Unclamped Inductive Switching Capability

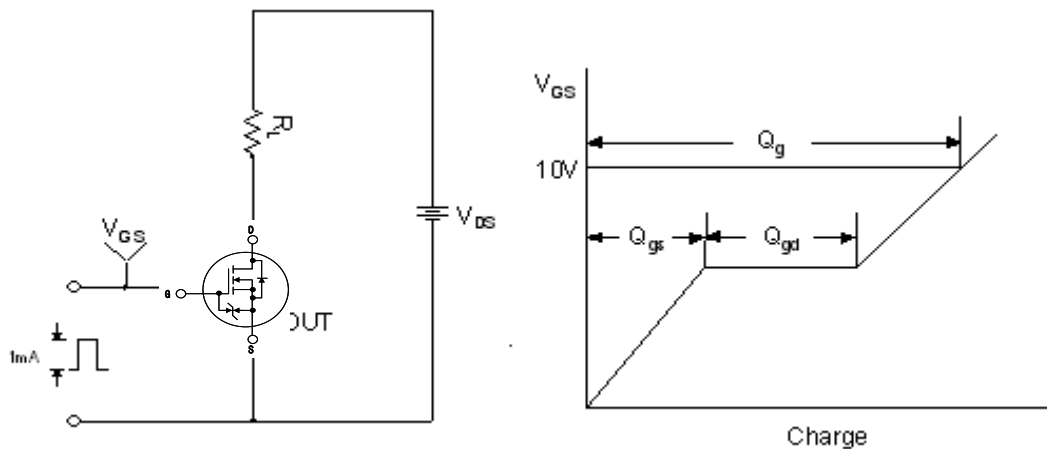


Typical Performance Characteristics (Continued)

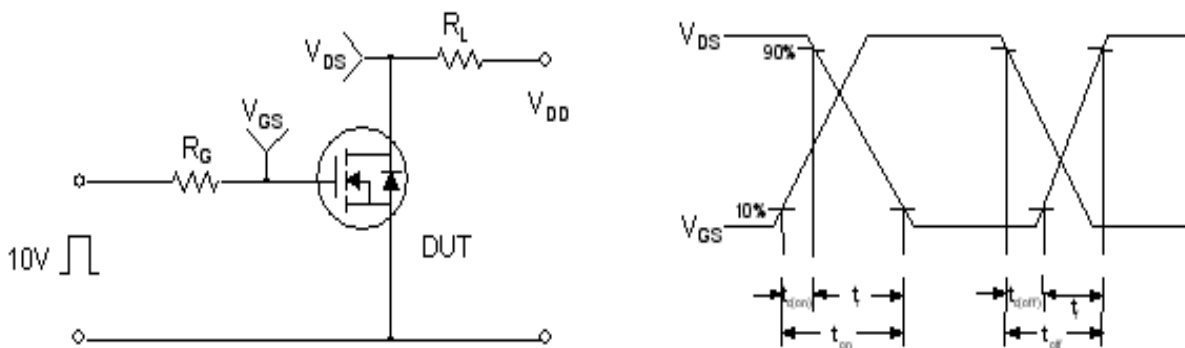
Figure 13. Transient Thermal Response Curve



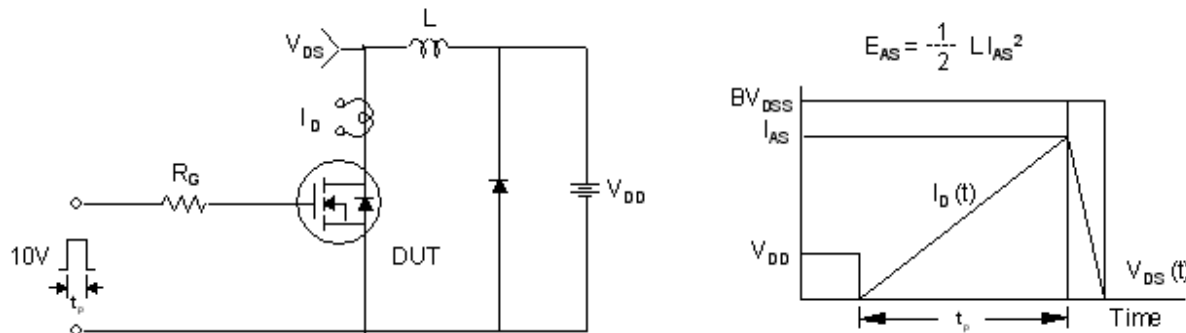
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



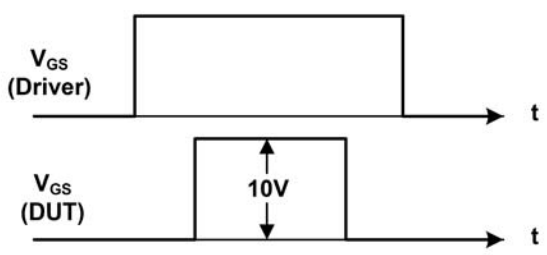
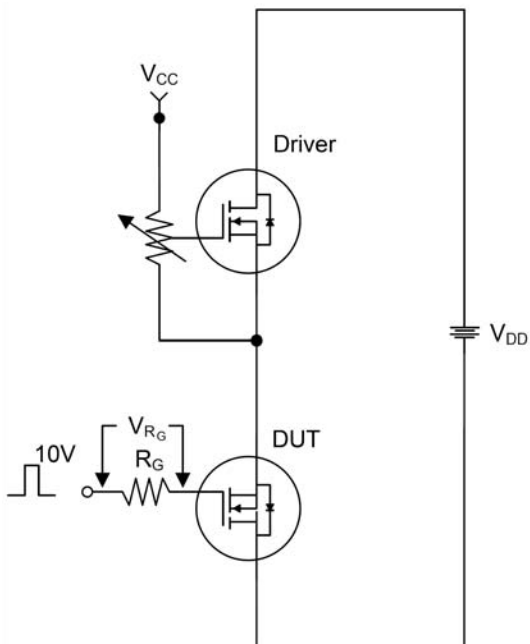
Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



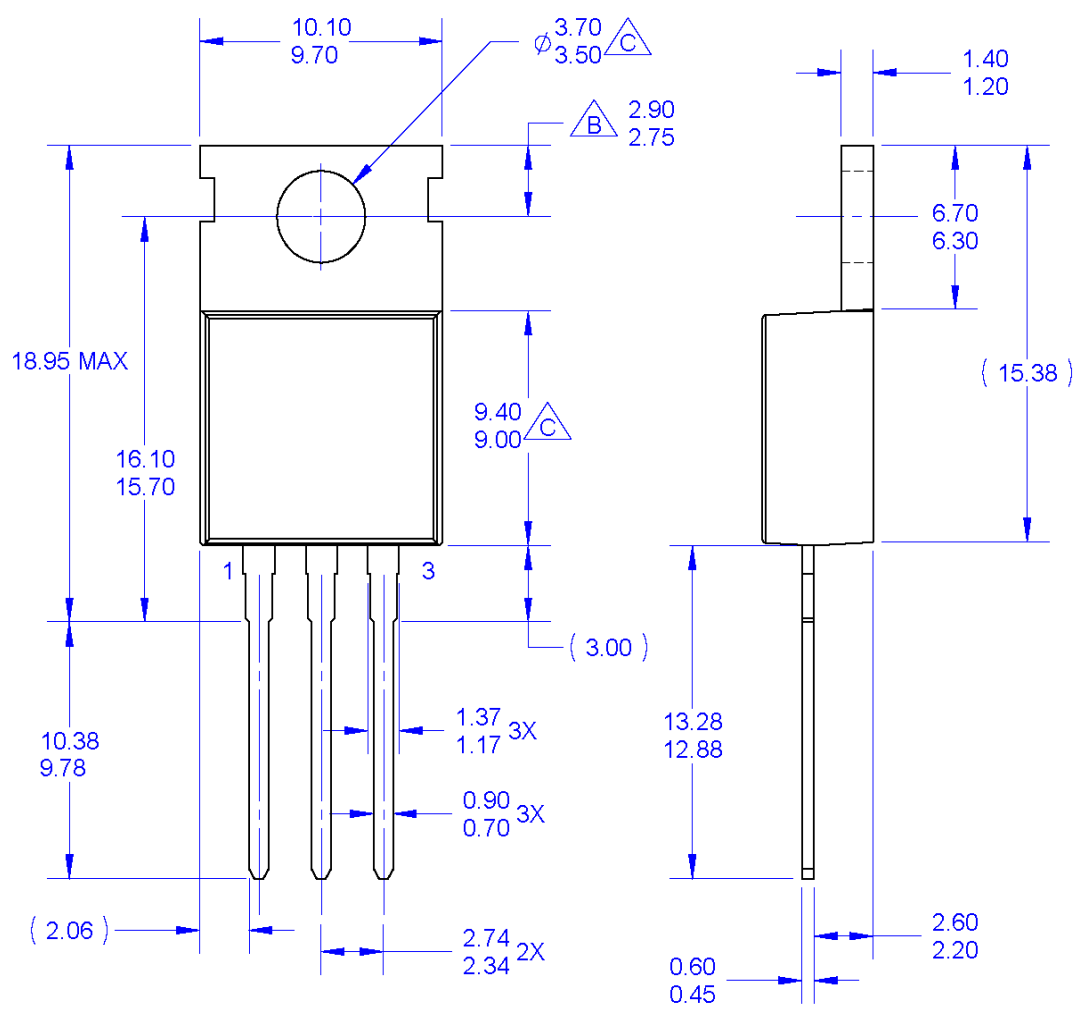
Total Gate Charge Qsync. Test Circuit & Waveforms



$$Q_{sync} = \frac{1}{R_G} \cdot \int V_{R_G}(t) dt$$

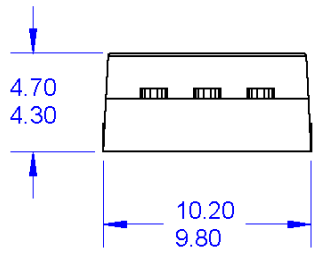
Mechanical Dimensions

TO-220A03



NOTES:






- A. PACKAGE CONFORMS TO JEDEC TO-220 VARIATION AB EXCEPT WHERE NOTED.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. OUT OF JEDEC STANDARD VALUE.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DRAWING FILE NAME: TO220A03REV1





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