



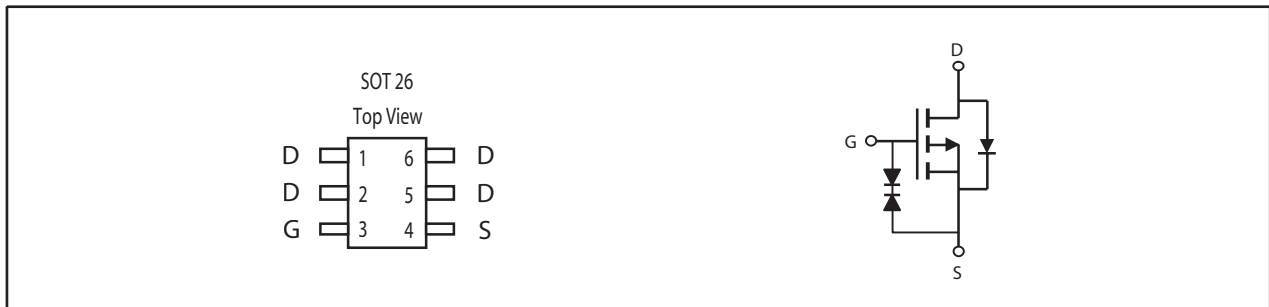
## P-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
-20V	-2.8A	100 @ V <sub>GS</sub> =-4.5V
		105 @ V <sub>GS</sub> =-4.0V
		110 @ V <sub>GS</sub> =-3.7V
		121 @ V <sub>GS</sub> =-3.1V
		138 @ V <sub>GS</sub> =-2.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Limit	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±10	V
I <sub>D</sub>	Drain Current-Continuous <sup>a</sup>	T <sub>A</sub> =25°C	-2.8
		T <sub>A</sub> =70°C	-2.2
I <sub>DM</sub>	-Pulsed <sup>b</sup>	-10	A
P <sub>D</sub>	Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	1.25
		T <sub>A</sub> =70°C	0.8
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 150	°C

### THERMAL CHARACTERISTICS

Symbol	Parameter	Limit	Units
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient <sup>a</sup>	100	°C/W

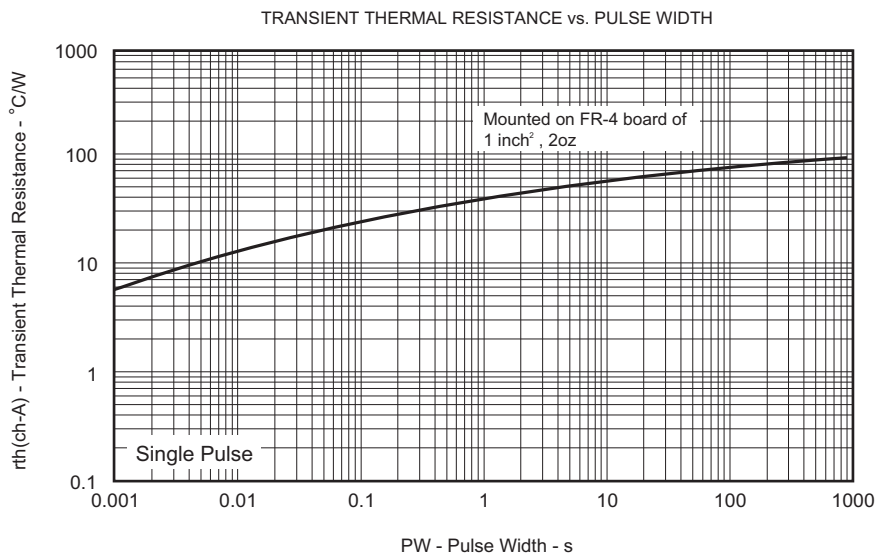
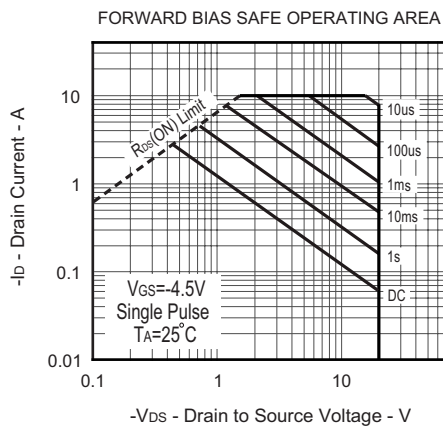
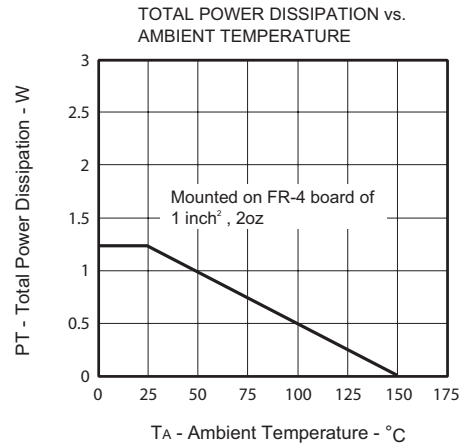
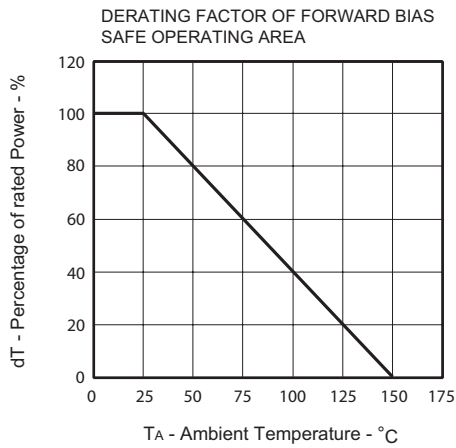
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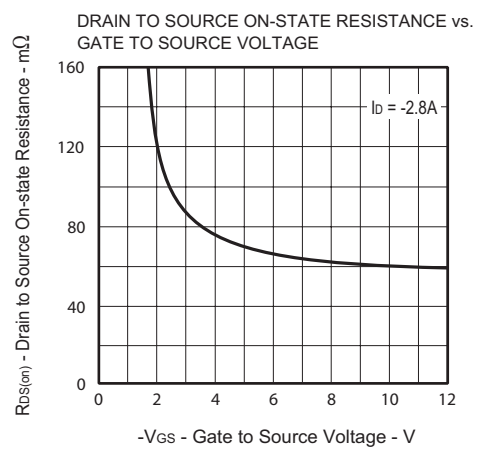
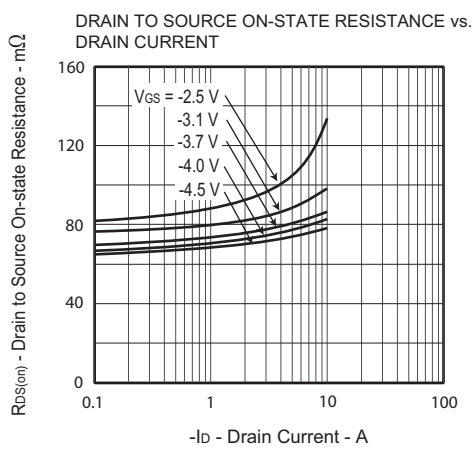
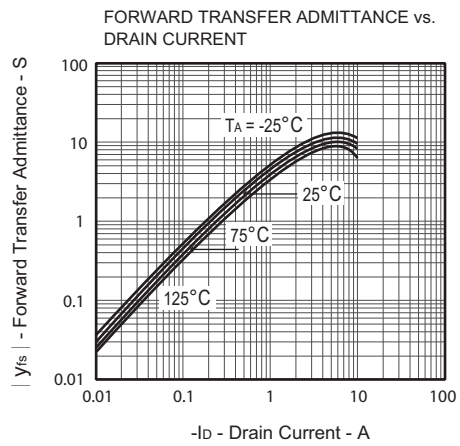
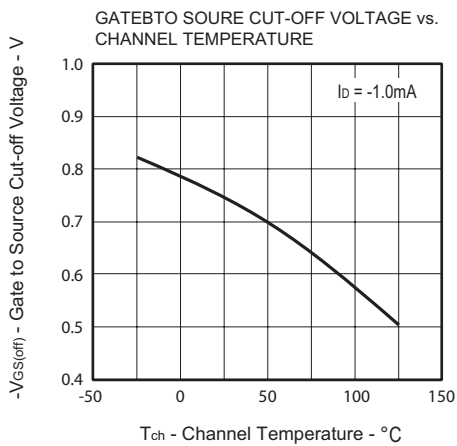
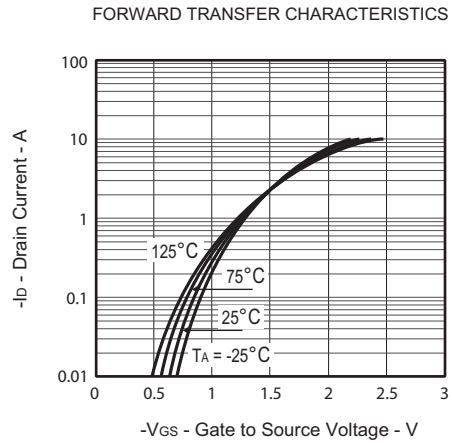
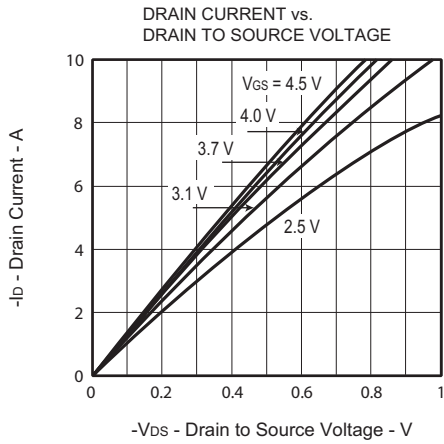
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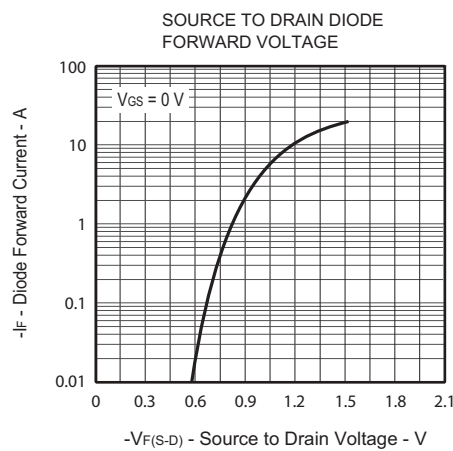
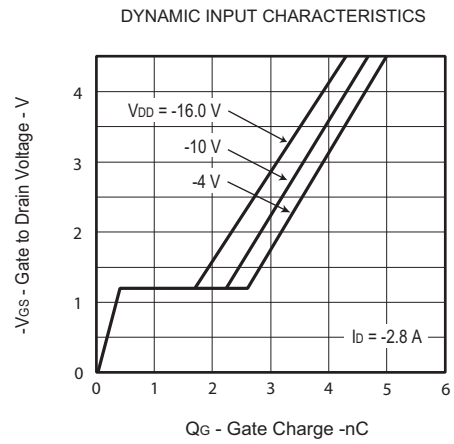
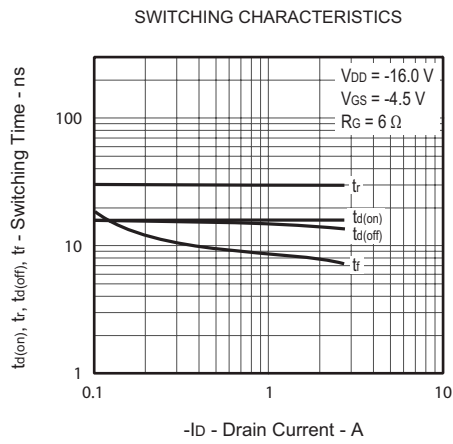
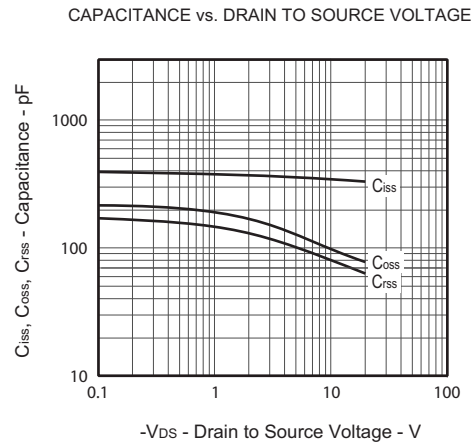
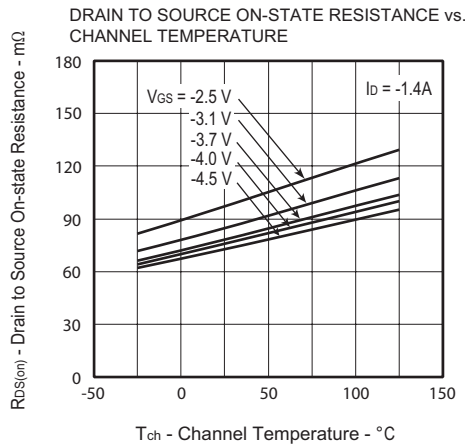
## ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±10V , V <sub>DS</sub> =0V			±10	uA
<b>ON CHARACTERISTICS</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-1mA	-0.5	-0.8	-1.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-1.4A	62	76	100	m ohm
		V <sub>GS</sub> =-4.0V , I <sub>D</sub> =-1.4A	65	80	105	m ohm
		V <sub>GS</sub> =-3.7V , I <sub>D</sub> =-1.4A	68	83	110	m ohm
		V <sub>GS</sub> =-3.1V , I <sub>D</sub> =-1.4A	74	90	121	m ohm
		V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-1.4A	82	102	138	m ohm
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-1.4A		6		S
<b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V f=1.0MHz		347		pF
C <sub>OSS</sub>	Output Capacitance			100		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			81		pF
<b>SWITCHING CHARACTERISTICS <sup>c</sup></b>						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-16V I <sub>D</sub> =-1.4A V <sub>GS</sub> =-4.5V R <sub>GEN</sub> = 6 ohm		17		ns
t <sub>r</sub>	Rise Time			30		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			15		ns
t <sub>f</sub>	Fall Time			8.3		ns
Q <sub>g</sub>	Total Gate Charge				5	
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-16V, I <sub>D</sub> =-2.8A, V <sub>GS</sub> =-4.5V		0.4		nC
Q <sub>gd</sub>	Gate-Drain Charge			2.2		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-0.5A		-0.8	-1.2	V
<b>Notes</b> a.Surface Mounted on FR4 Board, t ≤ 10sec. b.Pulse Test:Pulse Width ≤ 10us, Duty Cycle ≤ 1%. c.Guaranteed by design, not subject to production testing.						

Sep,14,2012







## PACKAGE OUTLINE DIMENSIONS

