



**ABSOLUTE MAXIMUM RATINGS**

(VCC = 5V ±5%, TA = TMIN to TMAX, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
No-Load Supply Current (Note 3)	I <sub>CC</sub>	DE = V <sub>CC</sub>		500	900	
		RE = 0V or V <sub>CC</sub>		300	500	μA
		DE = 0V				
Driver Short-Circuit Current	I <sub>OSD1</sub>	-7V V <sub>O</sub> 12V (Note 4)	35		250	mA
VO = High Driver Short-Circuit Current	I <sub>OSD2</sub>	-7V V <sub>O</sub> 12V (Note 4)	35		250	mA

VO = Low

TEST CIRCUITS

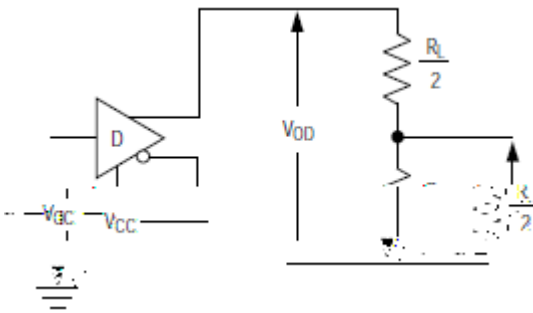


Figure 1. Driver  $V_{OD}$  and  $V_{OC}$

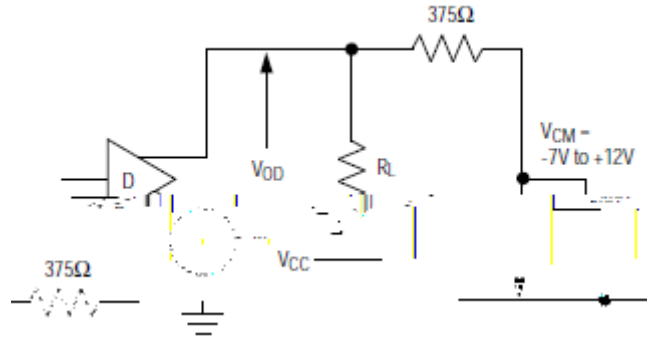


Figure 2. Driver  $V_{OD}$  with Varying Common-Mode Voltage

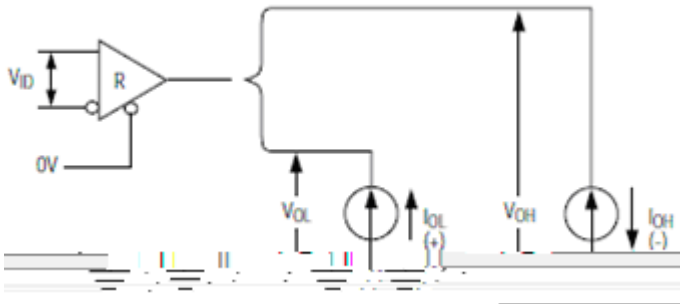


Figure 3. Receiver  $V_{OH}$  and  $V_{OL}$

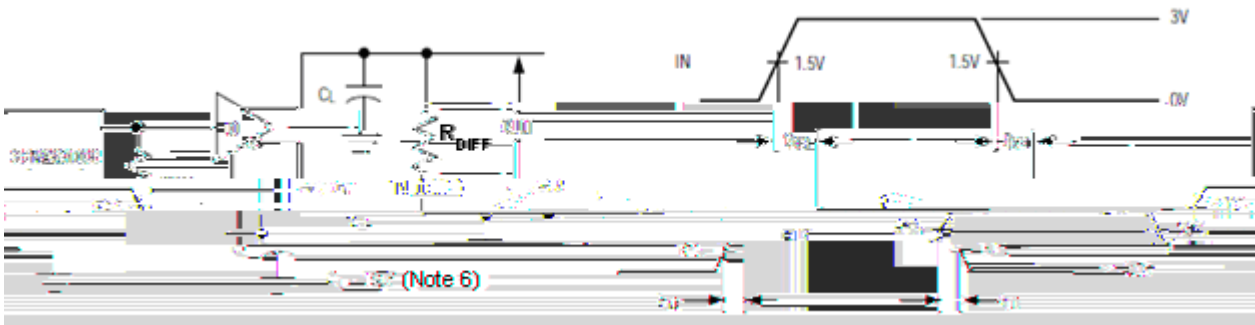


Figure 4. Driver Differential Output Delay and Transition Times

TEST CIRCUITS (continue)

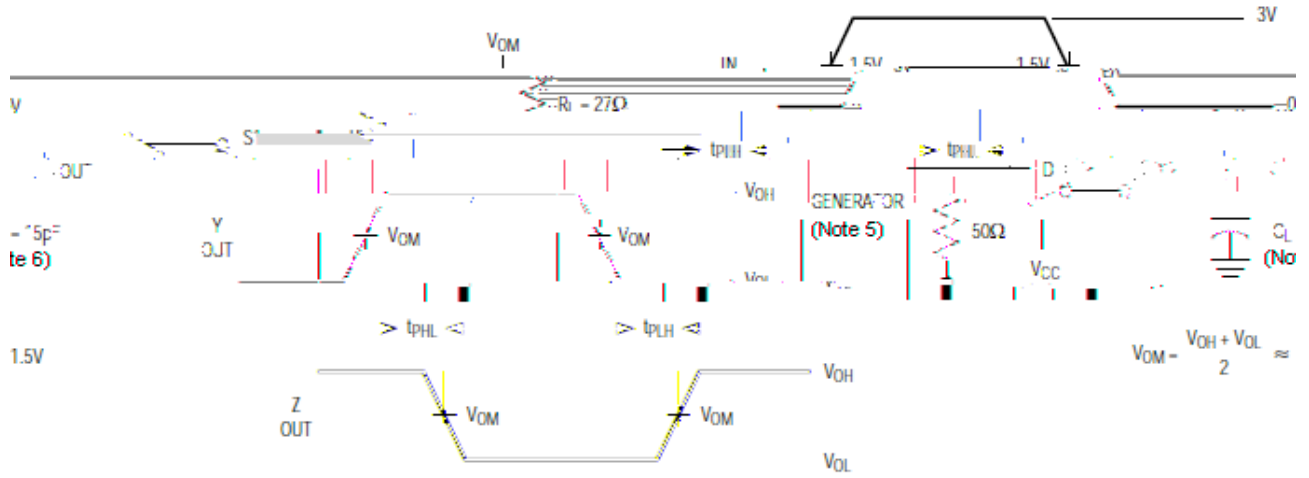


Figure 5. Driver Propagation Times



Figure 6. Driver Enable and Disable Times ( $t_{PZH}$ ,  $t_{PSH}$ ,  $t_{PHZ}$ )

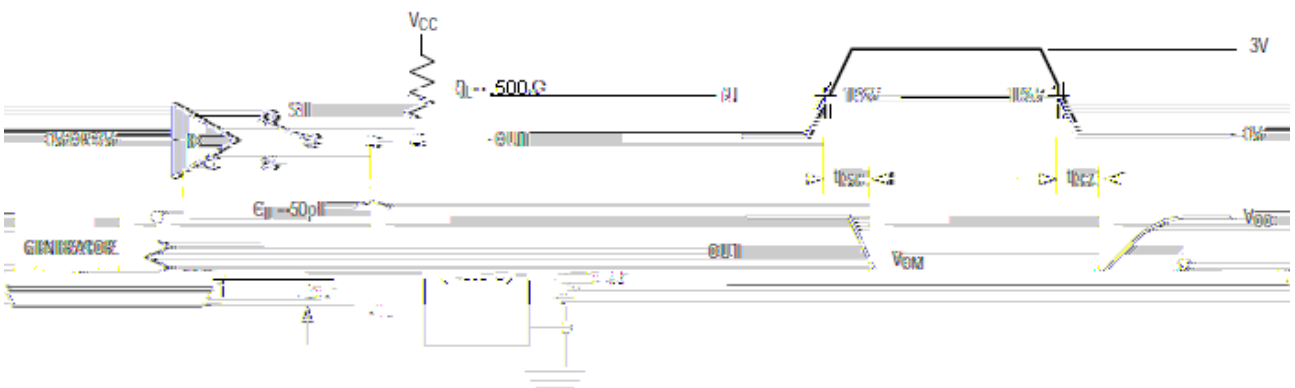


Figure 7. Driver Enable and Disable Times ( $t_{PZL}$ ,  $t_{PSL}$ ,  $t_{PLZ}$ )

TEST CIRCUITS (continue)

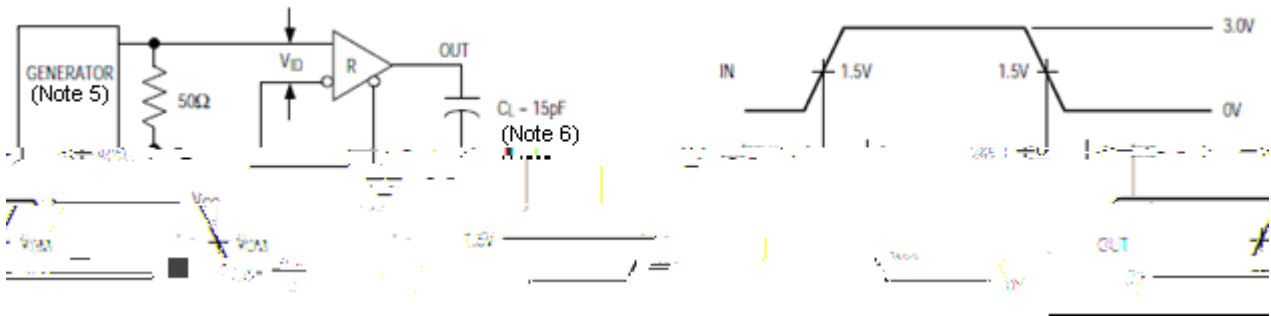


Figure 8. Receiver Propagation Delay

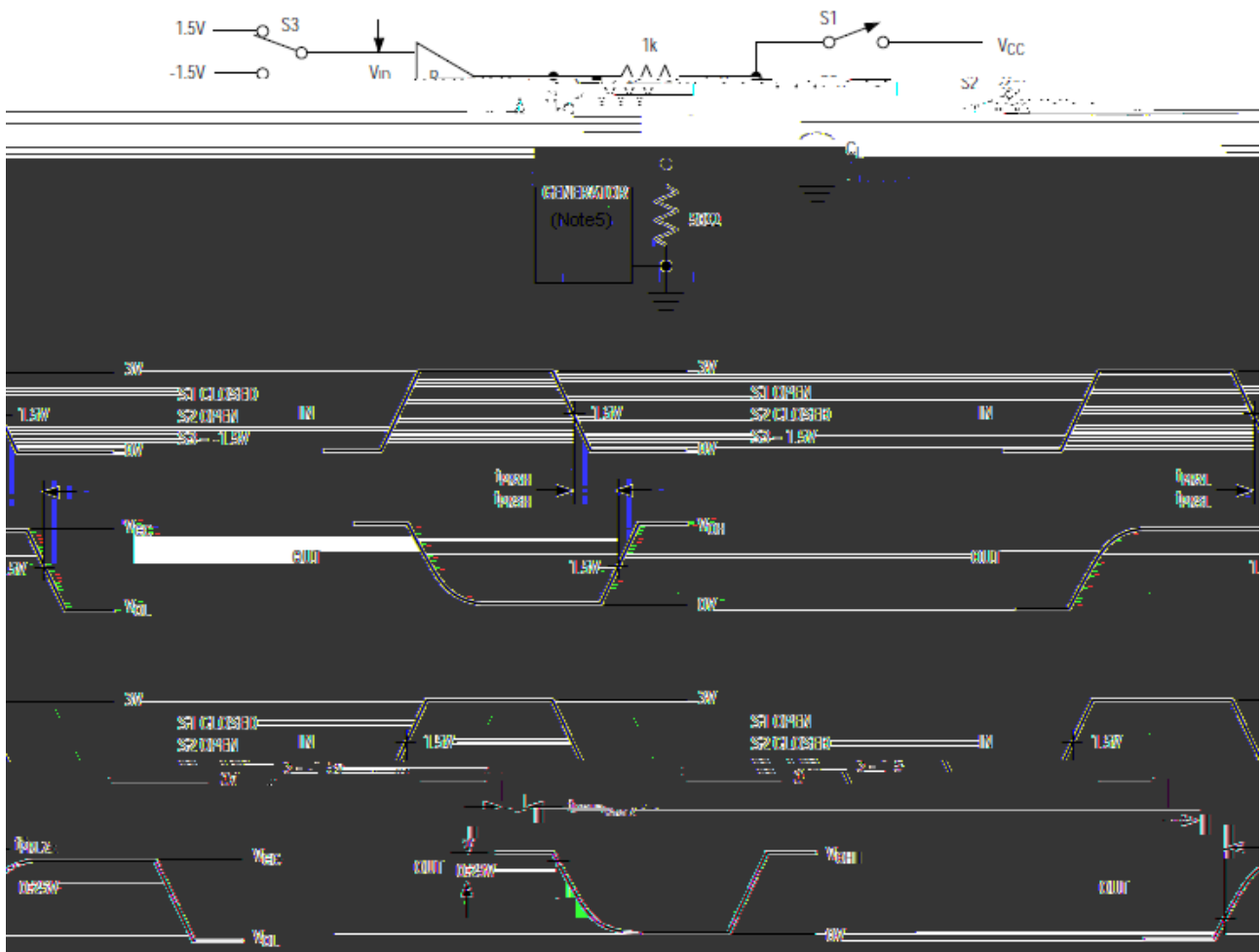


Figure 9. Receiver Enable and Disable Times

**Note 5:** The input pulse is supplied by a generator with the following characteristics: PRR = 250kHz, 50% duty cycle,  $t_r = 6.0\text{ns}$ ,  $Z_O = 50 \Omega$ .

**Note 6:**  $C_L$  includes probe and stray capacitance.



**Package Dimensions**

**N SUFFIX PLASTIC DIP**