

RoHS Compliant Product  
A suffix of "-C" specifies halogen & lead-free

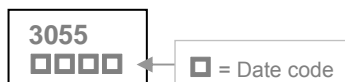
## DESCRIPTION

The GM3055 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The SOT-89 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

## FEATURES

- Fast Switching
- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Simple Drive Requirement

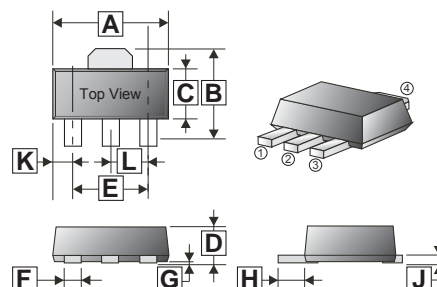
## MARKING



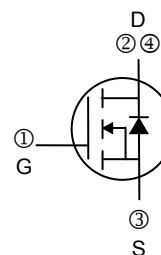
## PACKAGE INFORMATION

| Package | MPQ | Leader Size |
|---------|-----|-------------|
| SOT-89  | 1K  | 7 inch      |

## SOT-89



| REF. | Millimeter |      | REF. | Millimeter |      |
|------|------------|------|------|------------|------|
|      | Min.       | Max. |      | Min.       | Max. |
| A    | 4.40       | 4.60 | G    | -          | -    |
| B    | 4.05       | 4.25 | H    | 0.89       | 1.20 |
| C    | 2.40       | 2.60 | J    | 0.35       | 0.41 |
| D    | 1.40       | 1.60 | K    | 0.70       | 0.80 |
| E    | 3.00 REF.  |      | L    | 1.50 REF.  |      |
| F    | 0.40       | 0.52 |      |            |      |



## ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ unless otherwise specified)

| Parameter   | Symbol          | Rating                   | Unit                        |   |
|---|-----------------|--------------------------|-----------------------------|---|
| Drain-Source Voltage                                    | $V_{DS}$        | 30                       | V                           |   |
| Gate-Source Voltage                                     | $V_{GS}$        | $\pm 20$                 | V                           |   |
| Continuous Drain Current <sup>1</sup>                   | $I_D$           | $T_A = 25^\circ\text{C}$ | 5.8                         | A |
|   |                 | $T_A = 70^\circ\text{C}$ | 4.7                         | A |
| Pulsed Drain Current <sup>2</sup>                       | $I_{DM}$        | 30                       | A                           |   |
| Power Dissipation <sup>3</sup>                          | $P_D$           | 1.5                      | W                           |   |
| Linear Derating Factor                                  |                 | 0.0118                   | W / $^\circ\text{C}$        |   |
| Operating Junction & Storage Temperature                | $T_J, T_{STG}$  | -55~150                  | $^\circ\text{C}$            |   |
| <b>Thermal Resistance Rating</b>                        |                 |                          |                             |   |
| Thermal Resistance Junction-Ambient <sup>1</sup> (Max). | $R_{\theta JA}$ | 85                       | $^\circ\text{C} / \text{W}$ |   |
| Thermal Resistance Junction-Case <sup>1</sup> (Max).    | $R_{\theta JC}$ | 30                       | $^\circ\text{C} / \text{W}$ |   |

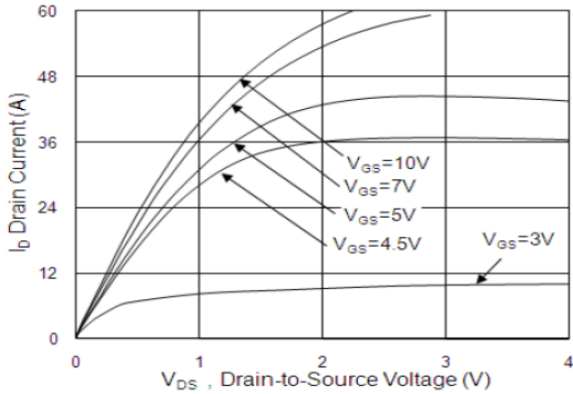
**ELECTRICAL CHARACTERISTICS** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

| Parameter  | Symbol                       | Min.                   | Typ.  | Max.      | Unit                 | Teat Conditions                                    |                               |
|--|------------------------------|------------------------|-------|-----------|----------------------|--|-------------------------------|
| Drain-Source Breakdown Voltage                       | $BV_{DSS}$                   | 30                     | -     | -         | V                    | $V_{GS}=0, I_D=250\mu\text{A}$                     |                               |
| Breakdown Voltage Temperature Coefficient            | $\Delta BV_{DSS}/\Delta T_J$ | -                      | 0.021 | -         | V / $^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$ |                               |
| Gate Threshold Voltage                               | $V_{GS(th)}$                 | 1.2                    | -     | 2.5       | V                    | $V_{DS}=V_{GS}, I_D=250\mu\text{A}$                |                               |
| Gate-Source Leakage Current                          | $I_{GSS}$                    | -                      | -     | $\pm 100$ | nA                   | $V_{GS}=\pm 20\text{V}$                            |                               |
| Drain-Source Leakage Current                         | $I_{DSS}$                    | $T_J=25^\circ\text{C}$ | -     | -         | 1                    | $\mu\text{A}$                                      | $V_{DS}=24\text{V}, V_{GS}=0$ |
|  |                              | $T_J=55^\circ\text{C}$ | -     | -         | 5                    |  | $V_{DS}=24\text{V}, V_{GS}=0$ |
| Static Drain-Source On-Resistance <sup>2</sup>       | $R_{DS(ON)}$                 | -                      | -     | 28        | m $\Omega$           | $V_{GS}=10\text{V}, I_D=5\text{A}$                 |                               |
|  |                              | -                      | -     | 40        |                      | $V_{GS}=4.5\text{V}, I_D=4\text{A}$                |                               |
| Total Gate Charge <sup>2</sup>                       | $Q_g$                        | -                      | 6     | 8.4       | nC                   | $I_D=5\text{A}$                                    |                               |
| Gate-Source Charge                                   | $Q_{gs}$                     | -                      | 2.5   | 3.5       |                      | $V_{DS}=15\text{V}$                                |                               |
| Gate-Drain ("Miller") Charge                         | $Q_{gd}$                     | -                      | 2.1   | 2.9       |                      | $V_{GS}=4.5\text{V}$                               |                               |
| Turn-on Delay Time <sup>2</sup>                      | $T_{d(on)}$                  | -                      | 2.4   | 4.8       | nS                   | $V_{DD}=15\text{V}$                                |                               |
| Rise Time  | $T_r$                        | -                      | 7.8   | 14        |                      | $I_D=5\text{A}$                                    |                               |
| Turn-off Delay Time                                  | $T_{d(off)}$                 | -                      | 22    | 44        |                      | $V_{GS}=10\text{V}$                                |                               |
| Fall Time  | $T_f$                        | -                      | 4     | 8         |                      | $R_G=3.3\Omega$<br>$R_D=1.9\Omega$                 |                               |
| Input Capacitance                                    | $C_{iss}$                    | -                      | 572   | 800       | pF                   | $V_{GS}=0$   |                               |
| Output Capacitance                                   | $C_{oss}$                    | -                      | 81    | 112       |                      | $V_{DS}=15\text{V}$                                |                               |
| Reverse Transfer Capacitance                         | $C_{rss}$                    | -                      | 65    | 91        |                      | $f=1.0\text{MHz}$                                  |                               |
| <b>Source-Drain Diode</b>                            |                              |                        |       |           |                      |  |                               |
| Forward On Voltage <sup>2</sup>                      | $V_{SD}$                     | -                      | -     | 1.2       | V                    | $I_S=3\text{A}, V_{GS}=0, T_J=25^\circ\text{C}$    |                               |
| Continuous Source Current(Body Diode) <sup>1,4</sup> | $I_S$                        | -                      | -     | 5.8       | A                    | $V_D=V_G=0, V_S=1.2\text{V}$                       |                               |
| Pulsed Source Current(Body Diode) <sup>2,4</sup>     | $I_{SM}$                     | -                      | -     | 30        | A                    |  |                               |

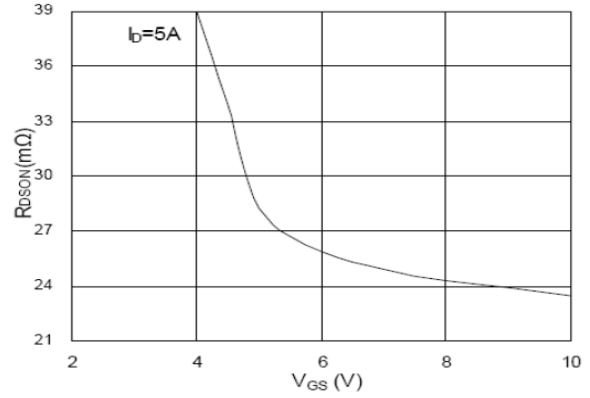
Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
3. The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
4. The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications, should be limited by total power dissipation.

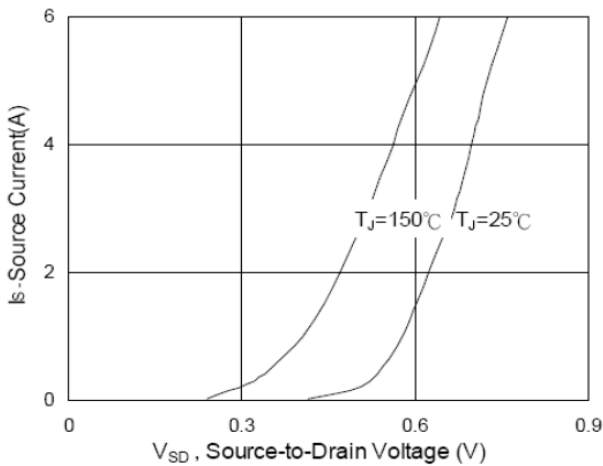
**CHARACTERISTIC CURVES**



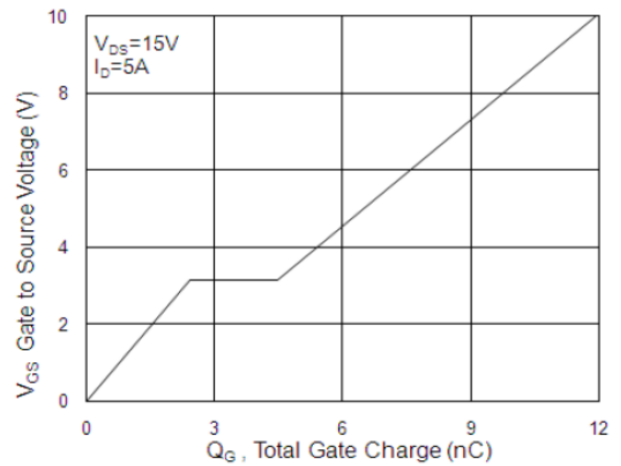
**Fig.1 Typical Output Characteristics**



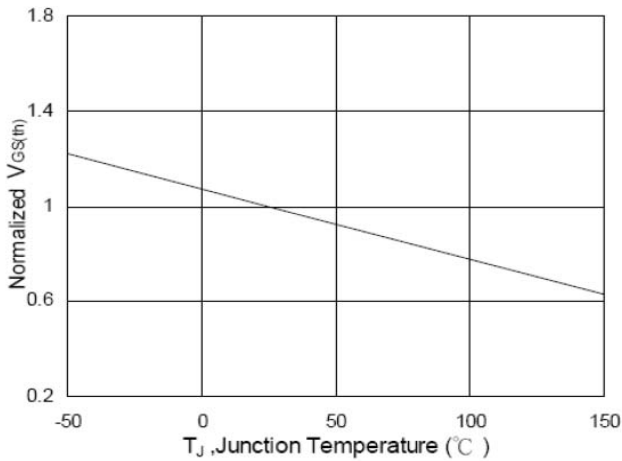
**Fig.2 On-Resistance vs. G-S Voltage**



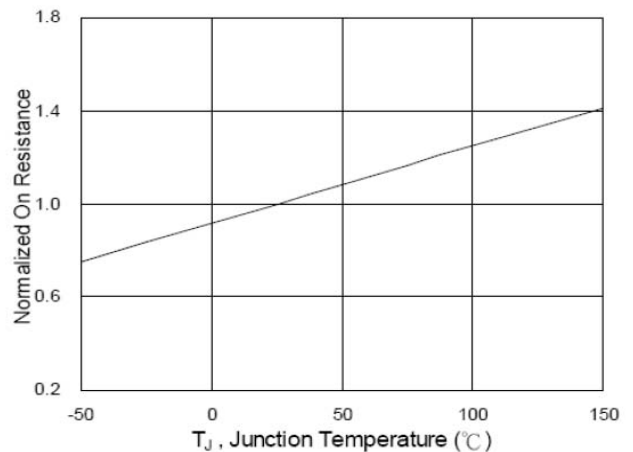
**Fig.3 Forward Characteristics Of Reverse**



**Fig.4 Gate-Charge Characteristics**

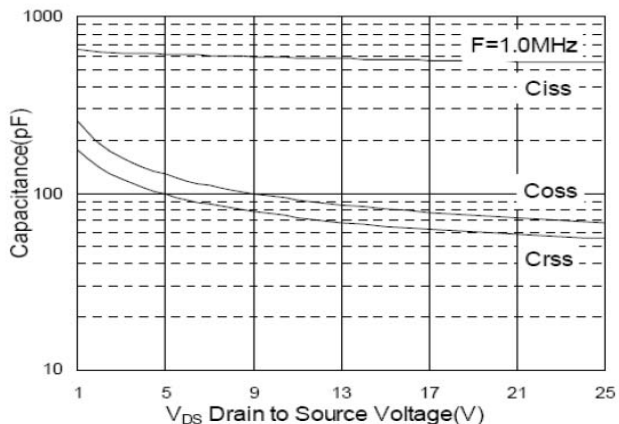


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

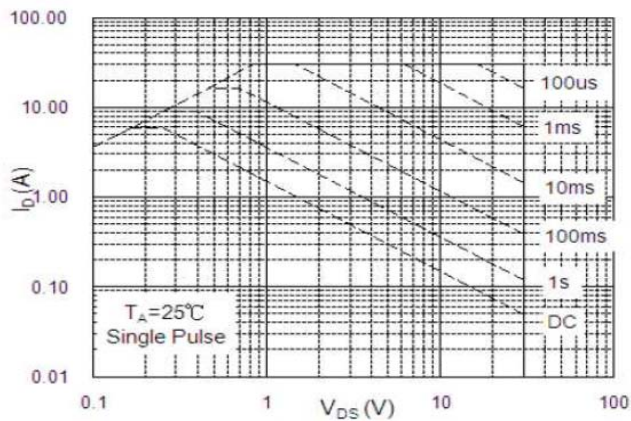


**Fig.6 Normalized  $R_{DS(ON)}$  vs.  $T_J$**

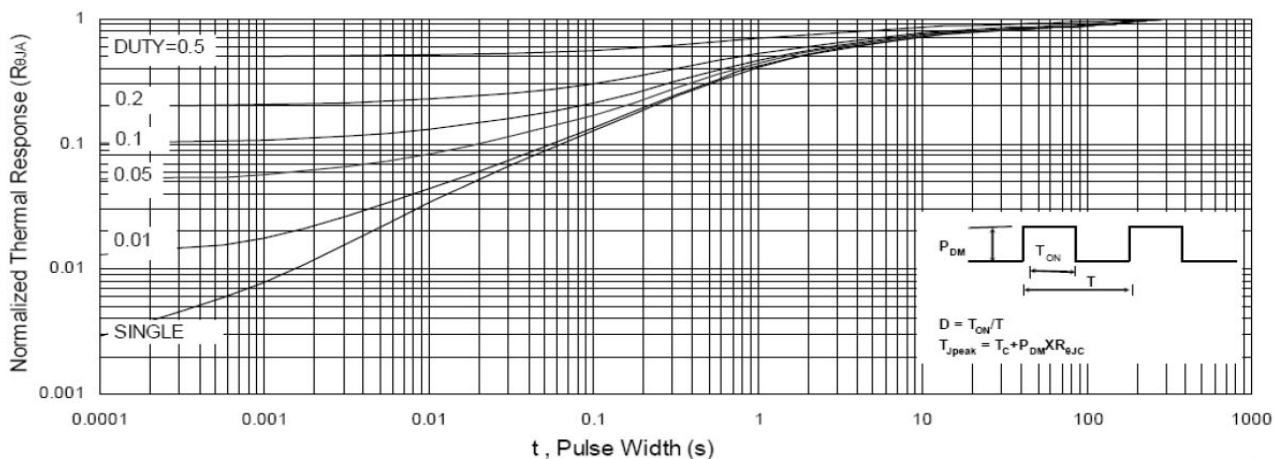
**CHARACTERISTIC CURVES**



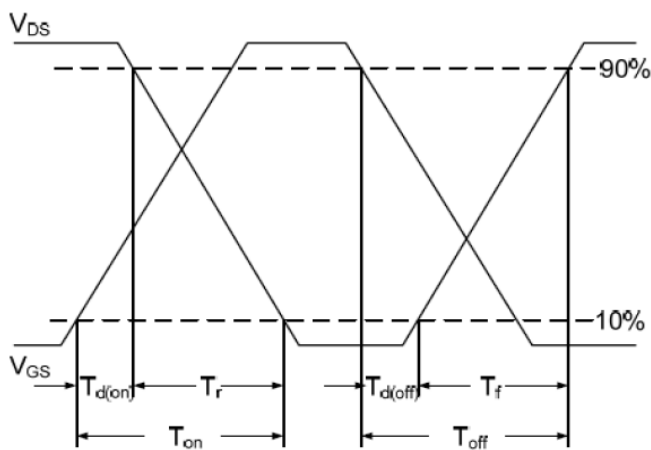
**Fig.7 Capacitance**



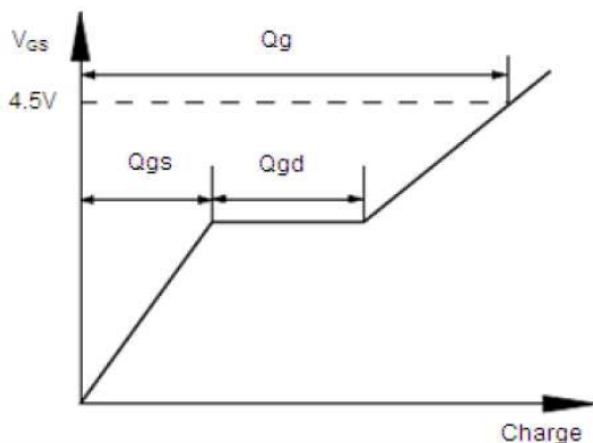
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**