CMLM3405

MULTI DISCRETE MODULE™

SURFACE MOUNT SILICON HIGH CURRENT

 $\begin{array}{c} \text{LOW V}_{\text{CE(SAT)}} \text{ NPN TRANSISTOR AND} \\ \text{LOW V}_{\text{F}} \text{ SCHOTTKY DIODE} \end{array}$





SOT-563 CASE

Central semiconductor corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLM3405 is a single NPN transistor and Schottky diode packaged in a space saving SOT-563 case and designed for small signal general purpose applications where size and operational efficiency are prime requirements.

- Complementary Device: CMLM7405
- Combination High Current Low V_{CE}(SAT) Transistor and Low V_F Schottky Diode.

MARKING CODE: C53

MAXIMUM RATINGS - CASE: (T _A =25°C) Power Dissipation Operating and Storage Junction Temperature Thermal Resistance	SYMBOL PD TJ, T _{Stg} ⊖JA	350 -65 to +150 357	UNITS mW °C °C/W
MAXIMUM RATINGS - Q1: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	Vсво	40	V
Collector-Emitter Voltage	VCEO	25	V
Emitter-Base Voltage	VEBO	6.0	V
Continuous Collector Current	ĪC	1.0	Α
Peak Collector Current	ICM	1.5	Α
MAXIMUM RATINGS - D1: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	VRRM	40	V
Continuous Forward Current	I _F	500	mA
Peak Repetitive Forward Current, tp≤1.0ms	IFRM	3.5	Α
Peak Forward Surge Current, tp=8.0ms	I _{FSM}	10	Α
ELECTRICAL CHARACTERISTICS - Q1: (TA=	25°C unless otherv	vise noted)	
SYMBOL TEST CONDITIONS	MIN	TYP N	MAX UNITS
ICBO VCB=40V			100 nA

SYMBOL	TEST CONDITIONS	` /\	MIN	TYP	MAX	UNITS
I _{CBO}	V _{CB} =40V				100	nA
I _{EBO}	V _{EB} =6.0V				100	nA
BV _{CBO}	I _C =100μA		40			V
BV _{CEO}	I _C =10mA		25			V
BVEBO	I _E =100μA		6.0			V
V _{CE(SAT)}	I_C =50mA, I_B =5.0mA			20	50	mV
VCE(SAT)	I _C =100mA, I _B =10mA			35	75	mV
V _{CE} (SAT)	I_C =200mA, I_B =20mA			75	150	mV
VCE(SAT)	I _C =500mA, I _B =50mA			130	250	mV
VCE(SAT)	I _C =800mA, I _B =80mA			200	400	mV
VCE(SAT)	I _C =1.0A, I _B =100mA			250	450	mV
V _{BE} (SAT)	I _C =800mA, I _B =80mA				1.1	V
V _{BE(ON)}	V_{CE} =1.0V, I_{C} =10mA				0.9	V

R3 (1-July 2015)

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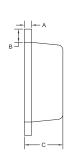
 $\begin{array}{c} \text{LOW V}_{\text{CE}(\text{SAT})} \text{ NPN TRANSISTOR AND} \\ \text{LOW V}_{\text{F}} \text{ SCHOTTKY DIODE} \end{array}$

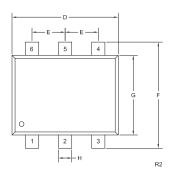


EL ECTRICAL	CHARACTERISTICS	O4 Continued
ELECTRICAL	CHARACIERISTICS	- Wi - Continued:

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
hFE	V _{CE} =1.0V, I _C =10mA	100		
hFE	V _{CE} =1.0V, I _C =100mA	100	300	
hFE	V _{CE} =1.0V, I _C =500mA	100		
hFE	V_{CE} =1.0V, I_{C} =1.0A	50		
fT	V_{CE} =10V, I_{C} =50mA, f=100MHz	100		MHz
C_{ob}	V_{CB} =10V, I_E =0, f=1.0MHz		10	pF
ELECTRICA	AL CHARACTERISTICS - D1: (T _A =25	5°C)		
I_{R}	V _R =10V		20	μΑ
I_{R}	V _R =30V		100	μΑ
BV_R	I _R =500μA	40		V
VF	I _F =100μA		0.13	V
VF	I _F =1.0mA		0.21	V
VF	I _F =10mA		0.27	V
VF	I _F =100mA		0.35	V
V_{F}	I _F =500mA		0.47	V
CJ	V _R =1.0V, f=1.0MHz		50	pF

SOT-563 CASE - MECHANICAL OUTLINE





6	5 4
Q1	D1 ¥
1	2 3

DIMENSIONS				
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
Α	0.0027	0.007	0.07	0.18
В	0.008		0.20	
С	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
H	0.006	0.012	0.15	0.30
SOT-563 (REV: R2)				

LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Cathode D1
- 4) Anode D1
- 5) Anode D1
- 6) Collector Q1

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