

N-Channel Power MOSFET

30V, 80A, 3.8mΩ

FEATURES

- 100% avalanche tested
- Fast switching
- Pb-free plating
- RoHS compliant
- Halogen-free mold compound

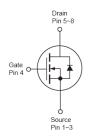
KEY PERFORMANCE PARAMETERS				
PARAMETER		VALUE	UNIT	
V_{DS}		30	V	
R _{DS(on)} (max)	V _{GS} =10V	3.8	mΩ	
	V _{GS} =4.5V	5.5		
Q_g		24	nC	

APPLICATION

- Mobile device DC-DC conversion
- Point of Load (POL) DC-DC
- Secondary Switch Rectification







Notes: Moisture sensitivity level: level 3. Per J-STD-020

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V_{GS}	±20	V	
Continuous Drain Current (Note 1)	$T_C = 25$ °C	l _D	80		
	$T_C = 100$ °C		51	Α	
Pulsed Drain Current (Note 2)		I _{DM}	320	Α	
Single Pulsed Avalanche Energy (Note 3)		E _{AS}	125	mJ	
Single Pulsed Avalanche Current (Note 3)		I _{AS}	50	А	
Total Power Dissipation @ T _C = 25°C		P _{DTOT}	66	W	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	- 55 to +150	°C	

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction to Case Thermal Resistance	R _{eJc}	2	°C/W	
Junction to Ambient Thermal Resistance	R _{OJA}	62	°C/W	

Notes: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case thermal reference is defined at the solder mounting surface of the drain pins. $R_{\theta JA}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design. $R_{\theta JA}$ shown below for single device operation on FR-4 PCB in still air





ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static (Note 4)						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250uA$	V _{GS(TH)}	1.2	1.6	2.5	V
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	nA
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Drain Source On State Registeres	$V_{GS} = 10V, I_D = 24A$			2.9	3.8	mΩ
Drain-Source On-State Resistance	V _{GS} = 4.5V, I _D = 12A	R _{DS(ON)}		4.3	5.5	
Dynamic (Note 5)						
Total Gate Charge	.,	Q_g		24		
Gate-Source Charge	$V_{DS} = 15V, I_{D} = 24A,$ $V_{GS} = 4.5V$	Q_{gs}		4.2		nC
Gate-Drain Charge	V _{GS} = 4.5 V	Q_{gd}		13		
Input Capacitance		C _{iss}		2200		
Output Capacitance	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	C _{oss}		280		pF
Reverse Transfer Capacitance	7 I = 1.0WIFIZ	C _{rss}		177		
Switching (Note 6)						
Turn-On Delay Time		t _{d(on)}		12.6		
Turn-On Rise Time	$V_{GS} = 10V, V_{DS} = 15V,$ $R_G = 3.3\Omega, I_D = 15A$	t _r		19.5		
Turn-Off Delay Time		t _{d(off)}		42.8		ns
Turn-Off Fall Time		t _f		13.2		
Source-Drain Diode (Note 4)						
Diode Forward Voltage	V _{GS} =0V, I _S =10A	V _{SD}			1	V

Notes:

- 1. Current limited by package
- 2. Pulse width limited by the maximum junction temperature
- 3. L=0.1mH, $I_{AS}=50A$, $V_{DD}=50V$, $R_G=25\Omega$, Starting $T_J=25^{\circ}C$
- 4. Pulse test: PW \leq 300 μ s, duty cycle \leq 2%
- 5. For DESIGN AID ONLY, not subject to production testing.
- 6. Switching time is essentially independent of operating temperature.



Taiwan Semiconductor

ORDERING INFORMATION

PART NO.	PACKAGE	PACKING
TSM038N03PQ33 RGG	PDFN33	5,000pcs / 13"Reel

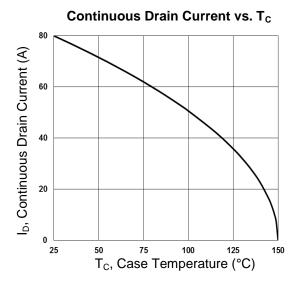
Note:

- 1. Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- 2. Halogen-free according to IEC 61249-2-21 definition

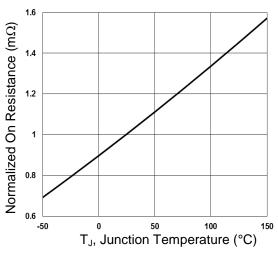


CHARACTERISTICS CURVES

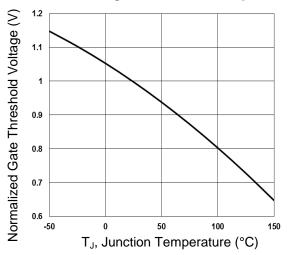
(T_C = 25°C unless otherwise noted)



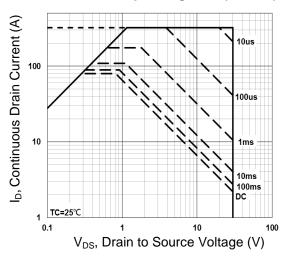
On-Resistance vs. Junction Temperature



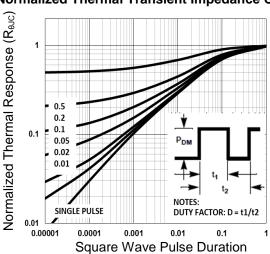
Threshold Voltage vs. Junction Temperature



Maximum Safe Operating Area (TO-220)



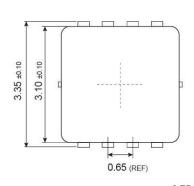
Normalized Thermal Transient Impedance Curve

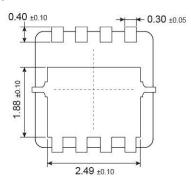


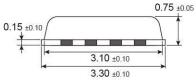


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

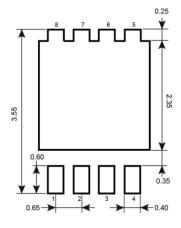
PDFN33







SUGGESTED PAD LAYOUT (Unit: Millimeters)



MARKING DIAGRAM



Y = Year Code

M = Month Code for Halogen Free Product

 \mathbf{O} =Jan \mathbf{P} =Feb \mathbf{Q} =Mar \mathbf{R} =Apr

S =May T =Jun U =Jul V =Aug

W =Sep X =Oct Y =Nov Z =Dec

 \mathbf{L} = Lot Code (1~9, A~Z)





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