

STW9NA80 STH9NA80FI

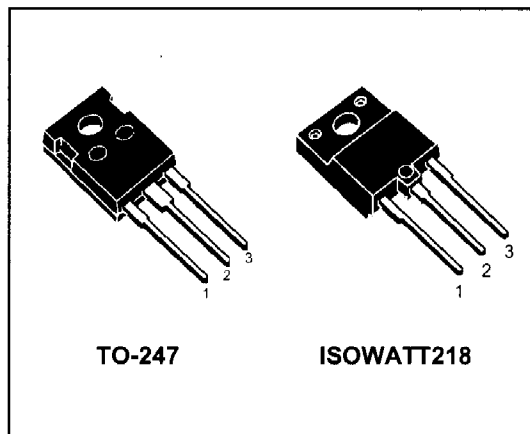
N - CHANNEL 800V - 0.85Ω - 9.1A - TO-247/ISOWATT218 FAST POWER MOS TRANSISTOR

TYPE	V _{DSS}	R _{DS(on)}	I _D
STW9NA80	800 V	< 1.0 Ω	9.1 A
STH9NA80FI	800 V	< 1.0 Ω	5.9 A

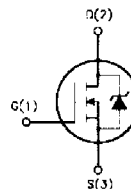
- TYPICAL R_{DS(on)} = 0.85 Ω
- ± 30V GATE TO SOURCE VOLTAGE RATING
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED
- REDUCED THRESHOLD VOLTAGE SPREAD

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE



INTERNAL SCHEMATIC DIAGRAM

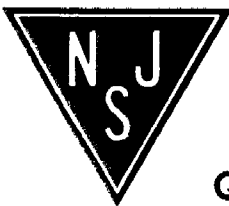


ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STW9NA80	STH9NA80FI	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	800		V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 kΩ)	800		V
V _{GS}	Gate-source Voltage	± 30		V
I _D	Drain Current (continuous) at T _c = 25 °C	9.1	5.9	A
I _D	Drain Current (continuous) at T _c = 100 °C	6	3.9	A
I _{DM} (*)	Drain Current (pulsed)	36.4	36.4	A
P _{tot}	Total Dissipation at T _c = 25 °C	190	80	W
	Derating Factor	1.52	0.64	W/°C
V _{ISO}	Insulation Withstand Voltage (DC)	—	4000	V
T _{stg}	Storage Temperature	-65 to 150		°C
T _j	Max. Operating Junction Temperature	150		°C

(*) Pulse width limited by safe operating area

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STW9NA80-STH9NA80FI

THERMAL DATA

		TO-247	ISOWATT218		
R _{thj-case}	Thermal Resistance Junction-case	Max	0.65	1.56	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient	Max	30		°C/W
R _{thc-sink}	Thermal Resistance Case-sink	Typ	0.1		°C/W
T _j	Maximum Lead Temperature For Soldering Purpose		300		°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max)	9.1	A
E _{AS}	Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V)	415	mJ

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 250 μA V _{GS} = 0	800			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating T _c = 100 °C			50 500	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 30 V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250 μA	2.25	3	3.75	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V I _D = 4.5 A		0.85	1	Ω
I _{D(on)}	On State Drain Current	V _{DS} > I _{D(on)} × R _{DS(on)max} V _{GS} = 10 V	9.1			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (*)	Forward Transconductance	V _{DS} > I _{D(on)} × R _{DS(on)max} I _D = 4.5A	7.5	10		S
C _{iSS}	Input Capacitance	V _{DS} = 25 V f = 1 MHz V _{GS} = 0		2900	3800	pF
C _{oSS}	Output Capacitance			290	380	pF
C _{rSS}	Reverse Transfer Capacitance			80	105	pF

ELECTRICAL CHARACTERISTICS (continued)
SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r	Turn-on Time Rise Time	$V_{DD} = 400\text{ V}$ $I_D = 4.5\text{ A}$ $R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 3)		37 45	50 60	ns ns
Q_g Q_{gs} Q_{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 640\text{ V}$ $I_D = 9\text{ A}$ $V_{GS} = 10\text{ V}$		115 15 55	150	nC nC nC

SWITCHING OFF

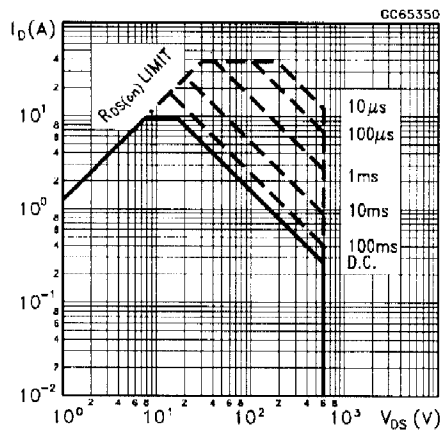
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(voff)}$ t_f t_c	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 640\text{ V}$ $I_D = 9\text{ A}$ $R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		45 15 70	60 20 91	ns ns ns

SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD} $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				9.1 36.4	A A
$V_{SD}(\ast)$	Forward On Voltage	$I_{SD} = 9.1\text{ A}$ $V_{GS} = 0$			1.6	V
t_{rr} Q_{rr} I_{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 9\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 100\text{ V}$ $T_j = 150\text{ }^\circ\text{C}$ (see test circuit, figure 5)		765 113.4 35		ns μC A

(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %
(•) Pulse width limited by safe operating area

Safe Operating Area for TO-247



Safe Operating Area for ISOWATT218

