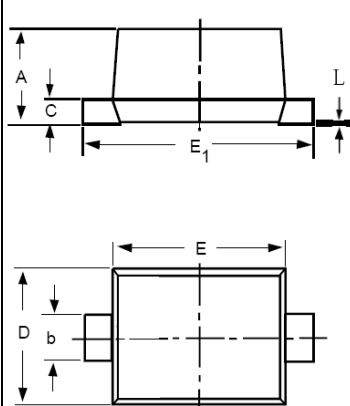


SURFACE MOUNT SCHOTTKY BARRIER DIODE	REVERSE VOLTAGE – 30 Volts FORWARD CURRENT – 0.1 Ampere																											
<p>FEATURES</p> <ul style="list-style-type: none"> • Low VF drop • Low Reverse Current <p>MECHANICAL DATA</p> <ul style="list-style-type: none"> • Case: SOD-723 Plastic • Case Material: “Green” molding compound, UL flammability classification 94V-0, (No Br. Sb. Cl) • Moisture Sensitivity: Level 1 per J-STD-020D • Lead Free in RoHS 2002/95/EC Compliant 	<p style="text-align: center;">SOD-723</p> <div style="display: flex; align-items: center;">  <table border="1" style="margin-left: 20px; border-collapse: collapse;"> <thead> <tr> <th colspan="3">SOD-723</th> </tr> <tr> <th>Dim.</th> <th>Min.</th> <th>Max.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>0.525</td> <td>0.65</td> </tr> <tr> <td>b</td> <td>0.25</td> <td>0.35</td> </tr> <tr> <td>C</td> <td>0.08</td> <td>0.15</td> </tr> <tr> <td>D</td> <td>0.55</td> <td>0.65</td> </tr> <tr> <td>E</td> <td>0.90</td> <td>1.10</td> </tr> <tr> <td>E1</td> <td>1.30</td> <td>1.50</td> </tr> <tr> <td>L</td> <td>0.01</td> <td>0.07</td> </tr> </tbody> </table> </div> <p style="text-align: center; font-size: small;">Dimensions in millimeter</p>	SOD-723			Dim.	Min.	Max.	A	0.525	0.65	b	0.25	0.35	C	0.08	0.15	D	0.55	0.65	E	0.90	1.10	E1	1.30	1.50	L	0.01	0.07
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Maximum Ratings & Thermal Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	RB521G-30	Units
DC reverse voltage	V _R	30	
Average Rectified Forward Current	I _O	100	mA
Peak Forward Surge Current @ tp=8.3ms	I _{FSM}	1000	mA
Operating Temperature Range	T _J	125	°C
Storage Temperature Range	T _{STG}	-40~+125	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Test Condition	Symbol	RB521G-30	Unit
Maximum Forward Voltage	I _F = 10mA	V _F	350	mV
Maximum DC Reverse Current at Rated DC Blocking Voltage	V _R = 10V	I _R	0.5	uA

REV.1, Oct-2010, KSHR43

RATING AND CHARACTERISTIC CURVES
RB521G-30



FIG.1- TYPICAL FORWARD CHARACTERISTICS

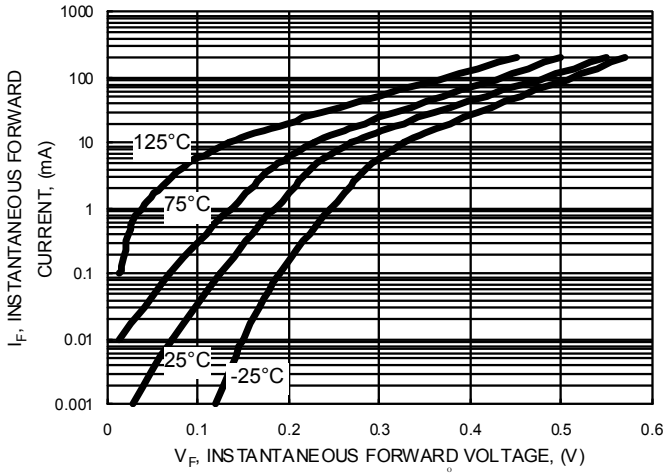


FIG.2- TYPICAL REVERSE CHARACTERISTICS

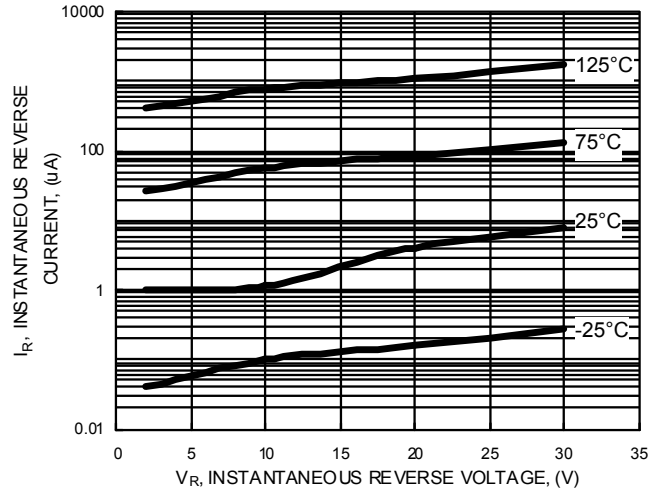
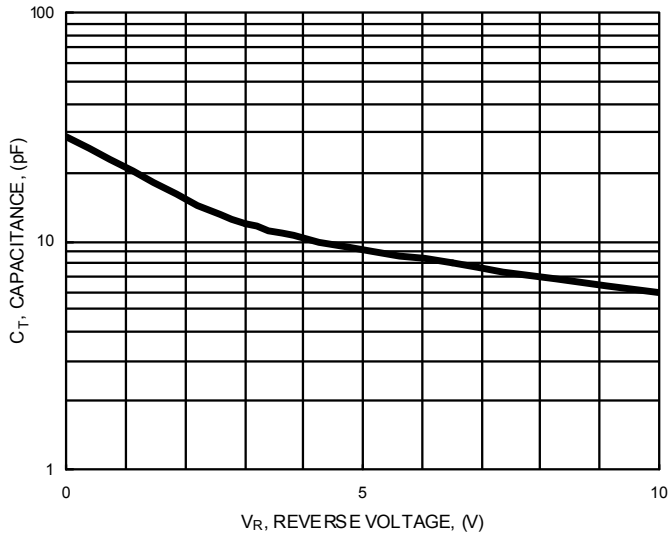


FIG.3- TYPICAL JUNCTION CAPACITANCE



Device Marking :

Device P/N	Marking	Equivalent Circuit Diagram
RB521G-30	F	

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