

SUD40N06-25L N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	
60	0.022 @ V _{GS} = 10 V	30	
	0.025 @ V _{GS} = 4.5 V	30	



Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	±20	V
	$T_{C} = 25^{\circ}C$	۱ _D	30	
Continuous Drain Current $(T_J = 175^{\circ}C)^b$	T _C = 100°C		30	
Pulsed Drain Current		I _{DM}	100	А
Continuous Source Current (Diode Conduction)		۱ _S	34	1
Avalanche Current		I _{AR}	34	
Repetitive Avalanche Energy (Duty Cycle \leq 1%)	L = 0.1 mH	E _{AR}	58	mJ
Maximum Power Dissipation	$T_{C} = 25^{\circ}C$	PD	75	w
	$T_A = 25^{\circ}C$		1.4 ^b , 2.5 ^c	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Maximum Junction-to-Ambient	Free Air, FR4 Board Mount	R _{thJA}	60	°C/W	
	Free Air, Vertical Mount		110		
Maximum Junction-to-Case		R _{thJC}	2.0		

Notes:

a. Package limited.

b. Free air, vertical mount.

c. Surface mounted on 1" x 1" FR4 Board, $t \le 10$ sec.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm



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Parameter	Symbol	Test Condition	Min	Тура	Max	Unit	
Static				1		<u> </u>	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \ \mu\text{A}$	60			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0	2.0	3.0	- ·	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ±20 V			±100	nA	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 60 V, V_{GS} = 0 V, T_{J} = 125 °C			50	μΑ	
		V_{DS} = 60 V, V_{GS} = 0 V, T_{J} = 175 °C			150	1	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	20			А	
		$V_{GS} = 10$ V, $I_D = 20$ A			0.022		
		V_{GS} = 10 V, I_D = 20 A, T_J = 125°C			0.043	Ω	
Drain-Source On-State Resistance ^b	^r DS(on)	V_{GS} = 10 V, I _D = 20 A, T _J = 175°C			0.053		
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$			0.025	1	
Forward Transconductanceb		V _{DS} = 15 V, I _D = 20 A				S	
Dynamic	_				1		
Input Capacitance	C _{iss}			1800		pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		350			
Reverse Transfer Capacitance	C _{rss}			100			
Total Gate Charge ^c	Qg			40	60	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 30 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 40 \text{ A}$		9			
Gate-Drain Charge ^c	Q _{gd}			10			
Turn-On Delay Time ^c	t _{d(on)}			10	20	ns	
Rise Time ^c	tr	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 0.9 \Omega$		9	20		
Turn-Off Delay Time ^c	t _{d(off)}	$\begin{array}{l} V_{DD}=30\;V,R_{L}=0.9\;\Omega\\ I_{D}\cong20\;A,V_{GEN}=10\;V,R_{G}=2.5\;\Omega \end{array}$		28	50		
Fall Time ^c	t _f			7	15		
Source-Drain Diode Ratings a	nd Characteristi	cs (T _C = 25°C)	-	•			
Pulsed Current	I _{SM}				20	A	
Diode Forward Voltage	V _{SD}	$I_{F} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	V	
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs		48	100	ns	

Notes: a. For design aid only; not subject to production testing. b. Pulse test; pulse width $\leq 300 \ \mu$ s, duty cycle $\leq 2\%$. c. Independent of operating temperature.



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-55°C

4

5

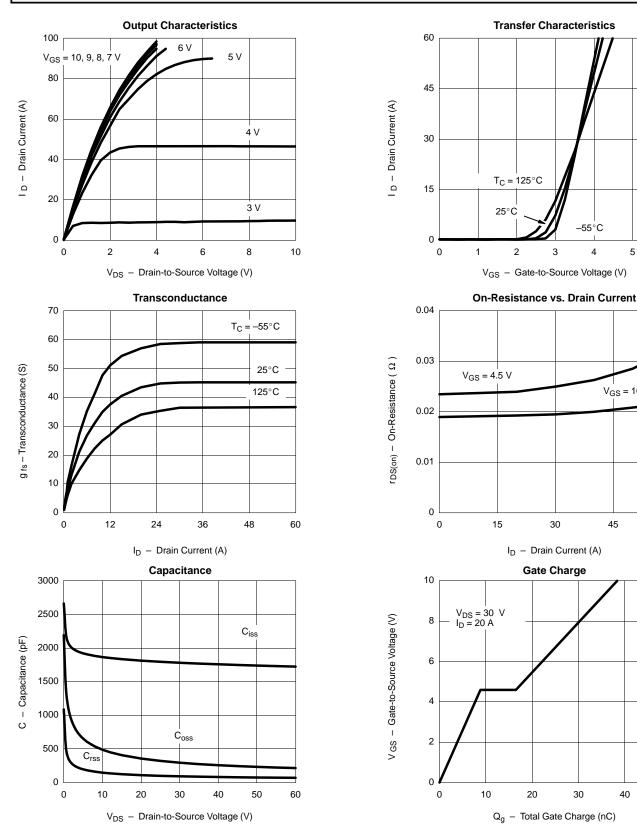
V_{GS} = 10 V

45

6

60

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



30

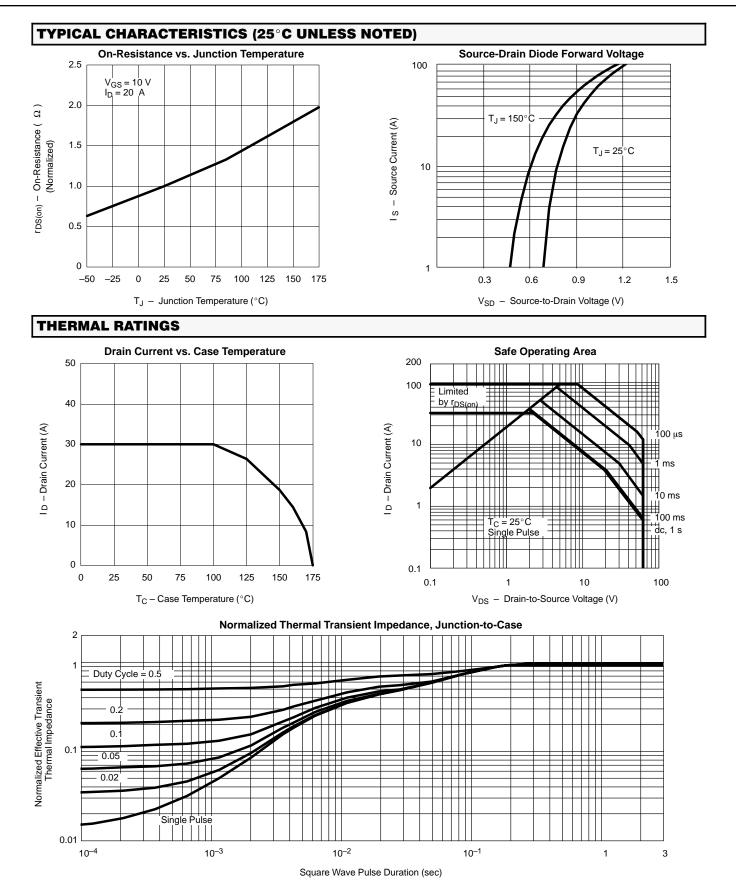
40

50



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