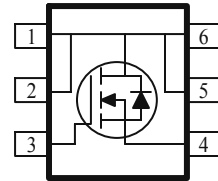
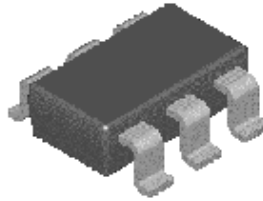


N-Channel 30V (D-S) MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are power switch, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low $r_{DS(on)}$ Provides Higher Efficiency and Extends Battery Life
- Low Gate Charge
- Fast Switch
- Miniature TSOP-6 Surface Mount Package Saves Board Space

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
30	0.044 @ $V_{GS} = 10\text{ V}$	5.1
	0.064 @ $V_{GS} = 4.5\text{ V}$	4.5



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	5.5	A
	$T_A = 70^\circ\text{C}$	4.4	
Pulsed Drain Current ^b	I_{DM}	± 20	
Continuous Source Current (Diode Conduction) ^a	I_S	1.3	A
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	2.0	W
	$T_A = 70^\circ\text{C}$	1.3	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 5\text{ sec}$	85	$^\circ\text{C/W}$
	Steady-State	62.5	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

SPECIFICATIONS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Switch Off Characteristics						
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$			1	uA
		$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}, T_J = 55^\circ\text{C}$			10	
Switch On Characteristics						
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ uA}$	1.0			V
Drain-Source On-Resistance ^A	$r_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 5.1\text{ A}$			44	m Ω
		$V_{GS} = 10\text{ V}, I_D = 5.1\text{ A}, T_J = 55^\circ\text{C}$			49	
		$V_{GS} = 4.5\text{ V}, I_D = 4.5\text{ A}$			64	
Forward Transconductance ^A	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 5.1\text{ A}$		45		S
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	20			A
Diode Forward Voltage	V_{SD}	$I_S = 1.3\text{ A}, V_{GS} = 0\text{ V}$		0.75		V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 15\text{ V}, V_{GS} = 5\text{ V}, I_D = 5.1\text{ A}$ $R_L = 6\text{ }\Omega$		4.0		nC
Gate-Source Charge	Q_{gs}			1.1		
Gate-Drain Charge	Q_{gd}			1.4		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 15\text{ V}, R_L = 6\text{ }\Omega, I_D = 1\text{ A},$ $V_{GEN} = 10\text{ V}$		6		ns
Rise Time	t_r			10		
Turn-Off Delay Time	$t_{d(off)}$			18		
Fall-Time	t_f			5		

Notes

- Pulse test: $PW \leq 300\mu\text{s}$ duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.

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