

**Fast Logic**

# Digital Delay Units

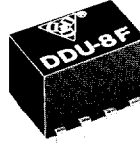
**SERIES: DDU-8F**

**TTL Interfaced, 5 Outputs  
8 pin DIP**



**Features:**

- Auto-insertable.
- Low Cost.
- Completely interfaced for TTL.
- Low profile.
- Fits standard 8 pins DIP socket.



**Specifications:**

- Outputs: 5, equally spaced.
- Delay tolerance: see table.
- Rise-time: 2 ns typ.
- Minimum pulse width: 40% of total delay.
- Temperature range: 0° C to + 70° C.
- Temperature coefficient: 100 PPM/°C.
- Supply voltage: 4.75 to 5.25 Vdc.
- Supply current:
  - I<sub>CCL</sub>: 32 ma.
  - I<sub>CCH</sub>: 7 ma.
- DC Parameters: See TTL-Fast Schottky Logic Table on Page 6.

| Part No.     | Total Delay (ns) | Tap Delay (ns) |
|--------------|------------------|----------------|
| *DDU-8F-5004 | 4 ± 1            | 1 ± 0.5        |
| *DDU-8F-5006 | 6 ± 1            | 1.5 ± 0.5      |
| *DDU-8F-5008 | 8 ± 2            | 2 ± 1          |
| *DDU-8F-5010 | 10 ± 2           | 2.5 ± 1        |
| *DDU-8F-5012 | 12 ± 2           | 3 ± 1          |
| *DDU-8F-5016 | 16 ± 2           | 4 ± 1.5        |
| *DDU-8F-5020 | 20 ± 3           | 5 ± 2          |
| DDU-8F-5025  | 25 ± 3           | 5 ± 2          |
| DDU-8F-5030  | 30 ± 3           | 6 ± 2          |
| DDU-8F-5035  | 35 ± 3           | 7 ± 2          |
| DDU-8F-5040  | 40 ± 3           | 8 ± 2          |
| DDU-8F-5045  | 45 ± 3           | 9 ± 3          |
| DDU-8F-5050  | 50 ± 3           | 10 ± 3         |

\*Time delay measured with respect to 1st tap.  
3.5 ns - 1 ns inherent delay.

