

Silicon NPN Power Transistors

2SC1828

DESCRIPTION

- With TO-66 package
- Continuous collector current- $I_C=1A$
- Power dissipation $-P_C=40W @T_C=25^\circ C$

APPLICATIONS

- For power amplifier applications

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

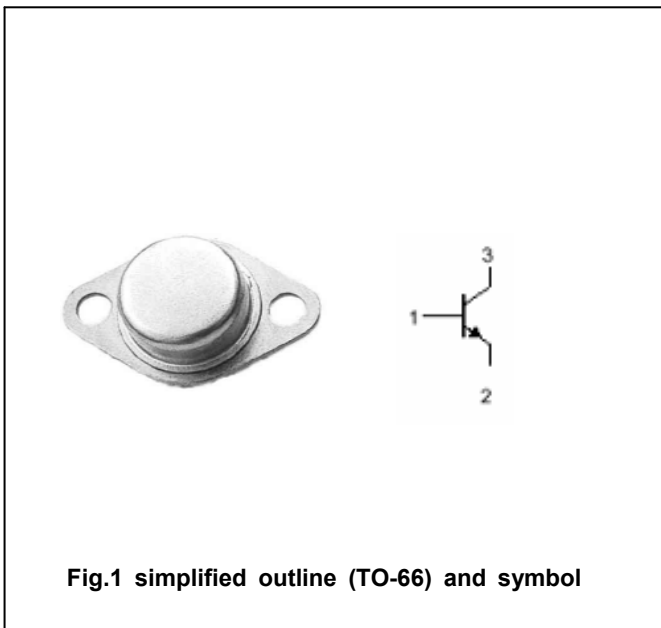


Fig.1 simplified outline (TO-66) and symbol

Absolute maximum ratings($T_a=25^\circ C$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	800	V
V_{CEO}	Collector-emitter voltage	Open base	400	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		1	A
P_T	Total power dissipation	$T_C=25^\circ C$	40	W
T_j	Junction temperature		150	$^\circ C$
T_{stg}	Storage temperature		-55~150	$^\circ C$

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =100mA ; I _B =0	400			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =1mA ; I _E =0	800			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =1A ; I _B =0.2A			1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =1A ; I _B =0.2A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =800V ; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =6V ; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =0.2A ; V _{CE} =10V	30		200	
f _T	Transition frequency	I _C =0.1A ; V _{CE} =10V		10		MHz

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PACKAGE OUTLINE



Fig.2 Outline dimensions