

MC74HC1G14

Single Inverter with Schmitt-Trigger Input

The MC74HC1G14 is a high speed CMOS inverter with Schmitt-Trigger input fabricated with silicon gate CMOS technology.

The internal circuit is composed of multiple stages, including a buffer output which provides high noise immunity and stable output.

The MC74HC1G14 output drive current is 1/2 compared to MC74HC series.

- High Speed: $t_{PD} = 7.0$ ns (Typ) at $V_{CC} = 5.0$ V
- Low Power Dissipation: $I_{CC} = 1.0$ μ A (Max) at $T_A = 25^\circ$ C
- High Noise Immunity
- Balanced Propagation Delays ($t_{PLH} = t_{PHL}$)
- Symmetrical Output Impedance ($I_{OH} = I_{OL} = 2.0$ mA)
- Chip Complexity: FET = 101
- Pb-Free Packages are Available

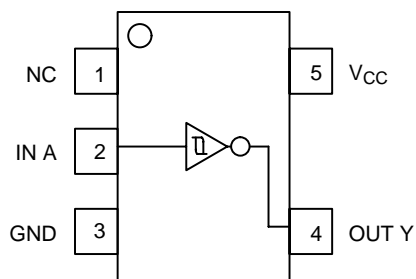


Figure 1. Pinout (Top View)

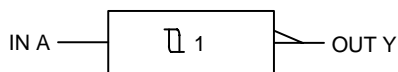


Figure 2. Logic Symbol



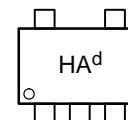
ON Semiconductor®

<http://onsemi.com>

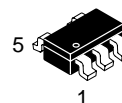


SC70-5/SC-88A/SOT-353
DF SUFFIX
CASE 419A

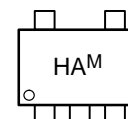
MARKING DIAGRAMS



Pin 1



SOT23-5/TSOP-5/SC59-5
DT SUFFIX
CASE 483



Pin 1

d = Date Code
M = Month Code

PIN ASSIGNMENT	
1	NC
2	IN A
3	GND
4	OUT Y
5	V _{CC}

FUNCTION TABLE	
Inputs	Outputs
L	H
H	L

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

MC74HC1G14

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V	
V _{IN}	DC Input Voltage	-0.5 to V _{CC} + 0.5	V	
V _{OUT}	DC Output Voltage	-0.5 to V _{CC} + 0.5	V	
I _{IK}	DC Input Diode Current	±20	mA	
I _{OK}	DC Output Diode Current	±20	mA	
I _{OUT}	DC Output Sink Current	±12.5	mA	
I _{CC}	DC Supply Current per Supply Pin	±25	mA	
T _{STG}	Storage Temperature Range	-65 to +150	°C	
T _L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C	
T _J	Junction Temperature Under Bias	+150	°C	
θ _{JA}	Thermal Resistance	SC70-5/SC-88A/SOT-353 (Note 1) SOT23-5/TSOP-5/SC59-5	350 230	°C/W
P _D	Power Dissipation in Still Air at 85°C	SC70-5/SC-88A/SOT-353 SOT23-5/TSOP-5/SC59-5	150 200	mW
MSL	Moisture Sensitivity	Level 1		
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V _{ESD}	ESD Withstand Voltage	Human Body Model (Note 2) Machine Model (Note 3) Charged Device Model (Note 4)	>2000 >200 N/A	V
I _{LATCHUP}	Latchup Performance	Above V _{CC} and Below GND at 125°C (Note 5)	±500	mA

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace with no air flow.
2. Tested to EIA/JESD22-A114-A.
3. Tested to EIA/JESD22-A115-A.
4. Tested to JESD22-C101-A.
5. Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	DC Supply Voltage	2.0	6.0	V
V _{IN}	DC Input Voltage	0.0	V _{CC}	V
V _{OUT}	DC Output Voltage	0.0	V _{CC}	V
T _A	Operating Temperature Range	-55	+125	°C
t _r , t _f	Input Rise and Fall Time	V _{CC} = 3.3 V ± 0.3 V V _{CC} = 5.0 V ± 0.5 V	No Limit No Limit	ns/V

DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES

Junction Temperature °C	Time, Hours	Time, Years
80	1,032,200	117.8
90	419,300	47.9
100	178,700	20.4
110	79,600	9.4
120	37,000	4.2
130	17,800	2.0
140	8,900	1.0

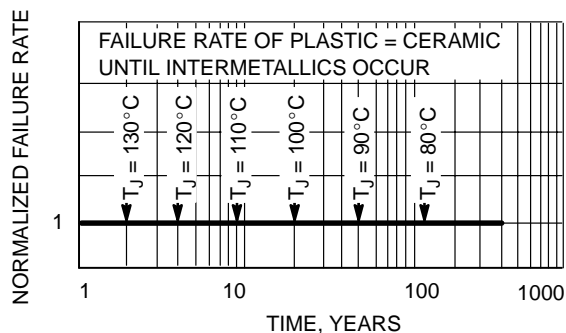


Figure 3. Failure Rate vs. Time Junction Temperature

MC74HC1G14

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	V _{CC} (V)	T _A = 25°C			T _A ≤ 85°C		-55°C ≤ T _A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V _{T+}	Positive-Going Input Threshold Voltage	V _{OUT} = 0.1 V I _{OUT} ≤ 20 μA	2.0	1.00	1.34	1.50	0.95	1.60	0.90	1.60	V
			3.0	1.50	2.02	2.5	1.45	2.60	1.40	2.60	
			4.5	2.30	2.97	3.15	2.25	3.15	2.20	3.15	
			6.0	3.00	3.93	4.20	2.95	4.20	2.90	4.20	
V _{T-}	Negative-Going Input Threshold Voltage	V _{OUT} = V _{CC} - 0.1 V I _{OUT} ≤ 20 μA	2.0	0.55	0.86	1.15	0.55	1.20	0.55	1.25	V
			3.0	1.0	1.44	1.80	1.0	1.85	1.0	1.90	
			4.5	1.75	2.28	2.75	1.75	2.8	1.75	2.85	
			6.0	2.65	3.14	3.65	2.65	3.7	2.65	3.75	
V _H	Hysteresis Voltage	V _{OUT} = 0.1 V or V _{OUT} = V _{CC} - 0.1 V I _{OUT} ≤ 20 μA	2.0	0.20	0.478	0.8	0.20	0.8	0.20	0.8	V
			3.0	0.25	0.584	0.95	0.25	0.55	0.25	0.95	
			4.5	0.30	0.686	1.1	0.30	1.1	0.30	1.1	
			6.0	0.40	0.790	1.25	0.40	1.5	0.40	1.25	
V _{OH}	Minimum High-Level Output Voltage	V _{IN} = V _{IH} or V _{IL} I _{OH} = -20 μA	2.0	1.9	2.0		1.9		1.9		V
			3.0	2.9	3.0		2.9		2.9		
			4.5	4.4	4.5		4.4		4.4		
			6.0	5.9	6.0		5.9		5.9		
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} ≥ V _T + Max I _{OL} = 20 μA	2.0		0.0	0.1		0.1		0.1	V
			3.0		0.0	0.1		0.1		0.1	
			4.5		0.0	0.1		0.1		0.1	
			6.0		0.0	0.1		0.1		0.1	
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} = V _{IH} or V _{IL} I _{OL} = 2 mA I _{OL} = 2.6 mA	4.5		0.17	0.26		0.33		0.40	V
			6.0		0.18	0.26		0.33		0.40	
I _{IN}	Maximum Input Leakage Current	V _{IN} = 6.0 V or GND	6.0			±0.1		±1.0		±1.0	μA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	6.0			1.0		10		40	μA

AC ELECTRICAL CHARACTERISTICS (Input t_r = t_f = 6.0 ns)

Symbol	Parameter	Test Conditions	T _A = 25°C			T _A ≤ 85°C		-55°C ≤ T _A ≤ 125°C		Unit	
			Min	Typ	Max	Min	Max	Min	Max		
t _{PLH} , t _{PHL}	Maximum Propagation Delay, Input A or B to \bar{Y}	V _{CC} = 5.0 V C _L = 15 pF		3.5	15		20		25	ns	
			V _{CC} = 2.0 V C _L = 50 pF		19	100		125			155
			V _{CC} = 3.0 V		10.5	27		35			90
			V _{CC} = 4.5 V		7.5	20		25			35
			V _{CC} = 6.0 V		6.5	17		21			26
t _{TLH} , t _{THL}	Output Transition Time	V _{CC} = 5.0 V C _L = 15 pF		3	10		15		20	ns	
			V _{CC} = 2.0 V C _L = 50 pF		25	125		155			200
			V _{CC} = 3.0 V		16	35		45			60
			V _{CC} = 4.5 V		11	25		31			38
			V _{CC} = 6.0 V		9	21		26			32
C _{IN}	Maximum Input Capacitance			5	10		10		10	pF	
C _{PD}	Power Dissipation Capacitance (Note 6)	Typical @ 25°C, V _{CC} = 5.0 V							pF		
		10									

6. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no-load dynamic power consumption; P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

MC74HC1G14

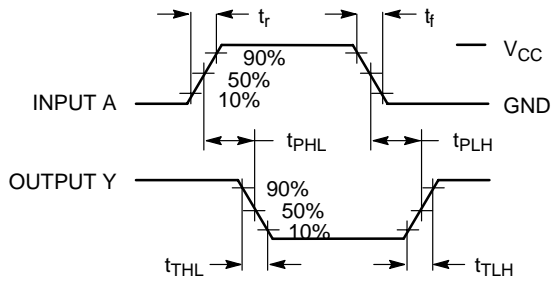
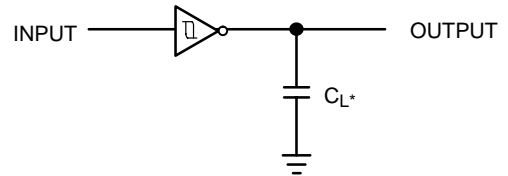


Figure 4. Switching Waveforms



*Includes all probe and jig capacitance.
A 1-MHz square input wave is recommended for propagation delay tests.

Figure 5. Test Circuit

DEVICE ORDERING INFORMATION

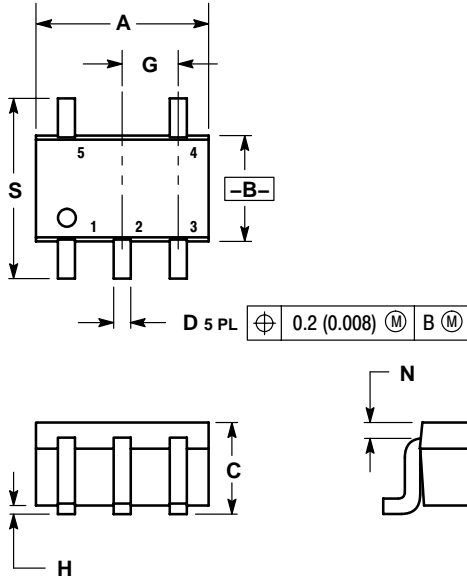
Device Order Number	Device Nomenclature						Package Type	Tape and Reel Size†
	Logic Circuit Indicator	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape and Reel Suffix		
MC74HC1G14DFT1	MC	74	HC1G	14	DF	T1	SC70-5/SC-88A/SOT-353	178 mm (7 in) 3000 Unit
MC74HC1G14DFT1G	MC	74	HC1G	14	DF	T1	SC70-5/SC-88A/SOT-353 (Pb-Free)	178 mm (7 in) 3000 Unit
MC74HC1G14DFT2	MC	74	HC1G	14	DF	T2	SC70-5/SC-88A/SOT-353	178 mm (7 in) 3000 Unit
MC74HC1G14DFT2G	MC	74	HC1G	14	DF	T2	SC70-5/SC-88A/SOT-353 (Pb-Free)	178 mm (7 in) 3000 Unit
MC74HC1G14DTT1	MC	74	HC1G	14	DT	T1	SOT23-5/TSOP-5/SC59-5	178 mm (7 in) 3000 Unit
MC74HC1G14DTT1G	MC	74	HC1G	14	DT	T1	SOT23-5/TSOP-5/SC59-5 (Pb-Free)	178 mm (7 in) 3000 Unit

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MC74HC1G14

PACKAGE DIMENSIONS

SC70-5/SC-88A/SOT-353
 DF SUFFIX
 5-LEAD PACKAGE
 CASE 419A-02
 ISSUE G

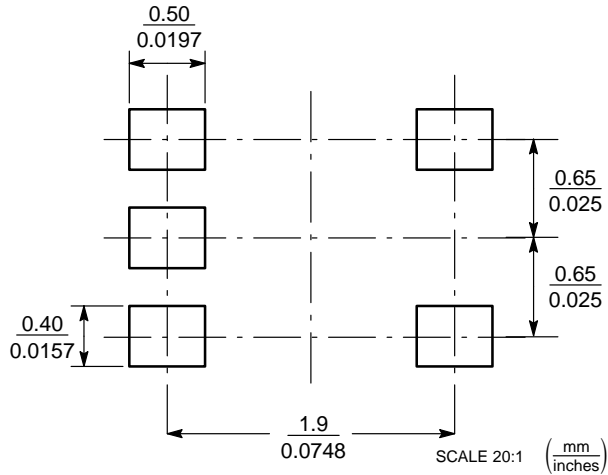


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20

SOLDERING FOOTPRINT*

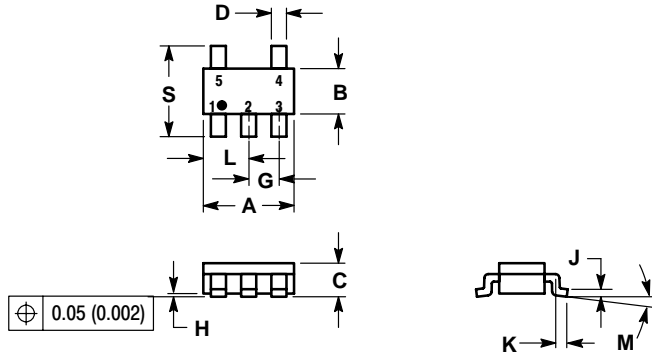


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MC74HC1G14

PACKAGE DIMENSIONS

SOT23-5/TSOP-5/SC59-5
DT SUFFIX
5-LEAD PACKAGE
CASE 483-02
ISSUE C

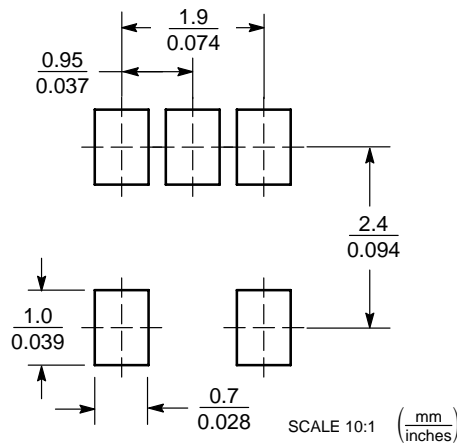


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. A AND B DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.90	3.10	0.1142	0.1220
B	1.30	1.70	0.0512	0.0669
C	0.90	1.10	0.0354	0.0433
D	0.25	0.50	0.0098	0.0197
G	0.85	1.05	0.0335	0.0413
H	0.013	0.100	0.0005	0.0040
J	0.10	0.26	0.0040	0.0102
K	0.20	0.60	0.0079	0.0236
L	1.25	1.55	0.0493	0.0610
M	0	10	0	10
S	2.50	3.00	0.0985	0.1181

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.