

Low VF Current Density Surface Mount Schottky Barrier Rectifiers

Major Ratings and Characteristics

$I_{F(AV)}$	2 A
V_{RRM}	20 V, 30 V
I_{FSM}	50 A
E_{AS}	11.25 mJ
V_F	0.45 V
T_j max.	150 °C



Features

- Very low profile - typical height of 1.0mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency
- Low thermal resistance
- Meets MSL level 1, per J-STD-020C
- AEC-Q101 qualified

DO-220AA (SMP)

Typical Applications

For use in low voltage high frequency inverters, free-wheeling, dc-to-dc converters, and polarity protection applications

Mechanical Data

Case: DO-220AA (SMP)

Epoxy meets UL-94V-0 Flammability rating

Terminals: Matte Tin plated (E3 Suffix) leads, solderable per J-STD-002B and MIL-STD-750, Method 2026

Polarity: Color band denotes the cathode end

Maximum Ratings

$T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	SS2P2L	SS2P3L	Unit
Device marking code		22L	23L	
Maximum repetitive peak reverse voltage	V_{RRM}	20	30	V
RMS reverse voltage	V_{RWM}	14	21	V
DC blocking voltage	V_R	20	30	V
Maximum average forward rectified current see Fig. 1	$I_{F(AV)}$	2.0		A
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I_{FSM}	50		A
Non-repetitive avalanche energy at $I_{AS} = 1.5 \text{ A}$, $L = 10 \text{ mH}$, $T_J = 25^\circ\text{C}$	E_{AS}	11.25		mJ
Voltage rate of change (rated V_R)	dv/dt	10000		V/us
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150		°C

SS2P2L & SS2P3L



Vishay Semiconductors

Electrical Characteristics

T_A = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Typ	Max.	Unit
Maximum instantaneous forward voltage ⁽¹⁾	at I _F = 2 A, T _J = 25 °C at I _F = 2 A, T _J = 125 °C	V _F	0.45 0.38	0.50 0.45	V
Maximum reverse current at rated VRM ⁽¹⁾	T _J = 25 °C T _J = 125 °C	I _R	- 9.0	200 20	µA mA
Typical junction capacitance	at 4.0 V, 1 MHz	C _J	130		pF

Note:

(1) Pulse test: 300µs pulse width, 1% duty cycle

Thermal Characteristics

T_A = 25 °C, unless otherwise specified

Parameter	Symbol	SS2P2L	SS2P3L	Unit
Typical thermal resistance ⁽¹⁾	R _{θJA} R _{θJL} R _{θJC}	115 15 20		°C/W

Note:

(1) Thermal resistance from junction to ambient and junction to lead mounted on P.C.B. with 5.0 x 5.0mm copper pad areas. R_{θJL} is measured at the terminal of cathode band. R_{θJC} is measured at the top centre of the body

Ratings and Characteristics Curves

($T_A = 25^\circ\text{C}$ unless otherwise specified)

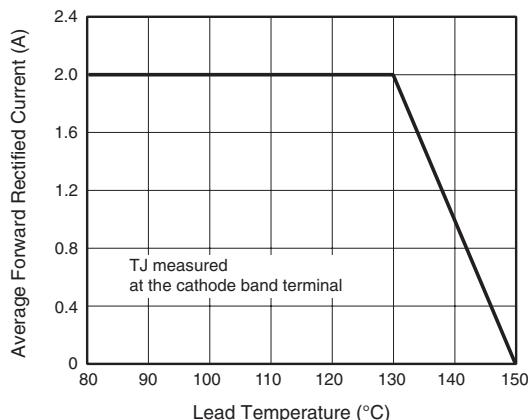


Figure 1. Forward Current Derating Curve

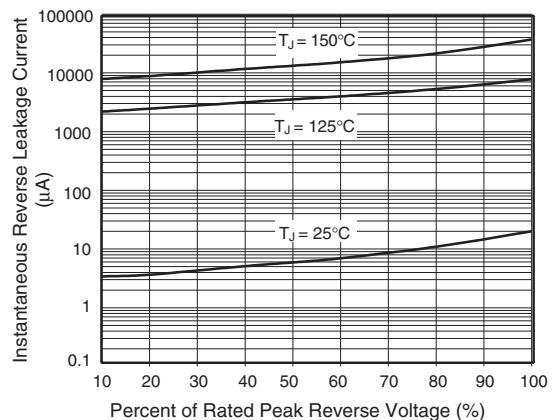


Figure 4. Typical Reverse Leakage Characteristics

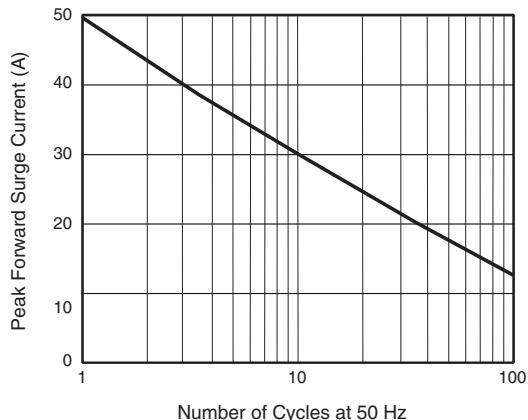


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

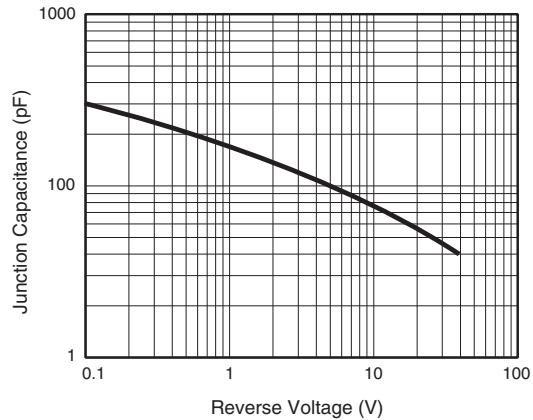


Figure 5. Typical Junction Capacitance

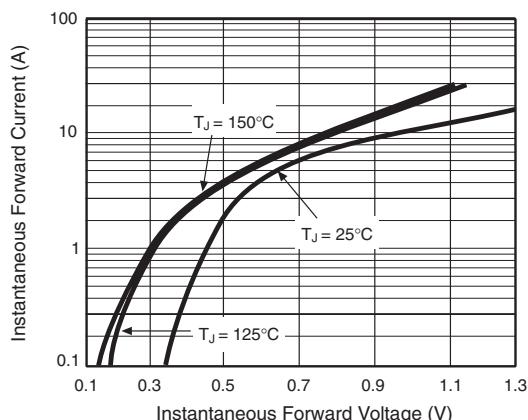


Figure 3. Typical Instantaneous Forward Characteristics

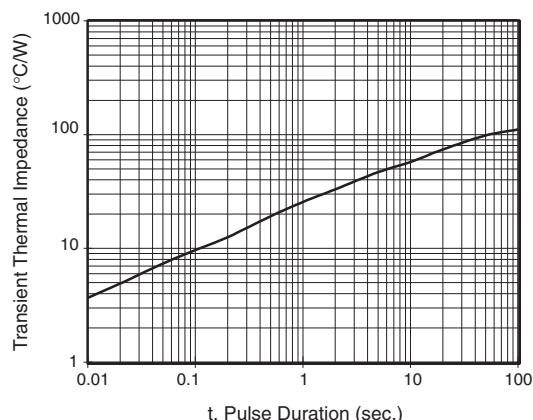


Figure 6. Typical Transient Thermal impedance

SS2P2L & SS2P3L

Vishay Semiconductors



Package Dimensions in Inches (millimeters)

DO-220AA (SMP)

