



# SPN4910

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN4910 is the Dual N-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

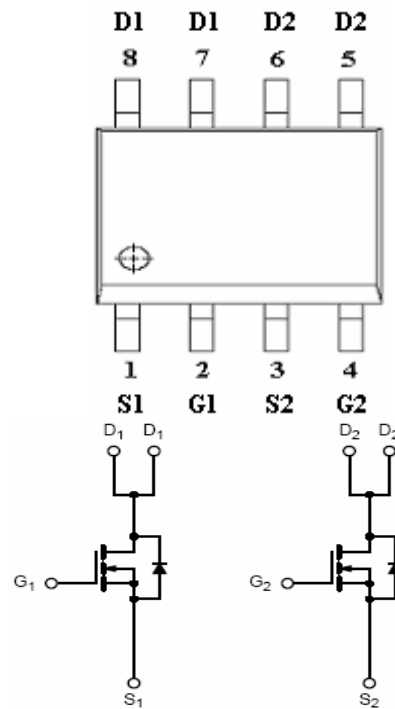
### FEATURES

- ◆ N-Channel
  - 40V/10A,  $R_{DS(ON)} = 20m\Omega @ V_{GS} = 10V$
  - 40V/ 8A,  $R_{DS(ON)} = 24m\Omega @ V_{GS} = 4.5V$
  - 40V/ 6A,  $R_{DS(ON)} = 30m\Omega @ V_{GS} = 2.5V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP – 8P package design

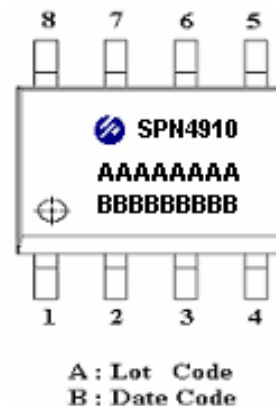
### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### PIN CONFIGURATION(SOP – 8P)



### PART MARKING





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### PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN4910S8RGB	SOP- 8P	SPN4910

※ SPN4910S8RGB 13" Tape Reel ; Pb – Free ; Halogen – Free  
e

### ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	40	V
Gate –Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	TA=25°C	10.0
		TA=70°C	8.0
Pulsed Drain Current	I <sub>DM</sub>	25	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	2.3	A
Power Dissipation	P <sub>D</sub>	TA=25°C	2.5
		TA=70°C	1.6
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	T ≤ 10sec	50
		Steady State	80



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### ELECTRICAL CHARACTERISTICS

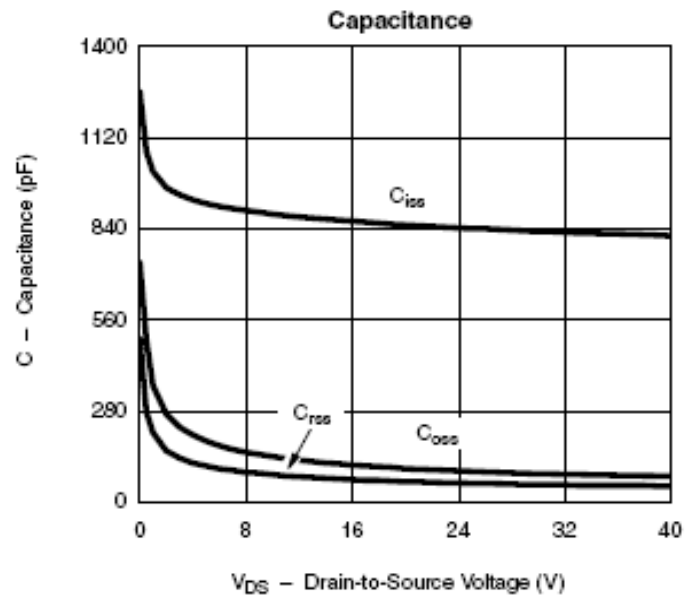
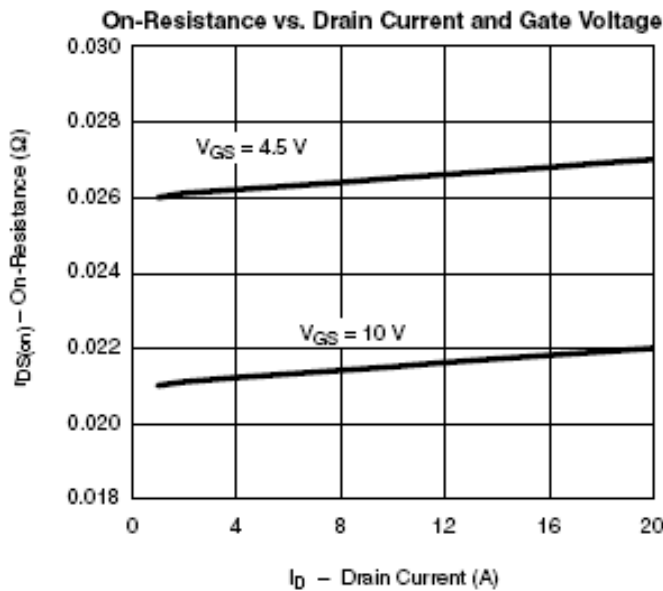
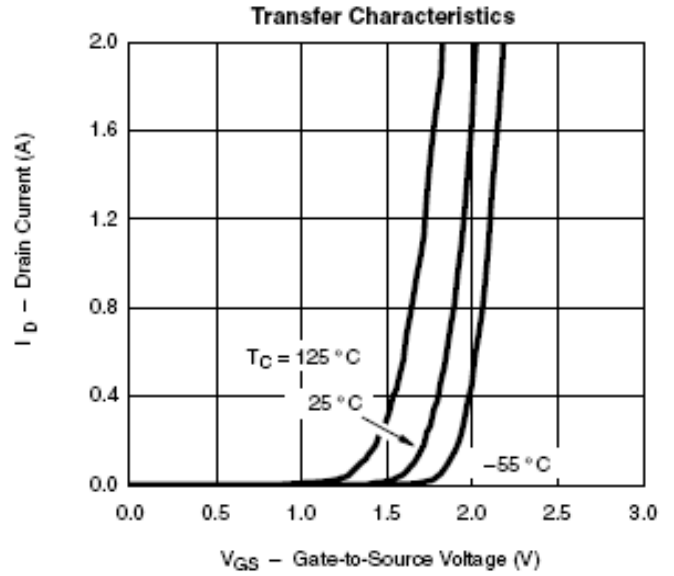
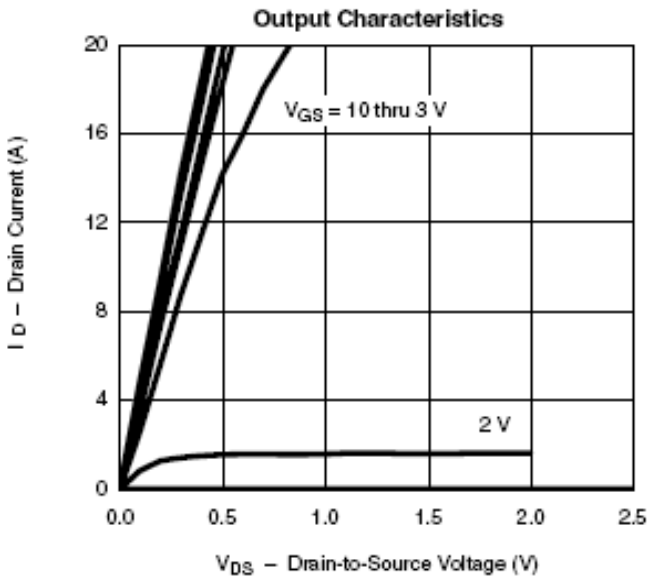
(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5		1.0	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μA
		V <sub>DS</sub> =40V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C			10	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> =4.5V	10			A
Drain-Source On-Resistance	R <sub>DSS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =10A		0.014	0.020	Ω
		V <sub>GS</sub> =4.5V, I <sub>D</sub> = 8A		0.017	0.024	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> = 6A		0.024	0.030	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =6.2A		13		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2.3A, V <sub>GS</sub> =0V		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V I <sub>D</sub> = 5A		10	14	nC
Gate-Source Charge	Q <sub>gs</sub>			2.8		
Gate-Drain Charge	Q <sub>gd</sub>			3.2		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V f=1MHz		850		pF
Output Capacitance	C <sub>oss</sub>			110		
Reverse Transfer Capacitance	C <sub>rss</sub>			75		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =20V, R <sub>L</sub> =4Ω I <sub>D</sub> =5.0A, V <sub>GEN</sub> =10V R <sub>G</sub> =1Ω		6	12	nS
	t <sub>r</sub>			10	20	
Turn-Off Time	t <sub>d(off)</sub>			20	36	
	t <sub>f</sub>			6	12	



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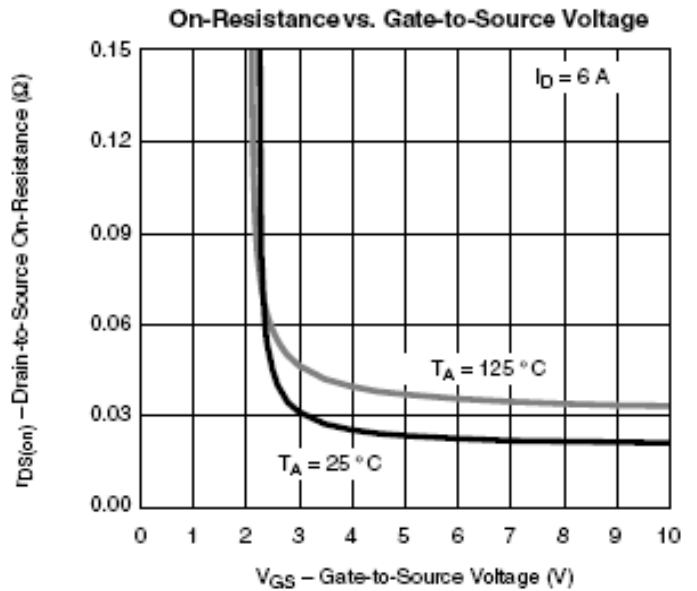
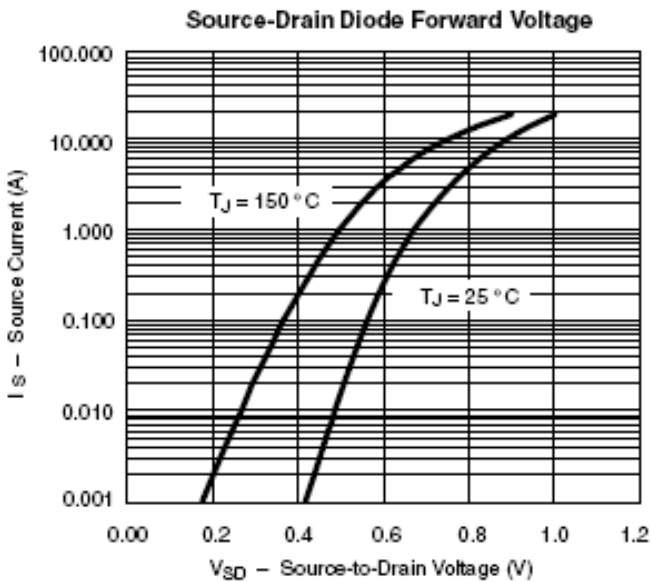
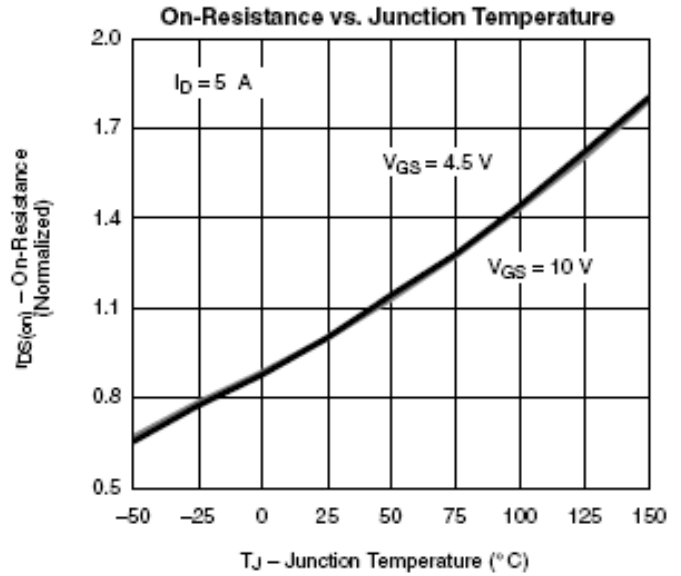
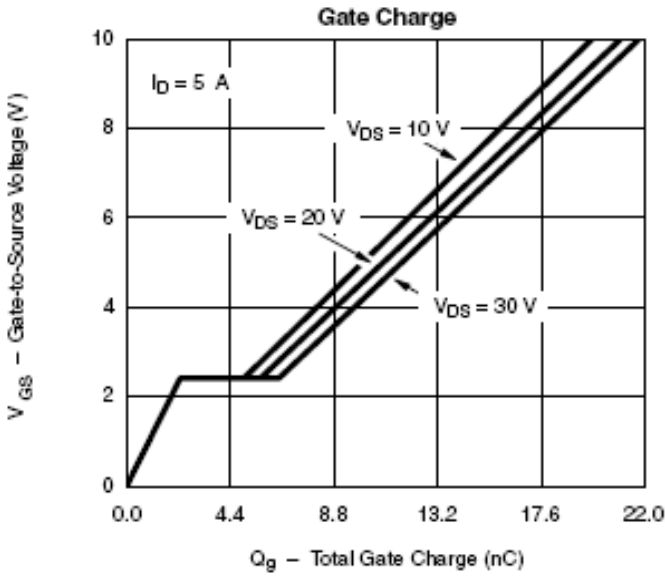
## TYPICAL CHARACTERISTICS





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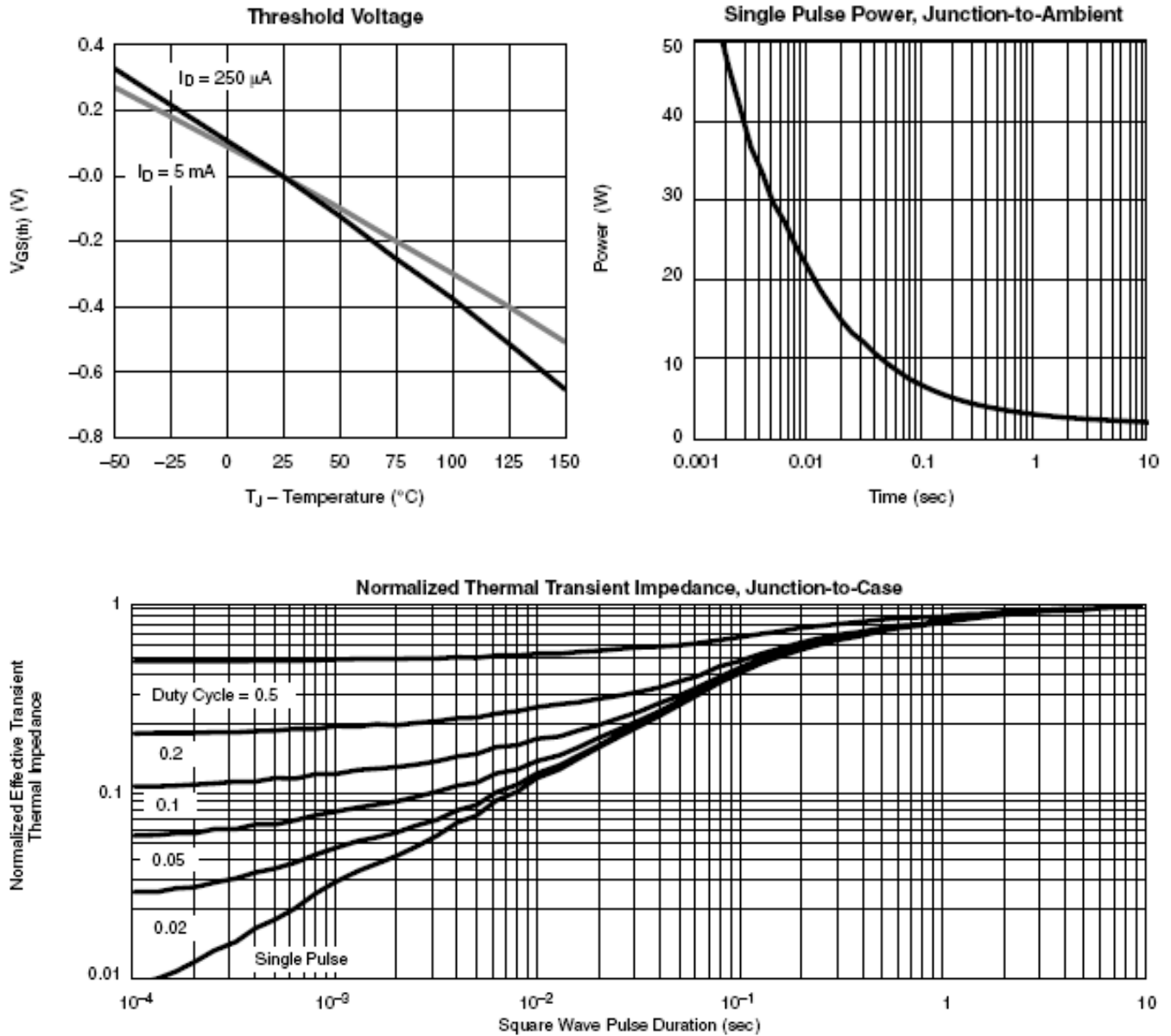
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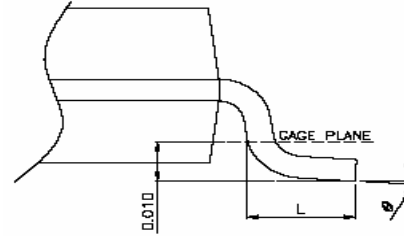
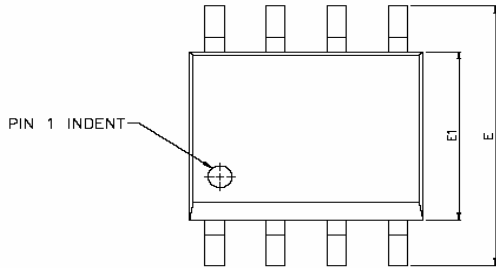
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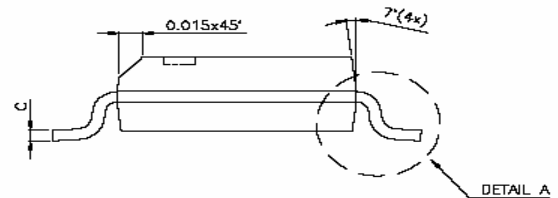
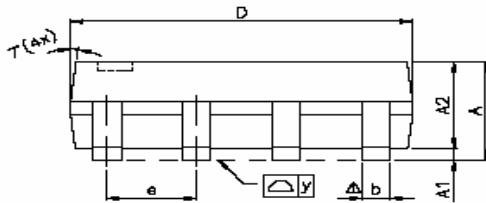


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## SOP- 8 PACKAGE OUTLINE



DETAIL A



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
△ y	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°



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