# Low Voltage Operating 75 $\Omega$ Driver

# Monolithic IC MM1671~MM1677 Series

July 8, 2003

## **Outline**

This IC is a  $75\Omega$  driver with a built-in LPF that can operate at low voltage. Operationg supply voltage supports 3V and 5V systems, and it is ideal for video signal output in devices ranging from portable equipment such as digital still cameras to stationary equipment such as DVD players. It incorporates a high-performance 4th-order LPF, which is ideal for removing DAC sampling noise. In addition, ultra-low current consumption has been achieved by suppressing current consumption in power-save mode to under  $1\mu$ A. This lengthens battery life in portable devices.

The built-in amp gain on these series ICs is available: 6dB/9dB/12dB/16.5dB, thus enabling support for DAC and a variety of output amplitudes. The series offers a choise of either with or without input clamp, allowing support for a range of video signals, other than composite signals.

## Line-up

Model Name	Clamp	Built-in amplifier	SAG measures pin	Power supply voltage
MM1671	0	6dB	0	2.8~5.5V
MM1672	0	9dB	0	2.8~5.5V
MM1673	0	12dB	0	2.8~5.5V
MM1674	×	6dB	0	2.8~5.5V
MM1675	×	9dB	0	2.8~5.5V
MM1676	×	12dB	0	2.8~5.5V
MM1677	0	16.5dB	0	2.8~5.5V

## **Features**

1. Operating supply voltage 2.8~5.5V (supports 3V and 5V systems)

Operating ambient temperature -40~85°C
 Current consumption (no signal) 7.0mA typ.
 Current consumption in power-save mode 1.0μA max.

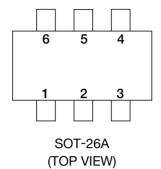
5. High-precision voltage gain
6±0.3dB at 100kHz (MM1671)
6. Includes a high-performance 4th-order LPF
6.75MHz/100kHz max. ±1.0dB
27MHz/100kHz typ. -40dB

- 7. Incorporates a sag auxiliary circuit
- 8. Built-in amp output gain available in these series models (6dB/9dB/12dB/16.5dB)
- 9. The series offers a choise of either with or without input clamp.
- 10. Mounted in a ultra-small package

## **Package**

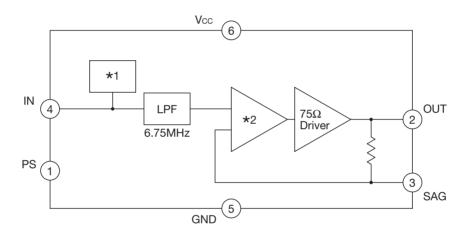
SOT-26A

# Pin Assignment



1	PS
2	OUT
3	SAG
4	IN
5	GND
5	Vcc

# Block Diagram



	*1 Input Clamp		★2 Built-in amplifier				
	bias	clamp	6dB (*2)	9dB(*2)	12dB (*2)	16.5dB (*2)	
MM1671		0	0				
MM1672		0		0			
MM1673		0			0		
MM1674	0		0				
MM1675	0			0			
MM1676	0				0		
MM1677		0				0	

# Pin Description

Pin no.	Pin name	Function	Internal equivalent circuit diagram
1	PS	Power save	10k
3	OUT SAG	Signal output SAG correction	Vcc 2 3 A A GND
4	IN	Signal input	Vcc 4 A GND
5	GND	GND	
6	Vcc	Vcc	

# Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	Tstg	-65~+150	°C
Operating temperature	Topr	-40~+85	°C
Supply Voltage	Vcc max.	7	V
Allowable loss	Pd	200	mW

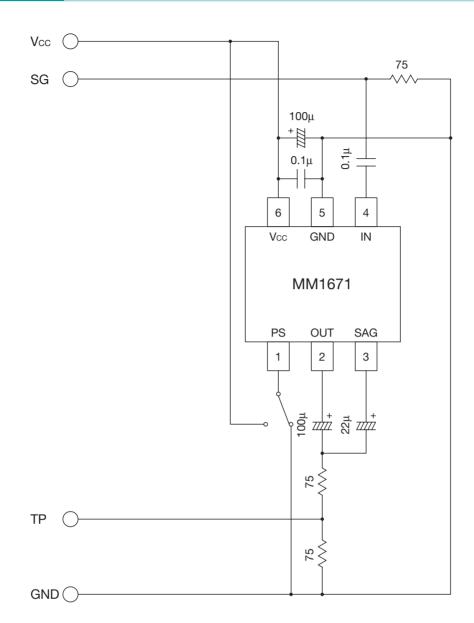
# **Recommended Operating Conditions**

Item	Symbol	Ratings	Units
Operating temperature	Topr	-40~+85	°C
Operating voltage	Vccop	2.8~5.5	V

# Electrical Characteristics (Except where noted otherwise, Ta=25°C, Vcc=3V)

Item		Symbol	Measurement conditions	Min.	Тур.	Max.	Units
Supply current		Icc1	No signal		7	10	mA
Supply current (at power save mod	Supply current (at power save mode)		No signal, PS: ON			1	μА
Power save terminal	Н	Iрsн	1PIN V <sub>H</sub> =2.8V			360	μA
input current	L	Ipsl	1PIN V <sub>L</sub> =0.2V			18	μA
Power save terminal	Н	VPSH		2.0		Vcc	V
input voltage	L	VPSL				0.5	V
Input terminal voltage	ge	V <sub>IN</sub>	4PIN		1.2		V
Output terminal voltage		Vout	2PIN	0.15	0.3	0.45	V
Voltage gain		Gv	SIN wave: 1V, f=100kHz	5.7	6.0	6.3	dB
Frequency characteristic 1		fc1	SIN wave: 1V, 6.75MHz/100kHz	-1.0	0	1.0	dB
Frequency characteristic 2		fc2	SIN wave: 1V, 27MHz/100kHz		-40	-27	dB
Differential gain		DG	Staircase signal 1V		0.7	1.5	%
Differential phase	Differential phase		Staircase signal 1V		0.7	1.5	۰
Output dynamic ran	Output dynamic range		SIN wave: 100kHz, THD=1.0%	2.2	2.4		V
S/N		SN	BW: 100k~6MHz		74		dB
Group delay		t1	at 100kHz		50	80	ns
			to 3.58MHz		4	10	ns
Group delay		⊿t1	to 4.43MHz		6	10	ns
			to 6MHz		12	20	ns

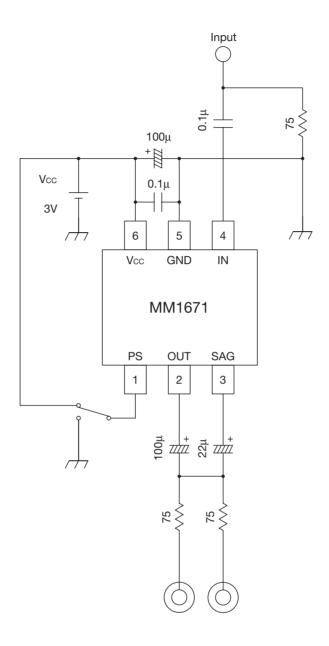
# **Measuring Circuit**



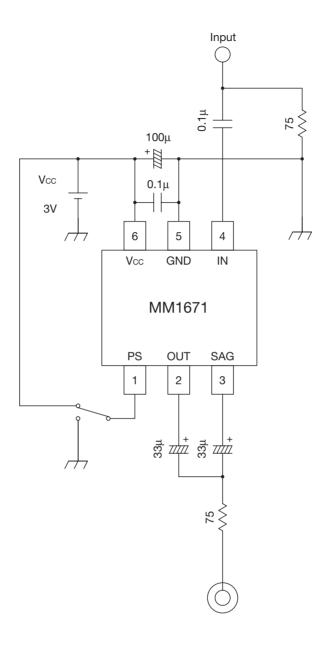
# Switch Control Table

PS-Pin	Power save
Н	OFF
L	ON
OPEN	ON

# **Application Circuit 1**

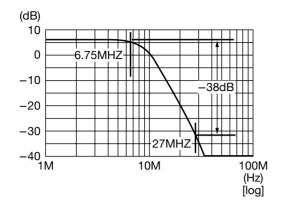


# **Application Circuit 2**

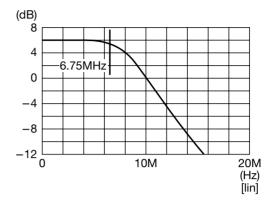


## Characteristics (Except where noted otherwise, Ta=25°C, Vcc=3V)

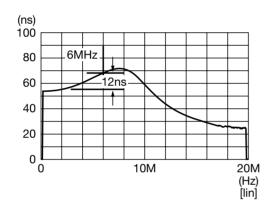
## ■ Frequency Characteristic [log]



## ■ Frequency Characteristic [lin]

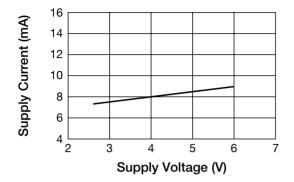


## Group Delay [lin]

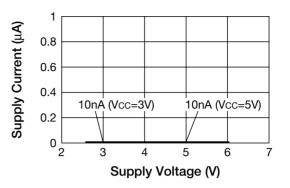


## Characteristics (Except where noted otherwise, Ta=25°C, Vcc=2.6~6.0V)

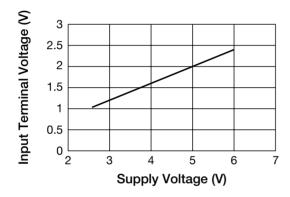
### Supply Current vs Supply Voltage



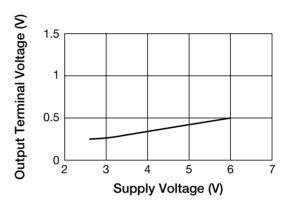
Supply Current vs Supply Voltage (at power save mode)



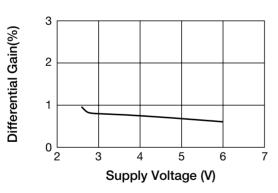
■ Input Terminal Voltage vs Supply Voltage



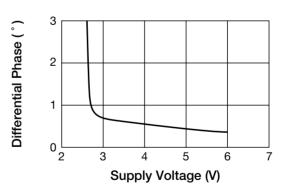
Output Terminal Voltage vs Supply Voltage



■ Differential Gain vs Supply Voltage

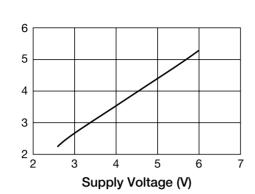


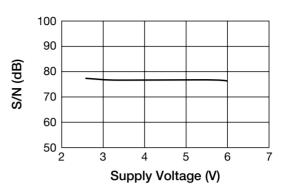
■ Differential Phase vs Supply Voltage

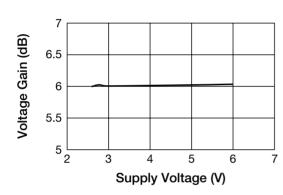


Output Dynamic Range (V)

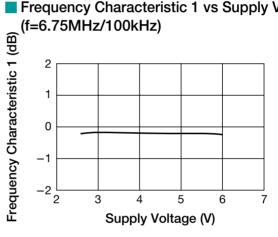
## ■ Output Dynamic Range vs Supply Voltage ■ S/N vs Supply Voltage



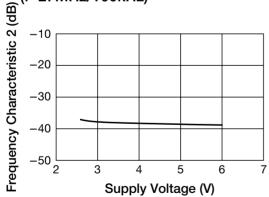




■ Voltage Gain vs Supply Voltage (f=100kHz) ■ Frequency Characteristic 1 vs Supply Voltage



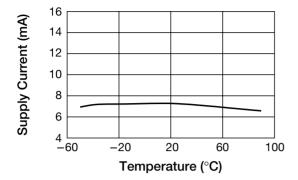
## ■ Frequency Characteristic 2 vs Supply Voltage (f=27MHz/100kHz)



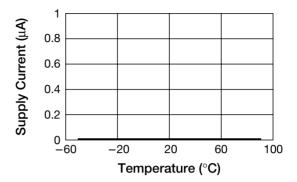
Note: There are typical characteristics. (Represent model MM1671XN)

## Characteristics (Except where noted otherwise, Ta=-50~90°C, Vcc=3V)

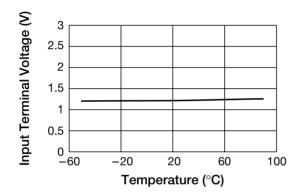
### Supply Current vs Temperature



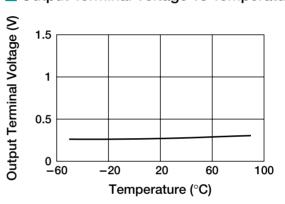
■ Supply Current vs Temperature (at power save mode)



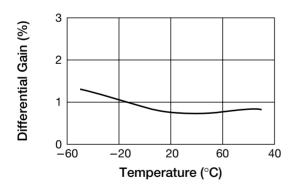
■ Input Terminal Voltage vs Temperature



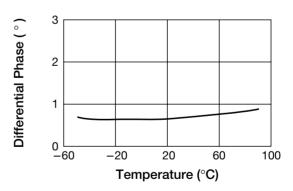
Output Terminal Voltage vs Temperature



■ Differential Gain vs Temperature

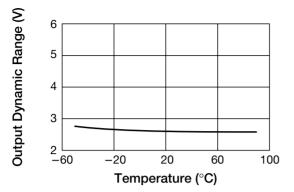


■ Differential Phase vs Temperature

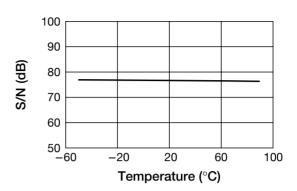


Note: There are typical characteristics. (Represent model MM1671XN)

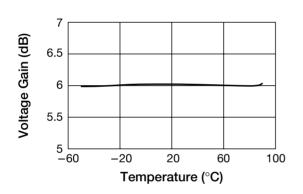
### Output Dynamic Range vs Temperature



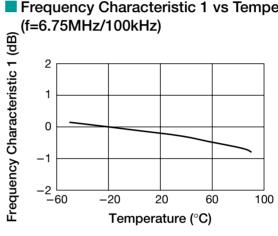
### S/N vs Temperature



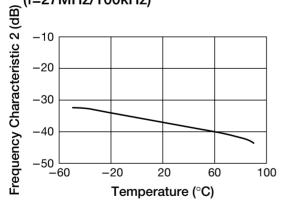
■ Voltage Gain vs Temperature (f=100kHz)



Frequency Characteristic 1 vs Temperature



Frequency Characteristic 2 vs Temperature (f=27MHz/100kHz)



Note: There are typical characteristics. (Represent model MM1671XN)