

**MSAFA1N100D**  
Fast MOSFET Die for  
Implantable Cardio Defibrillator  
Applications

**DESCRIPTION:**

- N-Channel enhancement mode high density MOSFET die
- Passivation: oxynitride, 4um
- Frontside (top) Metallization: Al/1%Cu for aluminum wire bonding, 9 um typical.
- Backside Metallization: Ti – Ni (1 um) – Ag (0.2 um) for soft solder attach

**FEATURES:**

- Low On-state resistance
- Avalanche and Surge Rated
- High Freq. Switching
- Ultra Low Leakage Current
- UIS rated
- Available with Lot Acceptance Testing Spec MSAFA1N100DL, "-L" Suffix

**MAXIMUM RATINGS:**

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>DSS</sub>	Drain - Source Voltage	1000	Volts
V <sub>GS</sub>	Gate - Source Voltage	±20	Volts
I <sub>D1</sub>	Continuous Drain Current @ T <sub>C</sub> = 25°C	1	Amps
I <sub>D2</sub>	Continuous Drain Current @ T <sub>C</sub> = 100°C	.8	Amps
I <sub>DM1</sub>	Pulsed Drain Current ① @ T <sub>C</sub> = 25°C	4	Amps
I <sub>AR</sub>	Avalanche Current	1	Amps
E <sub>AR</sub>	Repetitive Avalanche Energy	TBD	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy	TBD	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage: Junction Temperature Range	-55 to 150	°C

**STATIC ELECTRICAL CHARACTERISTICS:**

SYMBOL	CHARACTERISTIC / TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain - Source Breakdown Voltage (V <sub>GS</sub> = 0V, I <sub>D</sub> = 0.25mA)	1000			Volts
V <sub>GS(TH)2</sub>	Gate Threshold Voltage (V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 1 mA, T <sub>J</sub> = 37°C)		3.4		Volts
V <sub>GS(TH)1</sub>	Gate Threshold Voltage (V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 1 mA, T <sub>J</sub> = 25°C)	2	3.5	4.5	Volts
R <sub>DS(ON)1</sub>	Drain – Source On-State Resistance (V <sub>GS</sub> = 10V, I <sub>D</sub> = I <sub>D1</sub> , T <sub>J</sub> = 25°C)		12.5	13.5	ohm
R <sub>DS(ON)2</sub>	Drain – Source On-State Resistance (V <sub>GS</sub> = 7V, I <sub>D</sub> = 5...150 mA, T <sub>J</sub> = 37°C)		12.5		ohm
R <sub>DS(ON)3</sub>	Drain – Source On-State Resistance (V <sub>GS</sub> = 7V, I <sub>D</sub> = 5...150 mA, T <sub>J</sub> = 25°C)		11.5		ohm
R <sub>DS(ON)4</sub>	Drain – Source On-State Resistance (V <sub>GS</sub> = 7V, I <sub>D</sub> = 5...150 mA, T <sub>J</sub> = 60°C)		15		ohm
R <sub>DS(ON)5</sub>	Drain – Source On-State Resistance (V <sub>GS</sub> = 7V, I <sub>D</sub> = I <sub>D1</sub> , T <sub>J</sub> = 125°C)		23.5		ohm
I <sub>DSS1</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 80%BV <sub>DSS</sub> , V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C)			10	uA
I <sub>DSS2</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 80%BV <sub>DSS</sub> , V <sub>GS</sub> = 0V, T <sub>J</sub> = 37°C)		1		uA
I <sub>DSS3</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 80%BV <sub>DSS</sub> , V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C)			100	uA
I <sub>GSS1</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±20V, V <sub>CE</sub> = 0V)			±100	nA
I <sub>GSS2</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±20V V <sub>CE</sub> = 0V), T <sub>J</sub> = 37°C		10		nA
I <sub>GSS3</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±20V V <sub>CE</sub> = 0V), T <sub>J</sub> = 125°C			500	nA

## DYNAMIC CHARACTERISTICS:

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		290	350	pF
$C_{oss}$	Output Capacitance	$V_{DS} = 25V$		36	45	pF
$C_{rss}$	Reverse Transfer Capacitance	$f = 1\text{ MHz}$		15	25	pF
$Q_g$	Total Gate Charge ④	$V_{GS} = 10V$		20		nC
$Q_{gs}$	Gate-Source Charge	$V_{DS} = 0.5BV_{DSS}$		1		nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$I_C = 20\text{ mA}$		10		nC
$t_d(\text{on})$	Turn-on Delay Time	Resistive Switching (25°C)		6.3		ns
$t_r$	Rise Time	$V_{GS} = 10V, V_{DS} = 0.5BV_{DSS}$		5.9		ns
$t_d(\text{off})$	Turn-off Delay Time	$I_D = 20\text{ mA}$		315		ns
$t_f$	Fall Time	$R_g = 1.6\Omega$		2.6		us
$t_d(\text{on})$	Turn-On Delay Time	Resistive Switching (25°C)		6.3		ns
$t_r$	Rise Time	$V_{GS} = 10V, V_{DS} = 0.5BV_{DSS}$		5.8		ns
$t_d(\text{off})$	Turn-off Delay Time	$I_D = 100\text{ mA}$		76		ns
$t_f$	Fall Time	$R_g = 1.6\Omega$		470		ns
$V_{SD}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = 1\text{ A}$			1	V
$t_{rr}$	Reverse Recovery Time	$I_S = 1\text{ A}, dI_S/dt = 100\text{ A/us}$		300	400	ns
$Q_{rr}$	Reverse Recovery Charge	$I_S = 1\text{ A}, dI_S/dt = 100\text{ A/us}$		700	1200	uC

① Repetitive Rating: Pulse width limited by maximum junction temperature.

②  $I_C = I_{C2}, V_{CC} = 50V, R_{CE} = 25\Omega, L = 300\mu H, T_J = 25^\circ C$

③  $T_J = 150^\circ C$

④ See MIL-STD-750 Method 3471

## DIE PROBE PARAMETERS (100% TESTS):

SYMBOL	CHARACTERISTIC / TEST CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage ( $V_{GS} = 0V, I_C = 0.25mA$ )	1000			Volts
$V_{GS(TH)}$	Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_C = 1000\mu A, T_J = 25^\circ C$ )	2		4.5	
$R_{DS(ON)}$	Drain-Source On-Resistance ( $V_{GS} = 10V, I_C = 1\text{ A}, T_J = 25^\circ C$ )			14	ohm
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{DS} = 800\text{ V}, V_{GS} = 0V, T_J = 25^\circ C$ )			25	uA
$I_{GSS}$	Gate-Source Leakage Current ( $V_{GS} = \pm 20\text{ V}, V_{DS} = 0V$ )			±100	nA

**MECHANICAL CHARACTERISTICS**

