

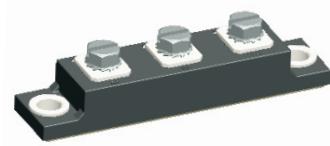
Schottky Rectifier, 300 A

FEATURES

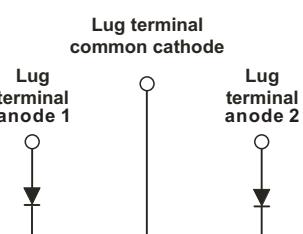
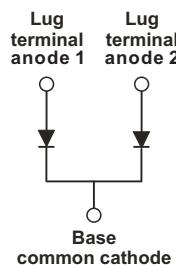
- 175°C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pd)-free
- Designed and qualified for industrial level



TO-244 (non-insulated)



TO-244 (insulated)



DESCRIPTION

The NKSD300... Schottky rectifier common cathode module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

PRODUCT SUMMARY

$I_{F(AV)}$	300A
V_R	100 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
$I_{F(AV)}$	Rectangular waveform	300	A
V_{RRM}		100	V
I_{FSM}	$t_p = 5 \mu s$ sine	22000	A
V_F	150 Apk, $T_J = 125^\circ C$ (per leg)	0.72	V
T_J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	NKSD300-100	UNIT
Maximum DC reverse voltage	V_R	100	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNIT		
Maximum average forward current See fig.5	$I_{F(AV)}$ per leg per device	50% duty cycle at $T_J = 138^\circ\text{C}$, rectangular waveform			150	A		
					300			
Maximum peak one cycle non-repetitive surge current per leg See fig.7	I_{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V_{RRM} applied		22000			
		10 ms sine or 6 ms rect. pulse			2500			
Non-repetitive avalanche energy per leg	E_{AS}	$T_J=25^\circ\text{C}$, $I_{AS}=13\text{A}$, $L=0.2\text{mH}$			15	mJ		
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A=1.5 \times V_R$ typical			1	A		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNIT	
Maximum forward voltage drop per leg See fig.1	$V_{FM}^{(1)}$	150A		$T_J = 25^\circ\text{C}$	0.91	V	
		300A			1.09		
		150A		$T_J = 125^\circ\text{C}$	0.72		
		300A			0.85		
Maximum reverse leakage current per leg See fig.2	$I_{RM}^{(1)}$	$T_J = 25^\circ\text{C}$		$V_R = \text{Rated } V_R$	4.5	mA	
		$T_J = 125^\circ\text{C}$			80		
Maximum junction capacitance per leg	C_T	$V_R = 5 \text{ V}_{\text{DC}}$ (test signal range 100 kHz to 1 MHz) 25°C			4150	pF	
Typical series inductance per leg	L_S	From top of terminal hole to mounting plane			6	nH	
Maximum voltage rate of change	dV/dt	Rated V_R			10000	$\text{V}/\mu\text{s}$	
Maximum RMS insulation voltage	V_{INS}	50 Hz			3000 (1min)	V	
					3600 (1s)		

Note

(1) Pulse width < 300 μs , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range		T_J, T_{Stg}	-55	-	175	$^\circ\text{C}$
Thermal resistance, junction to case per leg	TO-244 (non-insulated)	R_{thJC}	-	-	0.40	$^\circ\text{C/W}$
	TO-244 (insulated)		-	-	0.28	
Thermal resistance, junction to case per module	TO-244 (non-insulated)	R_{thCS}	-	-	0.20	
	TO-244 (insulated)		-	-	0.14	
Thermal resistance, case to heatsink			-	0.1	-	
Weight	TO-244 (non-insulated)		-	85 (3)	-	g(oz.)
	TO-244 (insulated)		-	100 (3.53)	-	
Mounting torque			35.4 (4)	-	53.1 (6)	$\text{lbf} \cdot \text{in}$ (N•m)
Mounting torque center hole			30 (3.4)	-	40 (4.6)	
Terminal torque			30 (3.4)	-	44.2 (5)	
vertical pull			-	-	80	$\text{lbf} \cdot \text{in}$
2" lever pull			-	-	35	
Case style			JEDEC		TO-244AA compatible	

Ordering Information Table

Device code	N	K	S	D	300	-	100	I
	1	2	3	4	5	6		

- [1] - Nell's power module
- [2] - S for Schottky Barrier Diode
- [3] - D for Dual Diodes, TO-244 Package
- [4] - Maximum average forward current, A
- [5] - Voltage rating (100 = 100V)
- [6] - None for non-insulated type
"I" for insulated type

Fig.1 Maximum forward voltage drop characteristics (Per Leg)

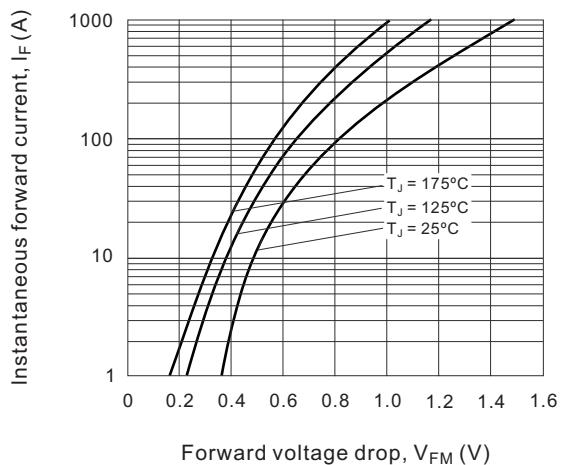


Fig.2 Typical values of reverse current vs. Reverse voltage (Per Leg)

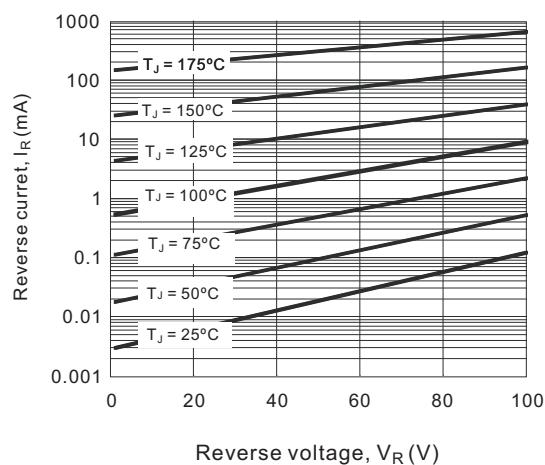
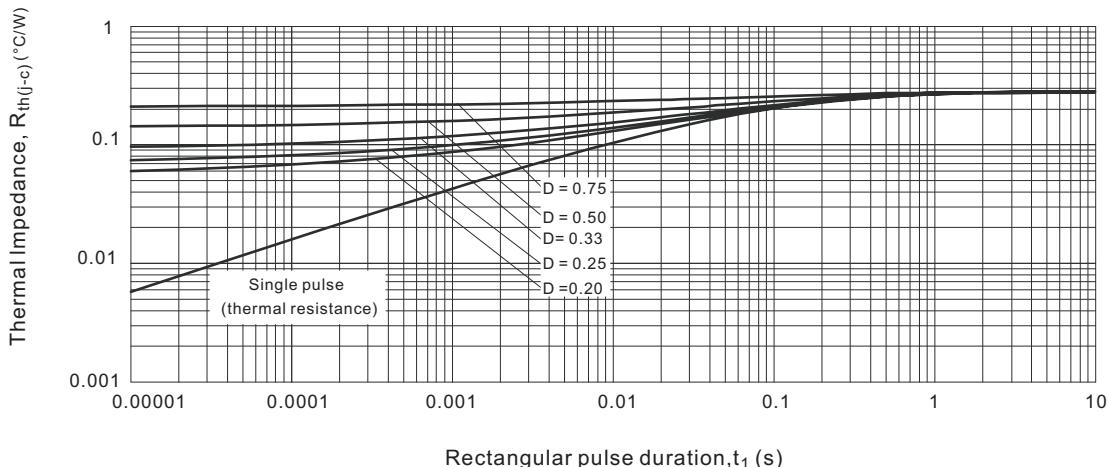
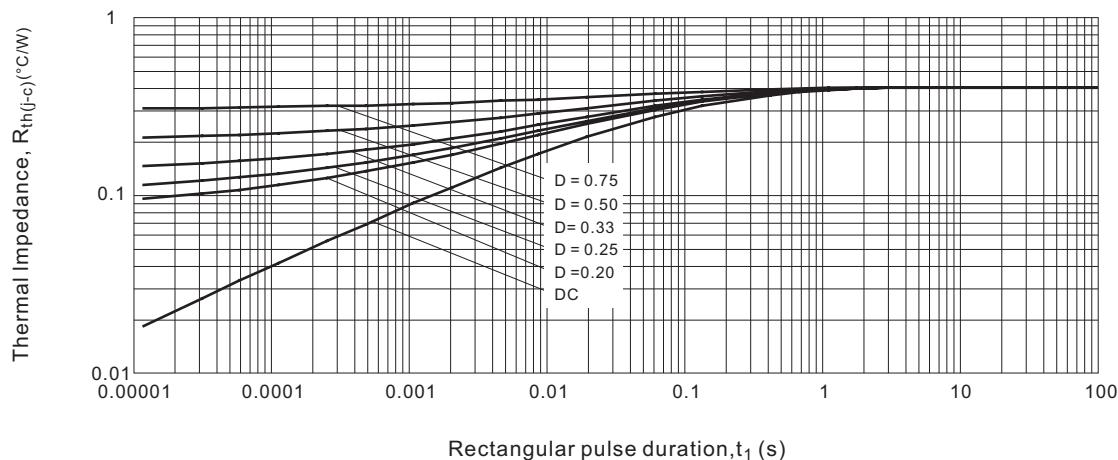


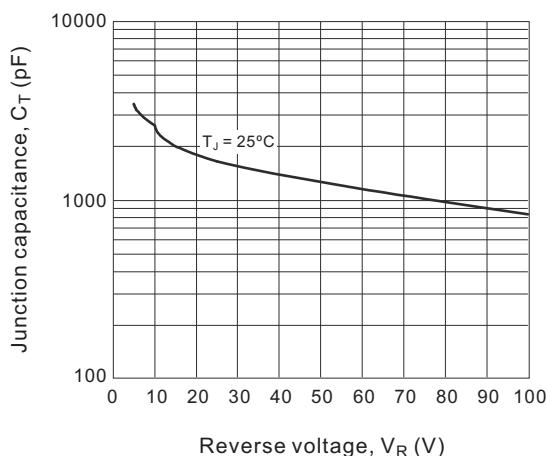
Fig.3-1 Maximum thermal impedance $R_{th(j-c)}$ characteristics (Per Leg, for TO-244 non-insulated)



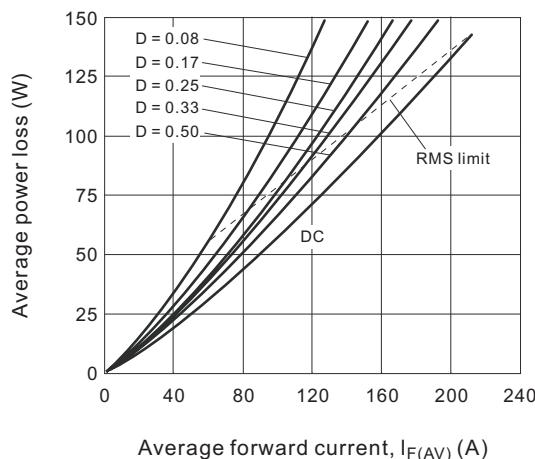
**Fig.3-2 Maximum thermal impedance $R_{th(j-c)}$ characteristics
(Per Leg, for TO-244 insulated)**



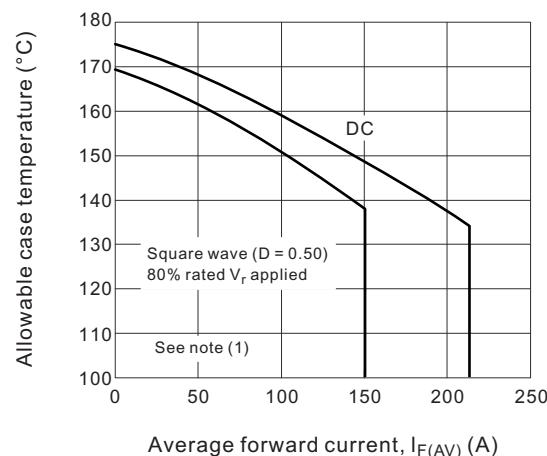
**Fig.4 Typical junction capacitance vs.
Reverse voltage**



**Fig.6 Forward power loss characteristics
(Per Leg)**



**Fig.5 Maximum allowable case temperature
vs. Average forward current (Per Leg)**



**Fig.7 Maximum non-repetitive surge current
(Per Leg)**

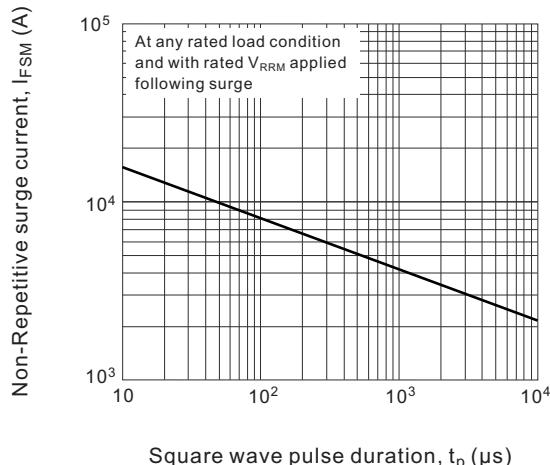
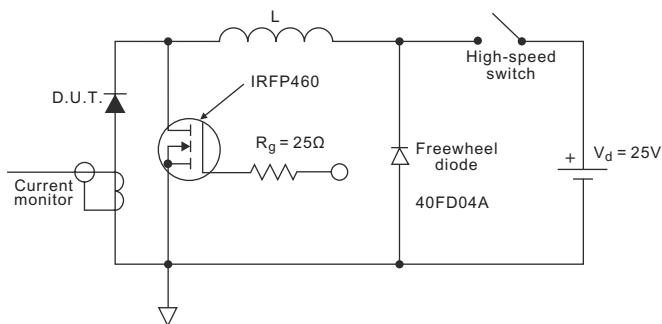
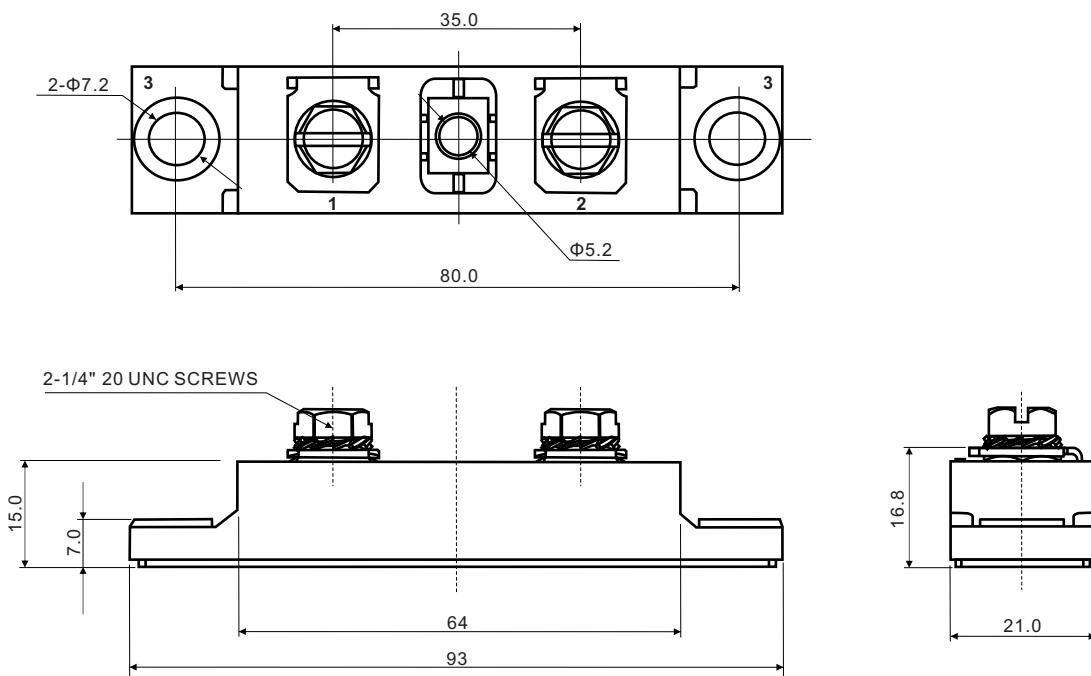


Fig.8 Unclamped Inductive test circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$
 $P_d = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig.6)}$
 $P_{dREV} = \text{Inverse power loss} = V_{R1} \times I_R (1-D); I_R \text{ at } V_{R1} = 80\% \text{ rated } V_R$

TO-244 (Non-Insulated)


All dimensions in millimeters

