EB51F4 Series

- Temperature Compensated Crystal Oscillator (TCXO)
- HCMOS Output
- 5.0V Supply Voltage
- Stability to 1.5ppm
- External voltage control option available





NOTES

ELECTRICAL SPECIFICATIONS

Frequency Range			1.544MHz	to 44.736MHz			
Operating Temperature Range			See Table 1	See Table 1			
Storage Temperature Range			-55°C to 1	-55°C to 125°C			
Supply Voltage (V _{DD})	Voltage (V _{DD})			5.0V _{DC} ±5%			
Input Current	Measured at Stead at Nominal V _c	y State at 25°C, at Nom	inal V _{DD} , 30mA Max	30mA Maximum			
Frequency Stability	vs. Initial Frequen	cy Tolerance	±2.5ppm (± 2.5 ppm (at Nominal V_{DD} and V_{C} , at 25 °C)			
	vs. Operating Temp	oerature Range	See Table 1	See Table 1 (at Nominal V_{DD} and V_{C})			
	vs. Input Voltage (V _{DD} ±5%)	±0.3ppm M	±0.3ppm Maximum			
	vs. Load (±10%)		±0.2ppm M	±0.2ppm Maximum			
ging (at 25°C)			±1ppm/y	±1ppm/year Maximum			
utput Voltage Logic High (V _{OH})			$V_{DD} - 0.5V_{D}$	V _{DD} - 0.5V _{DC} Minimum			
Output Voltage Logic Low (V _{OL})	rtput Voltage Logic Low (V _{OL})			0.5V _{DC} Maximum			
Rise Time / Fall Time	20% to 80% of Wa	veform	6 nSecond	6 nSeconds Maximum			
Duty Cycle	at 50% of Wavefor	m	50 ±5(%)	50 ±5(%)			
Load Drive Capability	Drive Capability			30pF HCMOS Load Maximum			
Control Voltage Range	$0.0V_{DC}$ to V	$0.0V_{DC}$ to V_{DD}					
Control Voltage (External)	Positive Transfer C	haracteristic	2.5V _{DC} ±2.0	2.5V _{DC} ±2.0V _{DC}			
Frequency Deviation	Referenced to F_0 at $V_C = 2.5V_{DC}$, $V_{DD} = 5.0V_{DC}$		±7ppm Mir	±7ppm Minimum, ±20ppm Maximum			
Linearity ±10% Maximum							
Input Impedance 10kOhms Typical							
Phase Noise (at 19.440MHz)	Measured at 25°C	Measured at 25° C, at Nominal V_{DD} , at Nominal V_{C}					
	at 10Hz Offset		-70dBc/Hz	-70dBc/Hz Typical			
at 100Hz Offset -100dBc/Hz Typical at 1kHz Offset -130dBc/Hz Typical				Iz Typical			
				Iz Typical			
	at 10kHz Offset	10kHz Offset		-140dBc/Hz Typical			
	at 100kHz Offset		-145dBc/H	-145dBc/Hz Typical			
MANUFACTURER CATEGORY ECLIPTEK CORP. OSCILLATOR	SERIES EB51F4	PACKAGE 14-PIN DIP	VOLTAGE 5.0V	class OS2W	REV = DA1		

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PART NUMBERING GUIDE

EB51F4 D 15 A V - 12.800M - G



One Letter Code Per Table 1

AVAILABLE OPTIONS

Blank=None (Standard) CB=Cut Leads to 2.540 ±0.500 (0.100" ±0.020") CC=Cut Leads to 3.175 ±0.500 (0.125" ±0.020") CD=Cut Leads to 3.810 ±0.500 (0.150" ±0.020") CE=Cut Leads to 4.445 ±0.500 (0.175" ±0.020") G=Full Size Gull Wing

FREQUENCY

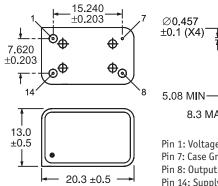
EXTERNAL TRIM

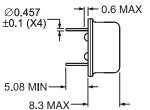
N=None (No Connection on Pin 1) V=Voltage Control on Pin 1

	TABLE 1: PART NUMBERING CODES										
Range			Frequency Stability X = Available from 1.544MHz to 32.768MHz Y = Available at any Frequency								
E R			±1.5ppm	±2.0ppm	±3.0ppm	±5.0ppm					
Operating Temperature		Code	15	20	30	50					
	0°C to +50°C	А	Υ	Y	Υ	Y					
	0°C to 70°C	В	Х	Y	Υ	Y					
	-20°C to +70°C	С	Х	Х	Υ	Υ					
	-30°C to +75°C	D		Х	Υ	Υ					
	-40°C to +85°C	Е			Х	Υ					

MECHANICAL DIMENSIONS

ALL DIMENSIONS IN MILLIMETERS





Pin 1: Voltage Control or No Connect

Pin 7: Case Ground

Pin 14: Supply Voltage

MARKING SPECIFICATIONS

Line 1: ECLIPTEK

Line 2: XX.XXX M M=MHz

Frequency (5 Digits Maximum + Decimal)

Line 3: XX Y ZZ Week of Year Last Digit of Year Ecliptek Manufacturing Identifier

Note: Pin 1 shall be designated with a dot

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

Characteristic **Specification**

MIL-STD-883, Method 1014, Condition A Fine Leak Test **Gross Leak Test** MIL-STD-883, Method 1014, Condition C MIL-STD-202, Method 213, Condition C Mechanical Shock MIL-STD-883, Method 2007, Condition A Vibration MIL-STD-883, Method 2004 Lead Integrity

Solderability MIL-STD-883, Method 2002 Temperature Cycling Resistance to Soldering Heat MIL-STD-883, Method 1010 MIL-STD-883, Method 210 Resistance to Solvents MIL-STD-883, Method 215

MANUFACTURER PACKAGE SERIES VOLTAGE CLASS ECLIPTEK CORP. OSCILLATOR 14 pin DIP EB51F4 5.0V 0S2W 02/05