





(0,50mm) .0197" **QTH SERIES** 

## HIGH SPEED GROUND PLANE HEADER

## **SPECIFICATIONS**

For complete specifications and recommended PCB layouts see www.samtec.com?QTI

Insulator Material: Liquid Crystal Polymer Terminal Material: Phosphor Bronze

Plating: Au or Sn over 50μ" (1,27μm) Ni Current Rating: Contact: 1.0A @ 30°C Temperature Rise Ground Plane: 7.8A @ 30°C Temperature Rise

(SP

Operating Temp Range: -55°C to +125°C Voltage Rating:

125 VAC (5mm Stack Height) Max Cycles:

Unmating Force (-RT1 option): -RT1 option increases unmating force up to 50% **RoHS Compliant:** 

## **Processing:**

Lead-Free Solderable:

SMT Lead Coplanarity: (0,10mm) .004" max (030-060) (0,15mm) .006" max (090-120) Board Stacking: For applications requiring

more than two connectors per board or 4 banks or more, contact ipg@samtec.com

## **APPLICATION** SPECIFIC OPTION

- 14mm, 15mm, 22mm and 30mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
- 30µ" (0,76µm) Gold (Specify -H plating for Data Rate cable mating applications.)
- Edge Mount & Guide Posts
- 150 positions per row Call Samtec.

\*Note: -C Plating passes 10 year MFG testing

Note: Some lengths, styles and options are non-standard, non-returnable.



QTH

**Cable Mates:** HFHM2, HQCD, HQDP (See Application Specific note)



QTH/QSH	Туре	Rated @ 3dB Insertion Loss	
5mm Stack Height		with PCB effects*	w/o PCB effects**
Single-Ended Signaling	-D	9 GHz / 18 Gbps	11 GHz / 22 Gbps
Differential Pair Signaling	-D	8 GHz / 16 Gbps	10.5 GHz / 21 Gbps
Differential Pair Signaling	-DP	9.5 GHz / 19 Gbps	

\*Performance data includes effects of a non-optimized PCB. Test board losses de-embedded from performance data

Performance data for other stack heights and complete test data available at www.samtec.com?QTH or contact sig@samtec.com

PINS PER ROW

NO. OF PAIRS

Standard Stack Heights Integral metal plane for from 5mm to 25mm power or ground Break Out Region erformance Certified TM Hypertransport™ XAUI PCI Express® Polarized SATA InfiniBand™ Download app notes at www.samtec.com/appnote **ALSO** Contact SIG @ samtec.com **AVAILABLE** 

> **LEAD** STYLE

**PLATING** OPTION

Board Spacing Standoffs.

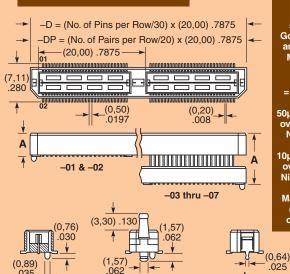
See SO Series.

**OTHER OPTION** 

for questions on protocols

030, -060, -090, -120 (60 total pins per bank = -D)

020, -040, -060, -080 (20 pairs per bank = -D-DP)



DIA -RT1

Specify **LEAD** STYLE from chart

= Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails

= 10µ" (0,25µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

= Electro-Polished Selective 50μ" (1,27μm) min Au over 150μ" (3,81μm) Ni on Signal Pins in contact area, 10μ" (0,25μm) min Au over 50μ" (1,27μm) Ni on Ground Plane in contact area, Matte Tin over 50µ" (1,27µm) min Ni on all solder tails

–D = Single-Ended -D-DP Differential Pair (-01 only)

HEIGHT

WITH QSH\*

(5,00) .197

8

-K = (7,00mm) .275" DIA Polyimide film Pick & Place Pad (N/A with -05 & 07 lead style)

-TR = Tape & Reel -090 positions maximum)

-RT1

= Retention Option (-01 lead style only & -090 positions maximum)

= Latching Option (-01 lead style only) (N/A on -060 (-D-DP) -080, -090 & -120 or

-RT1 option)

(7,26) .286 (8,00) .315 (11,00) 433 10,27 -03404 15,25) (16,00)-04.600 .630 (18,26) .718 (19,00) .748 -05(24,24) (25,00)-07 954 \*Processing conditions will affect mated height.

(4,27) .168

LEAD

STYLE

-01

-02

Due to technical progress, all designs, specifications and components are subject to change without notice.

WWW.SAMTEC.COM