

## **ELECTRICAL SPECIFICATIONS:**

SHIELD

2.0 INDUCTANCE:

(P4-P5) (P3-P2)

0.3 MAX. 0.3 MAX.

**@** @

1MHz 1MHz

350uH MIN. @ 0.1V, 350uH MIN. @ 0.1V,

100KHz, 8mA DC 100KHz, 8mA DC

Bias Bias

**ROHS** 2002/95/EC

 $1CT : 1CT \pm 3\%$  $1CT : 1CT \pm 3\%$ 

2.0 UNUSED PINS ARE OMITTED

HAZARDOUS

ELECTRONIC PRODUCTS

DIRECTIVE 2002/95/EC, CALLING FOR

THE ELIMINATION OF LEAD AND OTHER

SUBSTANCES FROM

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) (P3,P2,P1) TO (J2,J1)

5.0

DC RESISTANCE: (J6-J3)=(J2-J1)

30pf MAX 30pf MAX. ( @ 1MHz ( @ 1MHZ

1.35 ohms Max.

Bel Stewart Connector

http://www.stewartconnector.com

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DRAWING NO. SI-50220-F

6.0 RETURN LOSS: (P4-P5)=100 OHMS AND (P2-P3)=100 OHM REF.

1MHz TO 30MHz 60MHz TO 80MHz 18dB MIN. 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J3-J6).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P2, P3) (J3, J6) TO (P4, P5) XX AC

1500 1500

8.0 INSERTION LOSS: RS=RL=100 ohms 100KHz TO 100MHz

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS OUTPUT VOLTAGE = 1 V peak PULSE WIDTH= 112nS

10.0 CROSS TALK: 1-100 MHz

3.0 nS 3.0 nS MAX X

1.1 dB TYP

6

dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz 35dB TYP

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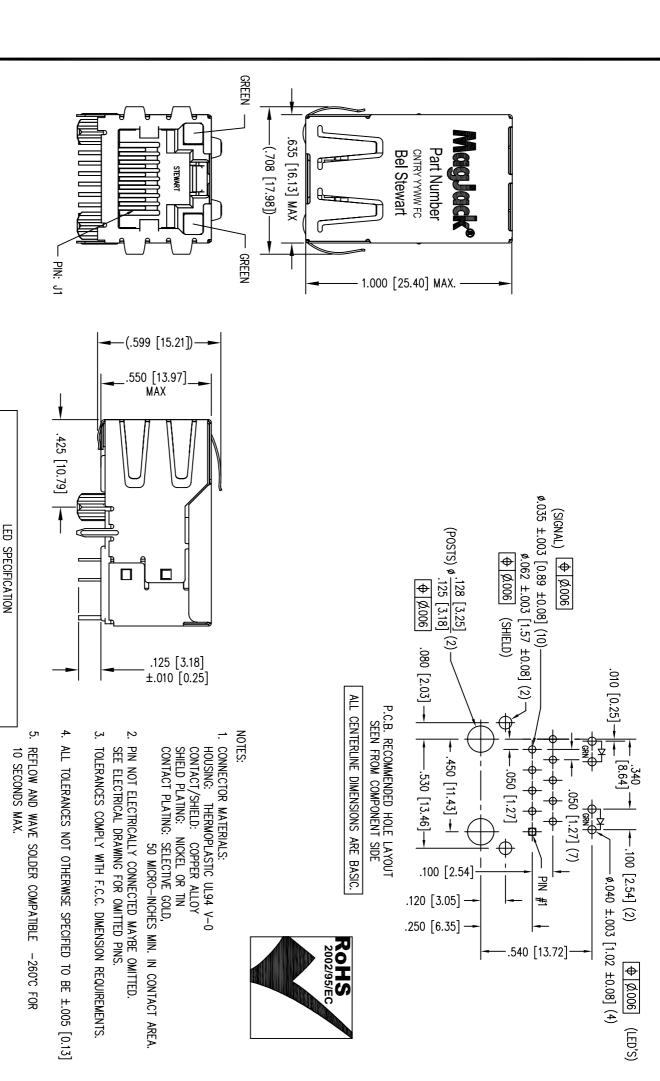
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DRAWING NO.

SI-50220-F

| REV. () 1

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\*WITH A FORWARD CURRENT OF 20 mA (TYP)

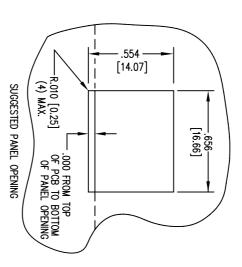
565 nm

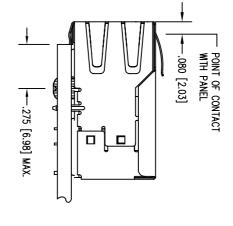
2.5 V

STANDARD LED

WAVELENGTH | FORWARD V (MAX)

\* (TYP)





- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES CAPABILITY. YET MAINTAIN RELIABLE GROUNDING / PANEL CLEARANCES
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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