

Silicon PNP Power Transistors

2SB707 2SB708

DESCRIPTION

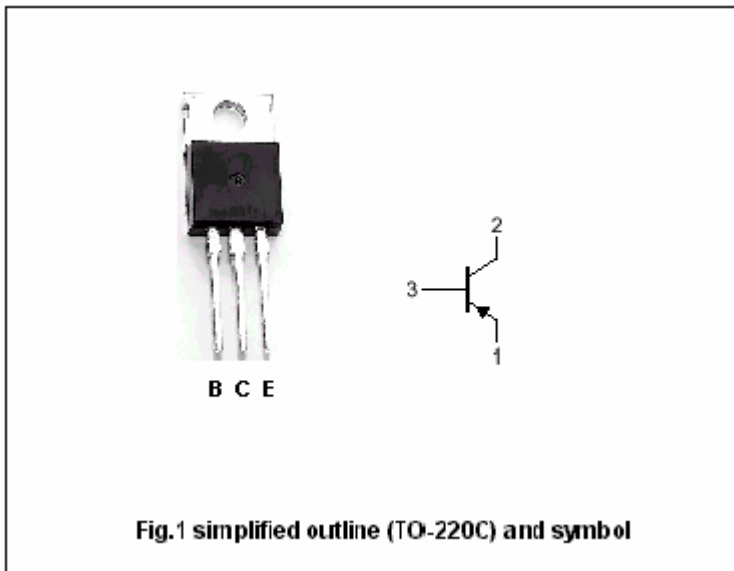
- With TO-220C package
- Complement to type 2SD568/569

APPLICATIONS

- For low frequency power amplifier
- low speed switching industrial use

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-80	V
V _{CEO}	Collector-emitter voltage	2SB707	-60	V
		2SB708	-80	
V _{EBO}	Emitter-base voltage	Open collector	-7	V
I _C	Collector current		-7	A
I _{CM}	Collector current-peak		-15	A
I _B	Base current		-3.5	A
P _C	Collector power dissipation	T _a =25°C	1.5	W
		T _C =25°C	40	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA; I _B =0	-60			V
			-80			
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5A; I _B =-0.5 A			-0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-5A; I _B =-0.5 A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-60V; I _E =0			-10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-10	μA
h _{FE-1}	DC current gain	I _C =-3A ; V _{CE} =-1V	40		200	
h _{FE-2}	DC current gain	I _C =-5A ; V _{CE} =-1V	20			

◆ h_{FE-2} classifications

R	O	Y
40-80	60-120	100-200

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance:±0.10 mm)