

PBHV8115T

150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

Rev. 02 — 9 December 2008

Product data sheet

1. Product profile

1.1 General description

NPN high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9115T.

1.2 Features

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- AEC-Q101 qualified
- Small SMD plastic package

1.3 Applications

- LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

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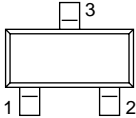
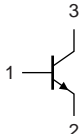
1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	150	V
I_C	collector current		-	-	1	A
h_{FE}	DC current gain	$V_{CE} = 10\text{ V};$ $I_C = 50\text{ mA}$	100	250	-	

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter		
3	collector		

sym021

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBHV8115T	-	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PBHV8115T	W6*

- [1] * = -: made in Hong Kong
 * = p: made in Hong Kong
 * = t: made in Malaysia
 * = W: made in China

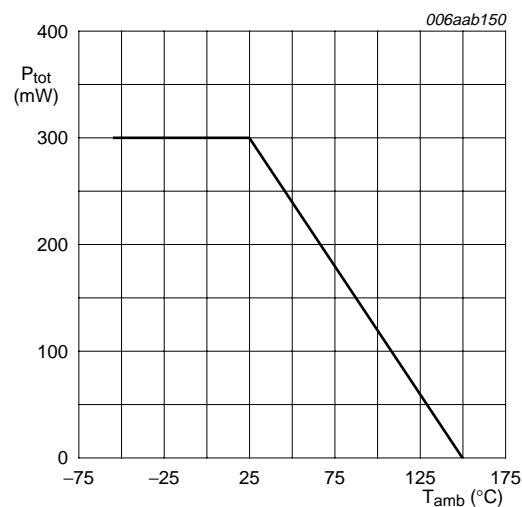
5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	400	V
V_{CEO}	collector-emitter voltage	open base	-	150	V
V_{EBO}	emitter-base voltage	open collector	-	6	V
I_C	collector current		-	1	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	2	A
I_{BM}	peak base current	single pulse; $t_p \leq 1$ ms	-	400	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	300	mW
T_j	junction temperature		-	150	°C
T_{amb}	ambient temperature		-55	+150	°C
T_{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



FR4 PCB, standard footprint

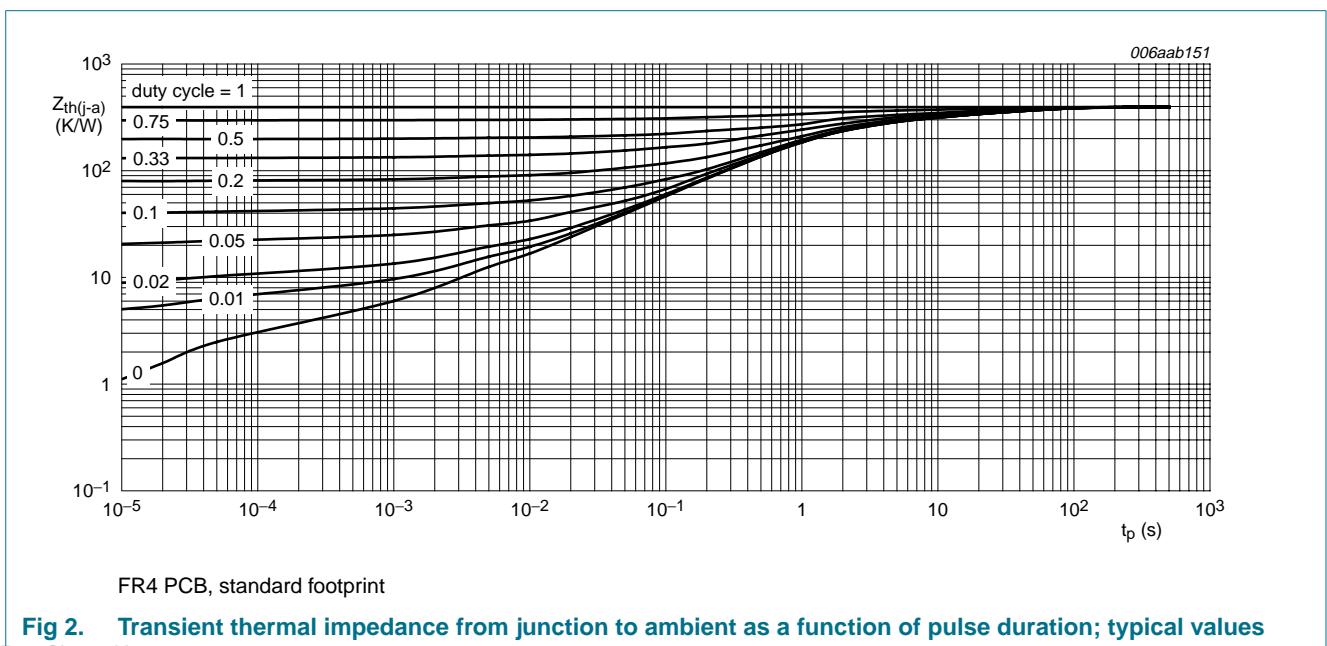
Fig 1. Power derating curve

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	417	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	70	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



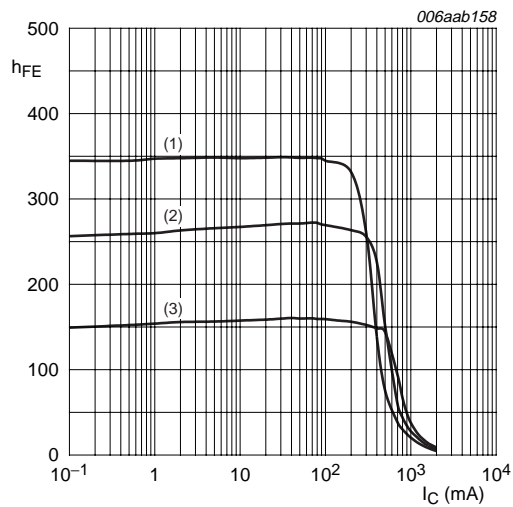
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7. Characteristics

Table 7. Characteristics*T_{amb} = 25 °C unless otherwise specified.*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 120 V; I _E = 0 A	-	-	100	nA
		V _{CB} = 120 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 120 V; V _{BE} = 0 V	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V				
		I _C = 50 mA	100	250	-	
		I _C = 100 mA	100	250	-	
		I _C = 0.5 A	[1] 50	160	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 10 mA	-	40	60	mV
		I _C = 100 mA; I _B = 20 mA	-	33	50	mV
		I _C = 1 A; I _B = 200 mA	[1] -	225	350	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 1 A; I _B = 200 mA	[1] -	1.1	1.2	V
f _T	transition frequency	V _{CE} = 10 V; I _E = 10 mA; f = 100 MHz	-	30	-	MHz
C _c	collector capacitance	V _{CB} = 20 V; I _E = i _e = 0 A; f = 1 MHz	-	5.7	-	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = i _c = 0 A; f = 1 MHz	-	150	-	pF
t _d	delay time	V _{CC} = 6 V; I _C = 0.5 A;	-	7	-	ns
t _r	rise time	I _{Bon} = 0.1 A; I _{Boff} = -0.1 A	-	565	-	ns
t _{on}	turn-on time		-	572	-	ns
t _s	storage time		-	1530	-	ns
t _f	fall time		-	700	-	ns
t _{off}	turn-off time		-	2230	-	ns

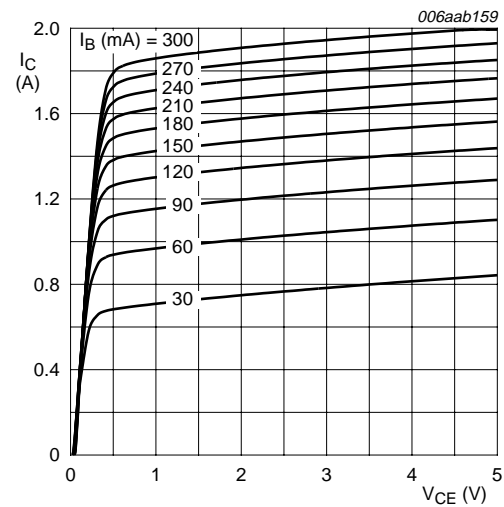
[1] Pulse test: t_p ≤ 300 μs; δ ≤ 0.02.



$V_{CE} = 10 \text{ V}$

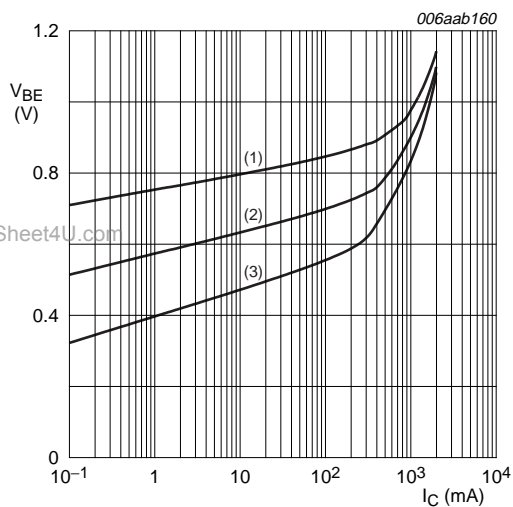
- (1) $T_{amb} = 100 \text{ }^\circ\text{C}$
- (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
- (3) $T_{amb} = -55 \text{ }^\circ\text{C}$

Fig 3. DC current gain as a function of collector current; typical values



$T_{amb} = 25 \text{ }^\circ\text{C}$

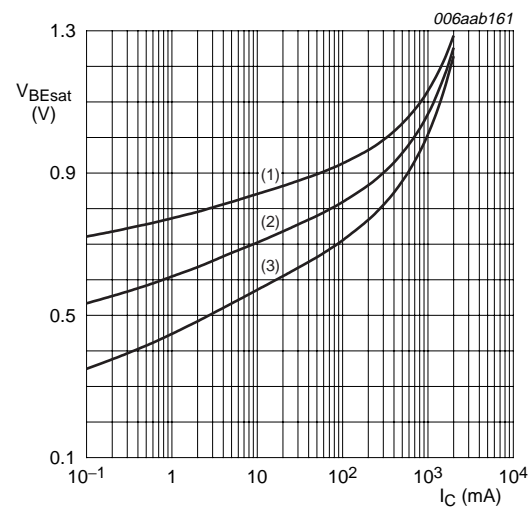
Fig 4. Collector current as a function of collector-emitter voltage; typical values



$V_{CE} = 10 \text{ V}$

- (1) $T_{amb} = -55 \text{ }^\circ\text{C}$
- (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
- (3) $T_{amb} = 100 \text{ }^\circ\text{C}$

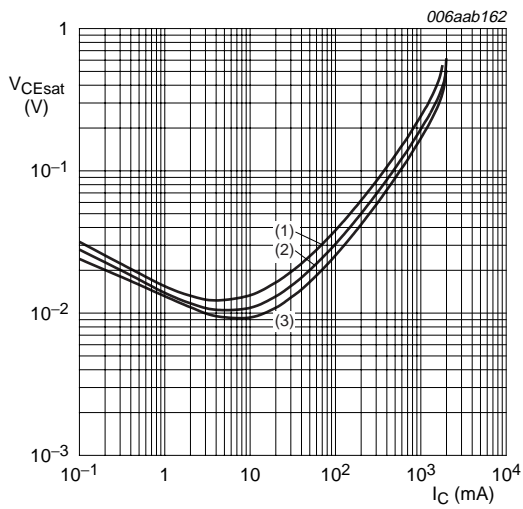
Fig 5. Base-emitter voltage as a function of collector current; typical values



$I_C/I_B = 5$

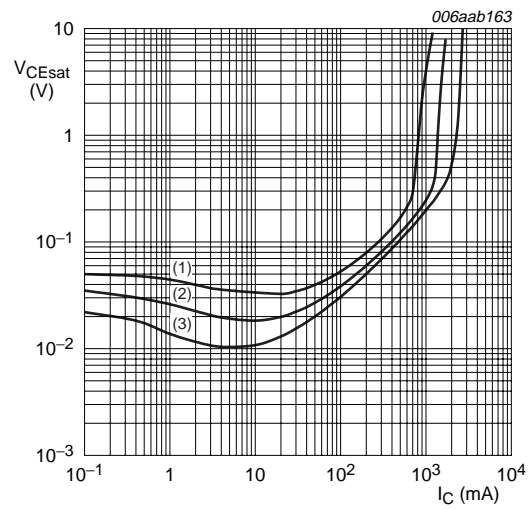
- (1) $T_{amb} = -55 \text{ }^\circ\text{C}$
- (2) $T_{amb} = 25 \text{ }^\circ\text{C}$
- (3) $T_{amb} = 100 \text{ }^\circ\text{C}$

Fig 6. Base-emitter saturation voltage as a function of collector current; typical values



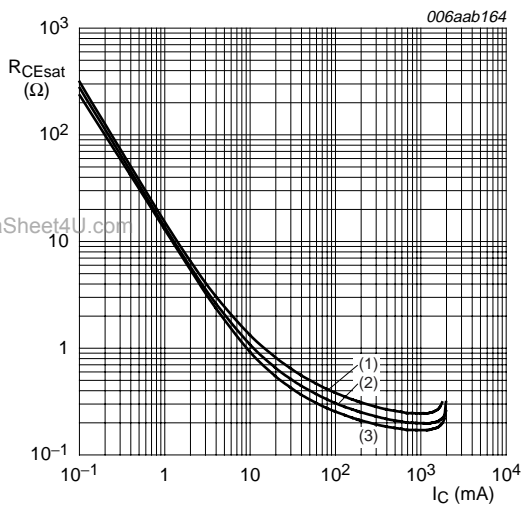
- $I_C/I_B = 5$
- (1) $T_{amb} = 100\text{ °C}$
 - (2) $T_{amb} = 25\text{ °C}$
 - (3) $T_{amb} = -55\text{ °C}$

Fig 7. Collector-emitter saturation voltage as a function of collector current; typical values



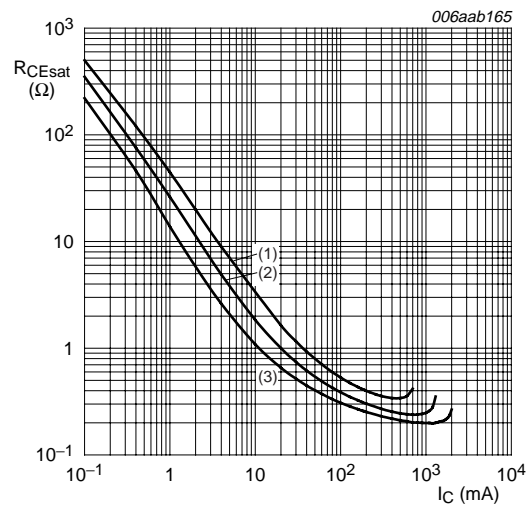
- $T_{amb} = 25\text{ °C}$
- (1) $I_C/I_B = 20$
 - (2) $I_C/I_B = 10$
 - (3) $I_C/I_B = 5$

Fig 8. Collector-emitter saturation voltage as a function of collector current; typical values



- $I_C/I_B = 5$
- (1) $T_{amb} = 100\text{ °C}$
 - (2) $T_{amb} = 25\text{ °C}$
 - (3) $T_{amb} = -55\text{ °C}$

Fig 9. Collector-emitter saturation resistance as a function of collector current; typical values



- $T_{amb} = 25\text{ °C}$
- (1) $I_C/I_B = 20$
 - (2) $I_C/I_B = 10$
 - (3) $I_C/I_B = 5$

Fig 10. Collector-emitter saturation resistance as a function of collector current; typical values

8. Test information

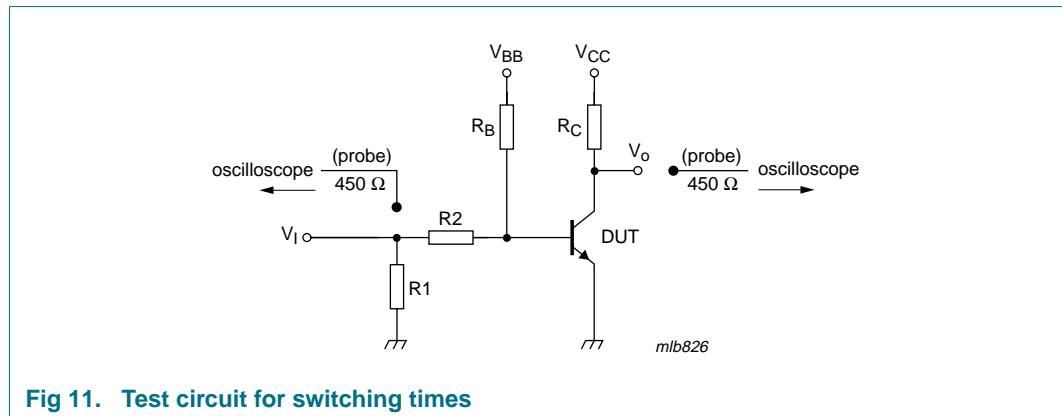


Fig 11. Test circuit for switching times

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline

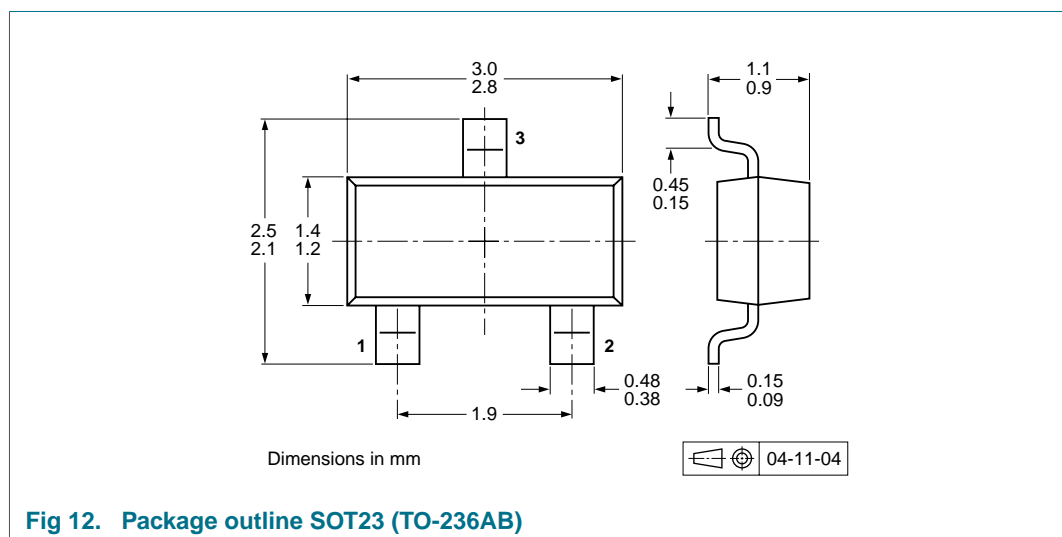


Fig 12. Package outline SOT23 (TO-236AB)

10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.^[1]

Type number	Package	Description	Packing quantity	
			3000	10000
PBHV8115T	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235

[1] For further information and the availability of packing methods, see [Section 14](#).

11. Soldering

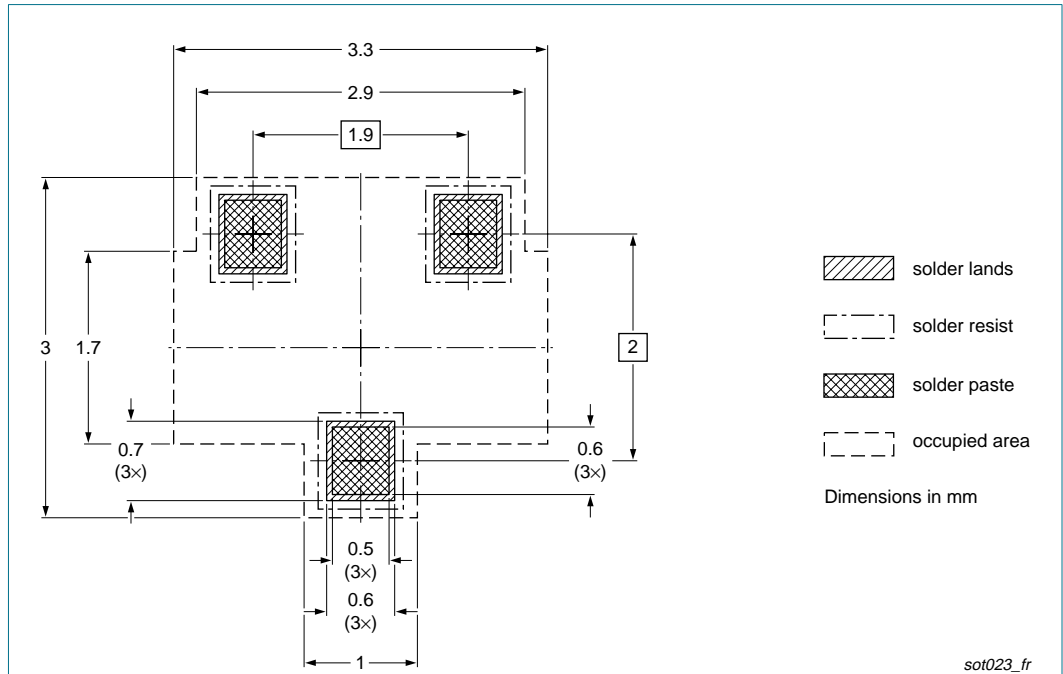


Fig 13. Reflow soldering footprint SOT23 (TO-236AB)

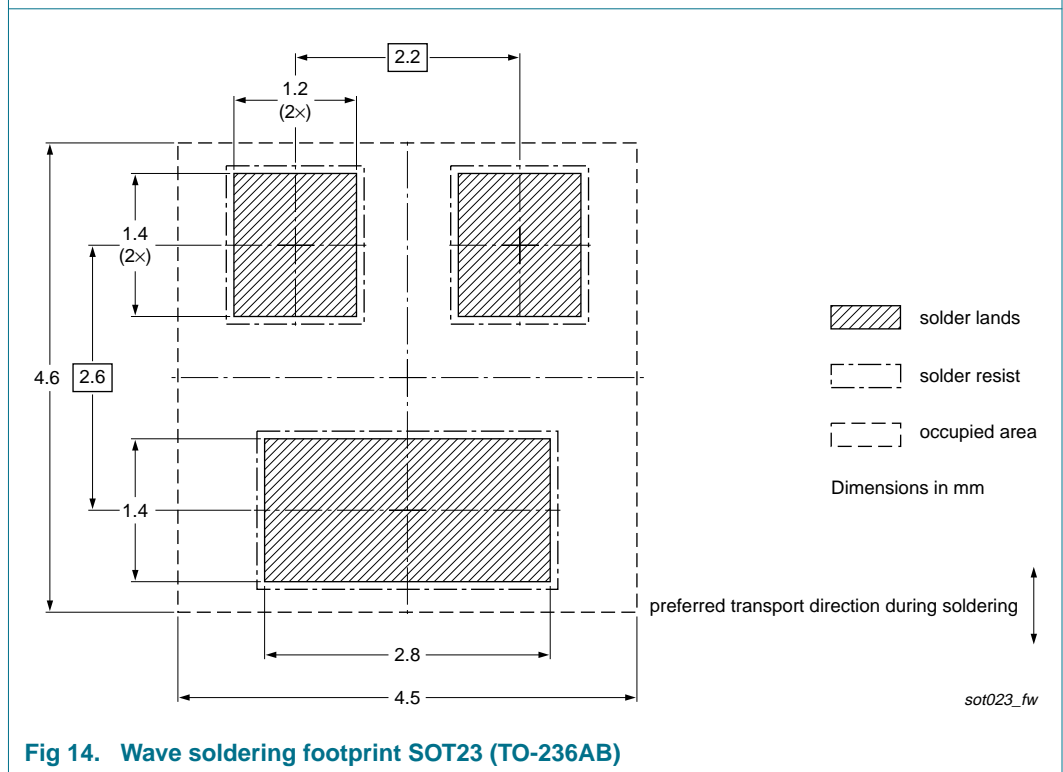


Fig 14. Wave soldering footprint SOT23 (TO-236AB)

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12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PBHV8115T_2	20081209	Product data sheet	-	PBHV8115T_1
Modifications:	<ul style="list-style-type: none"> • Table 5: I_{BM} maximum value changed from 100 mA to 400 mA • Figure 4: amended • Section 13 "Legal information": updated 			
PBHV8115T_1	20080204	Product data sheet	-	-

13. Legal information

13.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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