

GI80LS02

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	25V
RDS(ON)	8mΩ
ID	75A

Description

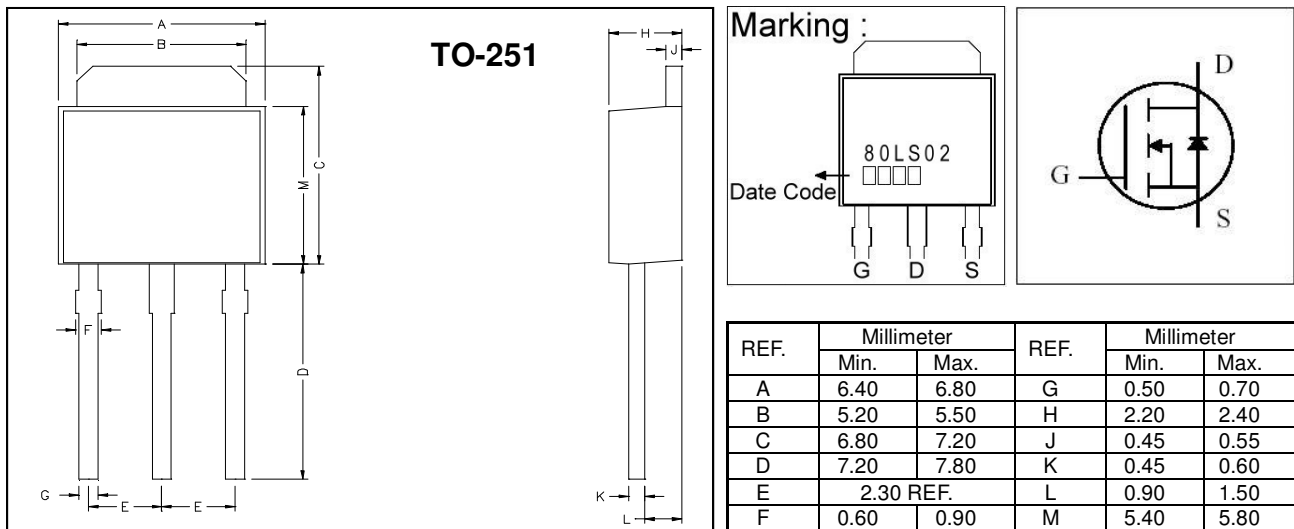
The GI80LS02 used advanced design and process to achieve low gate charge, low on-resistance and fast switching performance.

The through-hole version (TO-251) is available for low-profile applications and suited for low voltage applications such as DC/DC converters.

Features

- *Low On-resistance
- *Simple Drive Requirement
- *Fast Switching Speed

Package Dimensions



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	25	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=25^{\circ}C$	75	A
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=100^{\circ}C$	52	A
Pulsed Drain Current ¹	I_{DM}	250	A
Total Power Dissipation	$P_D @T_C=25^{\circ}C$	96	W
Linear Derating Factor		0.75	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-case Max.	$R_{thj-case}$	1.4	°C/W
Thermal Resistance Junction-ambient Max.	$R_{thj-amb}$	110	°C/W

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	25	-	-	V	$V_{GS}=0, I_D=250\mu A$
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.03	-	V/°C	Reference to 25°C, $I_D=1mA$
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	3.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Forward Transconductance	g_{fs}	-	20	-	S	$V_{DS}=10V, I_D=30A$
Gate-Source Leakage Current	I_{GSS}	-	-	±100	nA	$V_{GS}= \pm 20V$
Drain-Source Leakage Current(T _j =25°C)	I_{DSS}	-	-	1	uA	$V_{DS}=25V, V_{GS}=0$
Drain-Source Leakage Current(T _j =150°C)		-	-	100	uA	$V_{DS}=20V, V_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	-	-	8	mΩ	$V_{GS}=10V, I_D=40A$
		-	-	12		$V_{GS}=4.5V, I_D=30A$
Total Gate Charge ²	Q_g	-	19	30	nC	$I_D=30A$ $V_{DS}=20V$ $V_{GS}=4.5V$
Gate-Source Charge	Q_{gs}	-	3	-		
Gate-Drain ("Miller") Charge	Q_{gd}	-	12	-		
Turn-on Delay Time ²	$T_{d(on)}$	-	9	-	ns	$V_{DS}=15V$ $I_D=30A$ $V_{GS}=10V$ $R_G=3.3\Omega$ $R_D=0.5\Omega$
Rise Time	T_r	-	90	-		
Turn-off Delay Time	$T_{d(off)}$	-	25	-		
Fall Time	T_f	-	97	-		
Input Capacitance	C_{iss}	-	1290	2070	pF	$V_{GS}=0V$ $V_{DS}=25V$ $f=1.0MHz$
Output Capacitance	C_{oss}	-	630	-		
Reverse Transfer Capacitance	C_{rss}	-	205	-		
Gate Resistance	R_g	-	1.4	-	Ω	$f=1.0MHz$

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V_{SD}	-	-	1.3	V	$I_S=40A, V_{GS}=0V$
Reverse Recovery Time ²	T_{rr}	-	30	-	ns	$I_S=30A, V_{GS}=0V$ $di/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	-	22	-	nC	

Notes: 1. Pulse width limited by safe operating area.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

Characteristics Curve

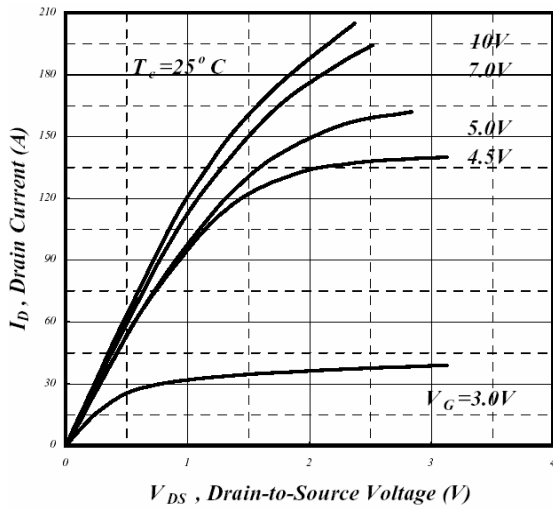


Fig 1. Typical Output Characteristics

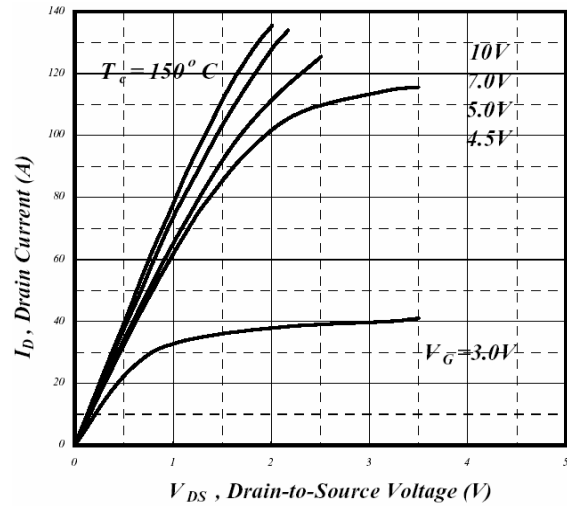


Fig 2. Typical Output Characteristics

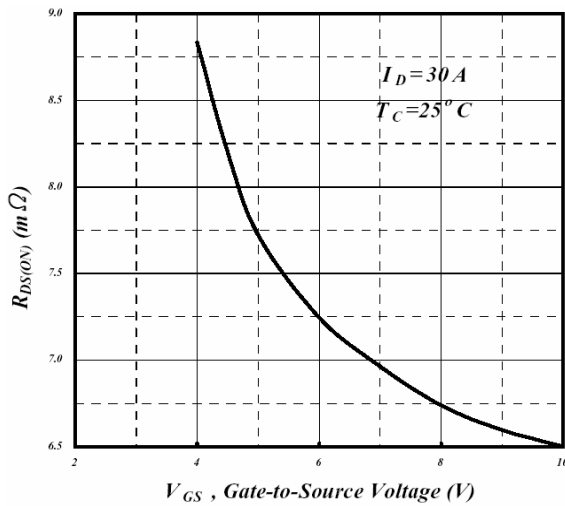


Fig 3. On-Resistance v.s. Gate Voltage

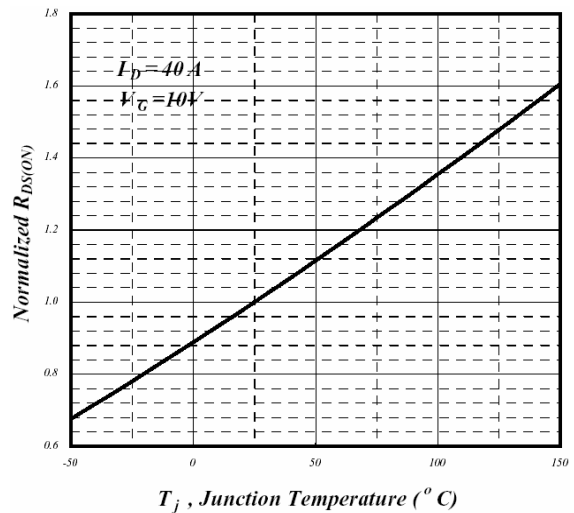


Fig 4. Normalized On-Resistance v.s. Junction Temperature

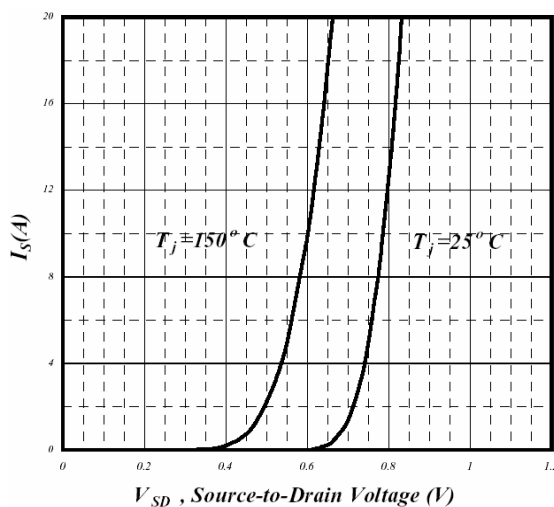


Fig 5. Forward Characteristics of Reverse Diode

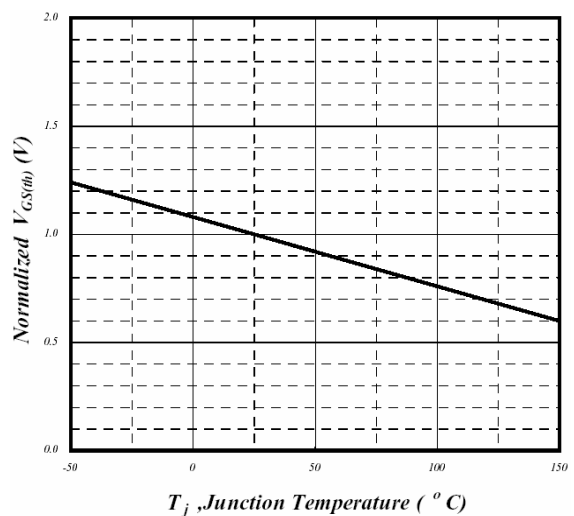


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

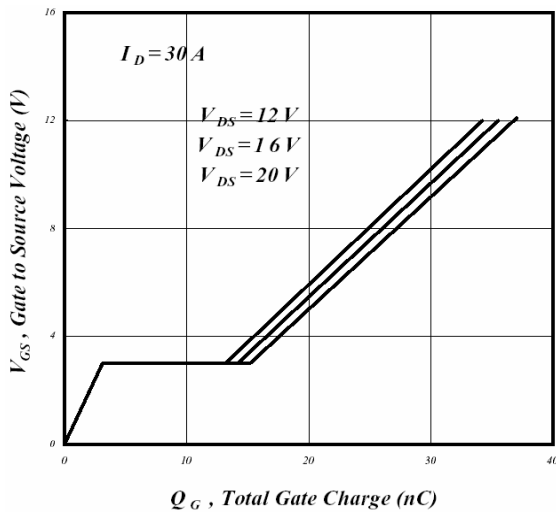


Fig 7. Gate Charge Characteristics

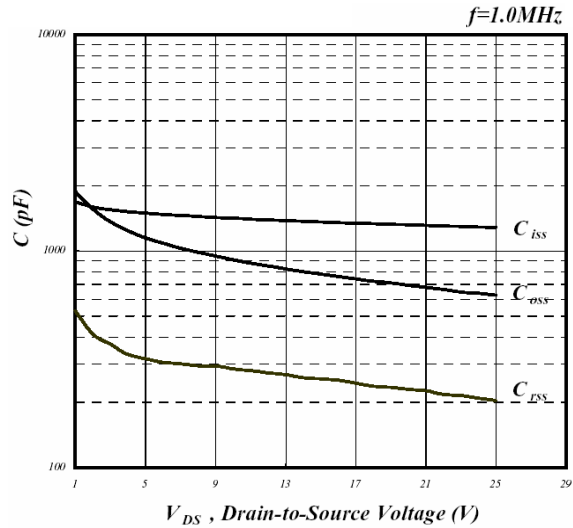


Fig 8. Typical Capacitance Characteristics

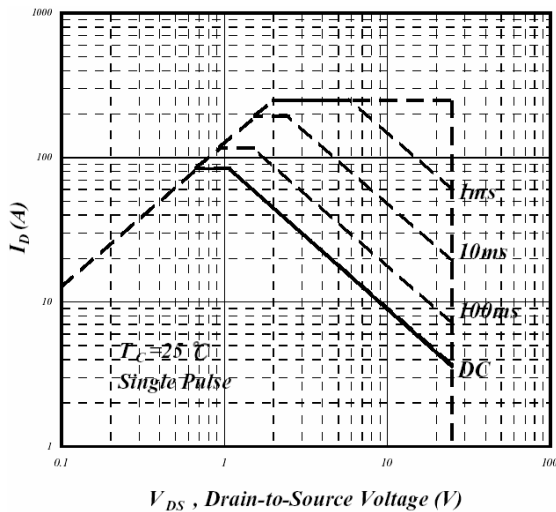


Fig 9. Maximum Safe Operating Area

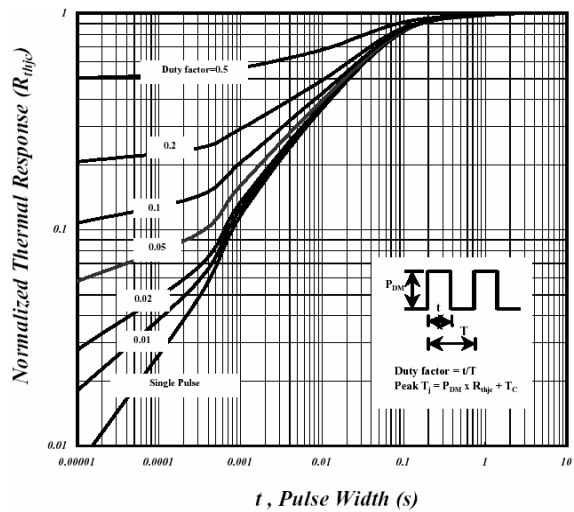


Fig 10. Effective Transient Thermal Impedance

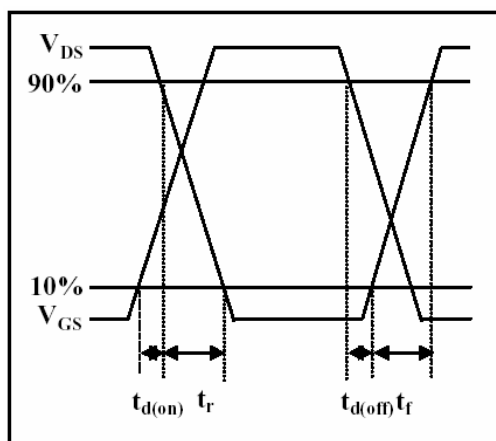


Fig 11. Switching Time Waveform

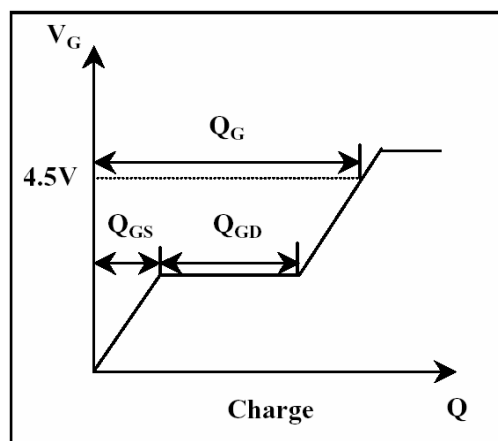


Fig 12. Gate Charge Waveform

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