

# 2SA1980E

**PNP Silicon Transistor** 

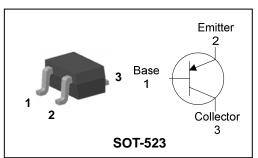
#### **Description**

• General small signal amplifier

#### **Features**

- Low collector saturation voltage :  $V_{CE(sat)} = -0.3V(Max.)$
- Low output capacitance : C<sub>ob</sub>=4pF(Typ.)
- Complementary pair with 2SC5343E

### **PIN Connection**



**Ordering Information** 

Type NO.	Marking	Package Code	
2SA1980E	<u>A</u> <u> </u> <u> </u> <u> </u> <u> </u> 3	SOT-523	

①Device Code ②hFE Rank ③Year&Week Code

## **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	-50	V
Collector-Emitter voltage	$V_{CEO}$	-50	V
Emitter-Base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-150	mA
Collector dissipation	P <sub>C</sub>	150	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

### **Electrical Characteristics**

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = -100 \mu A, I_E = 0$	-50	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=-1$ mA, $I_B=0$	-50	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = -10 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50V, I_{E} = 0$	-	-	-0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -5V, I_{C} = 0$	-	-	-0.1	μΑ
DC current gain	h <sub>FE</sub> *	$V_{CE}$ =-6V, $I_{C}$ =-2mA	70	-	700	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-100mA, I <sub>B</sub> =-10mA	-	-	-0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ =-10V, $I_{C}$ =-1mA	80	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz	-	4	7	рF
Noise figure	NF	$V_{CE}$ =-6V, $I_{C}$ =-0.1mA f=1KHz, $Rg$ =10K $\Omega$	-	-	10	dB

<sup>\*:</sup> h<sub>FE</sub> rank / O : 70~140, Y : 120~240, G : 200~400, L : 300~700

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### **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub>-T<sub>a</sub>

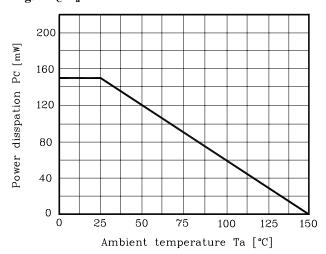


Fig. 3  $I_{C}$ - $V_{CE}$ 

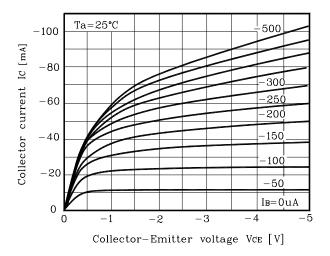


Fig. 5  $V_{CE(sat)}$ - $I_C$ 

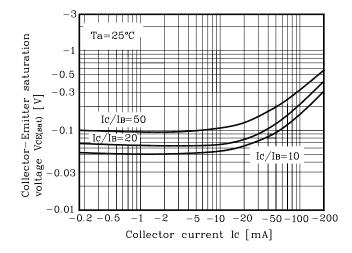


Fig. 2  $I_{\text{C-}}V_{\text{BE}}$ 

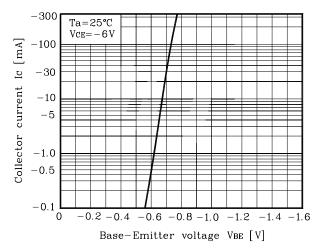
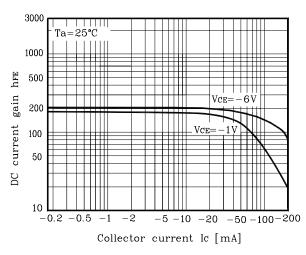
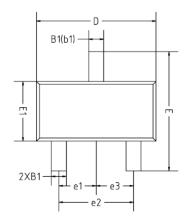


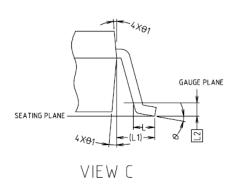
Fig. 4 h<sub>FE</sub>-I<sub>C</sub>

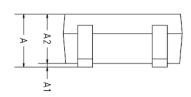


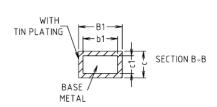
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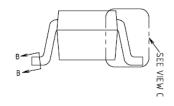
# **Outline Dimension**







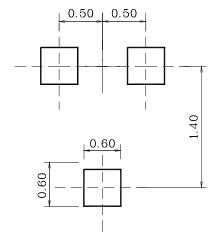




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	MILLIMETERS			NOTE	
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE	
Α	_	_	0.80		
A1	0.00	_	0.10		
A2	0.65	0.70	0.75		
B1	0.19	_	0.24		
b1	0.17	_	0.21		
С	0.13	_	0.15		
c1	0.10	_	0.12		
D	1.48	1.58	1.68		
E E1	1.50	1.60	1.70		
E1	0.66	0.76	0.86		
e1		0.50 BSC			
e2	1.00 BSC				
e3	0.50 BSC				
L	0.15	0.205	0.30		
L1	0.40 REF				
L2		0.15 BSC			
θ	0,	_	8,		
<del>0</del> 1	4.	_	10		

#### \*Recommend PCB solder land [Unit: mm]



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