ECLIPTEK CORPORATION

EB71F72

EB71F72 Series

- Oven Controlled Crystal Oscillator (OCXO)
- SC-Cut Crystal
- HCMOS output
- 5.0V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±20ppb

ELECTRICAL SPECIFICATIONS

oltage kage crol voltage	H 15.0 L 25.5 W 25.5
20ppb	OSCILLATOR
ATIONS	
10.000MHz, 12.288MHz, 12.800MHz, 16.000M	Hz, 19.440MHz, or 20.000MHz
(OTR)	0°C to 50°C, 0°C to 70°C, or -20°C to 70°C
	-55°C to 125°C
	5.0V _{DC} ±5%
ity	
at Nominal V_{DD} and V_c , at 25°C	±500ppb or ±300ppb Maximum
at Naminal V and V	00mmh 20mmh 50mmh 00mmh 100mmh

Frequency Ra	-	10.000MHz, 12.288MH	z, 12.800MHz, 16.000M)		
	perature Range (OTR))°C, 0°C to 70°C, or -20°C	to 70°C		
	erature Range			-55°C to				
Supply Voltag				5.0V _{DC} ±	5%			
	erance / Stability							
vs. Initial Tole		at Nominal V_{DD} and V_{CD}	-		±500ppb or ±300ppb Maximum			
vs. Temperatu	re Stability	at Nominal V_{DD} and V_{C}			. ±30ppb, ±50ppb, ±80p	pb, ±100ppb,		
					b, or ±280ppb Maximum			
vs.Vdd		$V_{DD} \pm 5\%$			Maximum			
vs. Load		Vload ±5%			Maximum			
vs. Aging (1 D	• /	after 72 Hours of Ope			Maximum			
vs. Aging (1 Ye	,	after 72 Hours of Ope			o Maximum			
vs. Aging (10)	Years)	after 72 Hours of Ope	ration		b Maximum			
Crystal Cut				SC-Cut				
Warm Up Time			quency at 1 Hour at 25°C		e Maximum			
Power Consumptionat Steady State, at 25°CDuring Warm Up, at 25°C				1.2 Watts Maximum				
				3.6 Watts Maximum				
	je Logic High (V _{он})	$I_{0H} = -8mA$			V _{DD} -0.5V _{DC} Minimum			
	Je Logic Low (V _{oL})	$I_{0L} = +8mA$		DC	0.5V _{DC} Maximum			
Rise Time / Fa	ll Time	Measured at 20% to 8			6nSec Maximum			
Duty Cycle		Measured at 50% of W	Vaveform		50 ±5(%)			
Load Drive Capability			1	15pF HCMOS Load Maximum				
Frequency DeviationReferenced to F_0 at $V_c = 2.5V_{DC}$; $V_{DD} = 5.0V_{DC}$ over OTR $\pm 0.5 ppm M_{DC}$								
Control Voltage Range 0.0V _{DC} to V _{DD}								
Control Voltage (V_c) $2.5V_{DC} \pm 2.5V_{DC}$								
Transfer Function Positive Transfer Characteristic								
Reference Vol	tage Output				0.3V _{DC} (Pin 4)			
Linearity				±10% M				
Input Impeda					s Typical			
Typical Phase Noise (at 12.800MHz) 1Hz Offset -90dBc/Hz								
10Hz Offset -100dBc/Hz								
100Hz Offset				-130dBc/Hz				
		1kHz Offset			-145dBc/Hz			
		10kHz Offset		-150dBc/Hz				
MANUFACTURER ECLIPTEK CORP.	CATEGORY OSCILLATOR	SERIES EB71F72	package 5 pin DIP	VOLTAGE 5.0V	CLASS OS2L	REV = DATE 05/07		
LULII ILK LUKP.	USCILLAIUK	LD/IF/2	o hii nte	5.00	US2L WWW.			

Specifications subject to change without notice.

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PART NUMBERING GUIDE

EB71F72 <u>D</u> <u>10</u> <u>B</u> <u>V</u> <u>2</u> - <u>20.000M</u>

INITIAL TOLERANCE

D=±500ppb E=±300ppb

FREQUENCY STABILITY 2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

- FREQUENCY

- DUTY CYCLE

2=50% ±5%

VOLTAGE CONTROL OPTION

V=Voltage Control on Pin 3 and Reference Voltage Output on Pin 4

	TABLE 1: PART NUMBERING CODES										
Bange			FREQUENCY STABILITY X Denotes availability								
ature			±20ppb	±30ppb	±50ppb	±80ppb	±100ppb	±200ppb	±280ppb		
Temperature		Code	02	03	05	08	10	20	28		
perating Ter	0°C to +50°C	А	Х	Х	Х	Х	Х	Х	Х		
	0°C to +70°C	В		Х	Х	Х	Х	Х	Х		
Ope	-20°C to +70°C	С			Х	Х	Х	Х	Х		

