



**CHENMKO ENTERPRISE CO.,LTD**

*Lead free devices*

**SURFACE MOUNT  
SCHOTTKY DIODE**

**VOLTAGE 30 Volts CURRENT 200 mAmpères**

**BAT54TCPT**

#### APPLICATION

- \* Ultra high speed switching

#### FEATURE

- \* Small surface mounting type. (SC-75/SOT-416)
- \* High speed. (TRR=2.5nSec Typ.)
- \* Suitable for high packing density.
- \* Maximum total power dissipation is 150mW.
- \* Peak forward current is 300mA.

#### CONSTRUCTION

- \* Silicon epitaxial planar

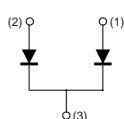
#### WEIGHT

- \* 0.002 grams ( Approx.)

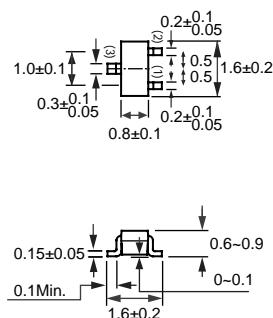
#### MARKING

- \* TU

#### CIRCUIT



**SC-75/SOT-416**



Dimensions in millimeters

**SC-75/SOT-416**

#### MAXIMUM RATINGES ( At TA = 25°C unless otherwise noted )

RATINGS	SYMBOL	BAT54TCPT	UNITS
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	30	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	21	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	30	Volts
Maximum Average Forward Rectified Current	I <sub>O</sub>	200	mAmps
Peak Forward Surge Current at 1Sec.	I <sub>FSM</sub>	600	mAmps
Typical Junction Capacitance between Terminal (Note 1)	C <sub>J</sub>	10	pF
Maximum Reverse Recovery Time (Note 2)	T <sub>RR</sub>	5.0	nSec
Maximum Operating Temperature Range	T <sub>J</sub>	+150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

#### ELECTRICAL CHARACTERISTICS ( At TA = 25°C unless otherwise noted )

CHARACTERISTICS	SYMBOL	BAT54TCPT	UNITS
Maximum Instantaneous Forward Voltage (@ I <sub>F</sub> = 0.1mA @ I <sub>F</sub> = 1.0mA @ I <sub>F</sub> = 10.0mA @ I <sub>F</sub> = 30.0mA @ I <sub>F</sub> = 100mA)	V <sub>F</sub>	240 320 400 500 1000	mVolts
Maximum Average Reverse Current at V <sub>R</sub> = 25V	I <sub>R</sub>	2.0	uAmps

NOTES : 1. Measured at 1.0 MHz and applied reverse voltage of 1.0 volts.  
2. Measured at applied froward current of 10mA and reverse current of 10mA.  
3. ESD sensitive product handling required.

2003-9

## RATING CHARACTERISTIC CURVES ( BAT54TCPT )

FIG. 1 - FORWARD CHARACTERISTICS

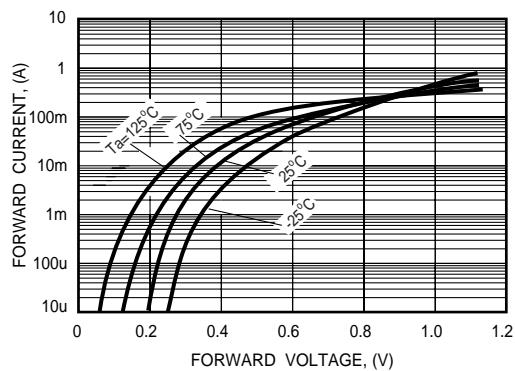


FIG. 2 - REVERSE CHARACTERISTICS

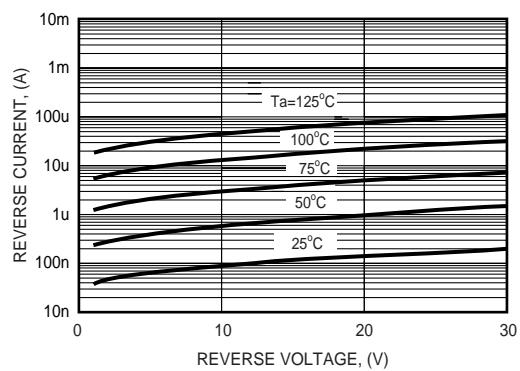


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

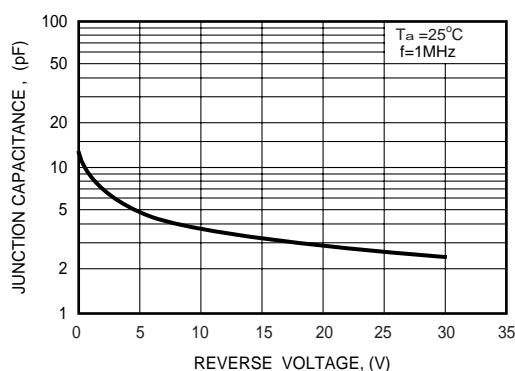


FIG. 4 - TYPICAL FORWARD CURRENT DERATING CURVE

