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SURFACE MOUNT TYPE AND REEL

LVY9033/TR2

DATA SHEET

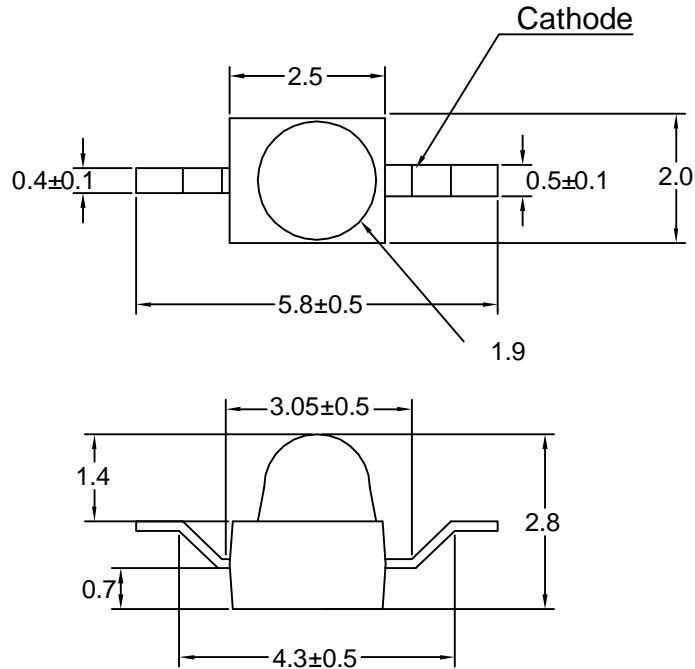
DOC. NO : QW0905-LVY9033/TR2

REV. : A

DATE : 09 - Jun - 2005

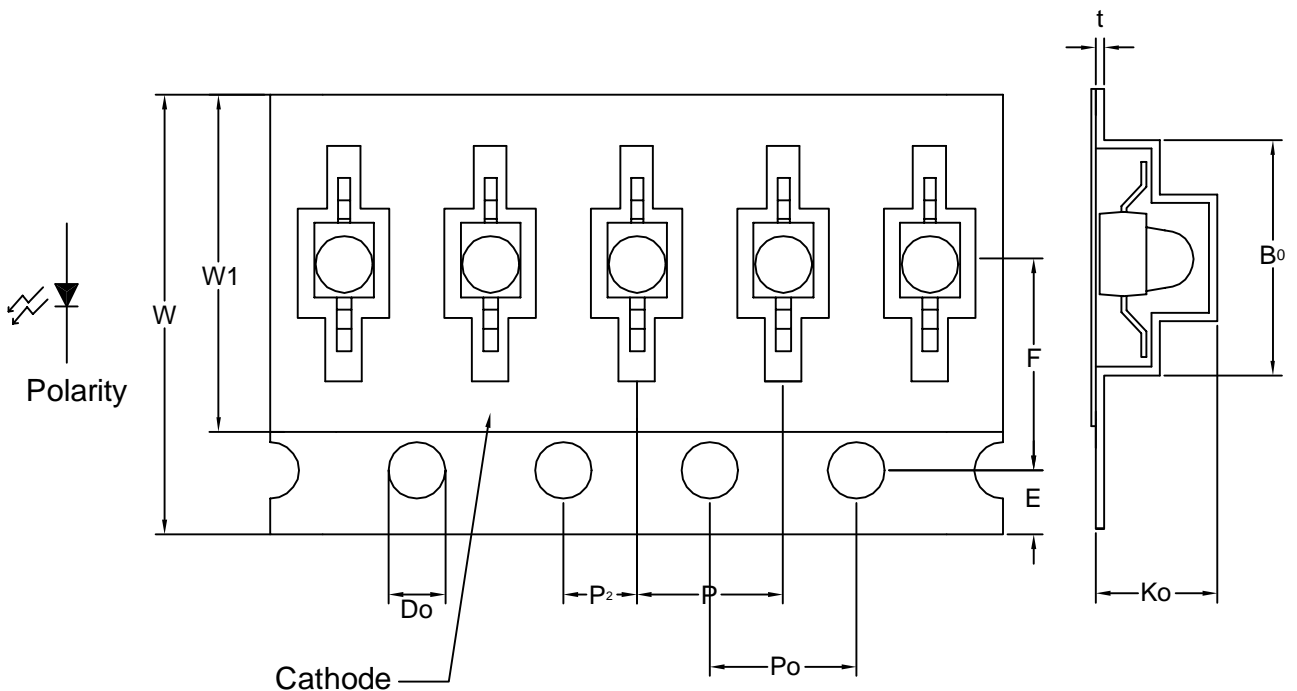


Package Dimensions



Note : 1.All dimension are in millimeter tolerance is $\pm 0.2\text{mm}$ unless otherwise noted.
2.Specifications are subject to change without notice.

Carrier Type Dimensions



Note : The tolerances unless mentioned is $\pm 0.2\text{mm}$, Angle ± 0.5 . Unit=mm.



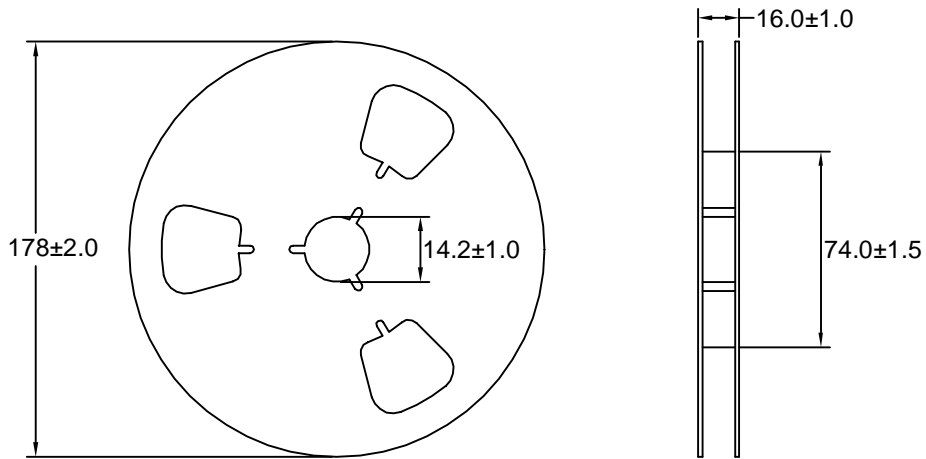
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PART NO. LVY9033/TR2

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Reel Dimensions



Part No.	Description	Quantity/Reel
LVY9033/TR2	12.0mm tape, 7" reel	1500 devices

**Absolute Maximum Ratings at Ta=25**

Parameter	Symbol	Ratings	UNIT
		VY	
Forward Current	IF	30	mA
Peak Forward Current Duty 1/10@10KHz	IFP	60	mA
Power Dissipation	PD	75	mW
Reverse Current @5V	Ir	10	μ A
Electrostatic Discharge	ESD	2000	V
Operating Temperature	Topr	-40 ~ +85	
Storage Temperature	Tstg	-40~ +100	
Soldering Temperature	Tsol	Max 260 for 5 sec Max	

Typical Electrical & Optical Characteristics (Ta=25)

PART NO	MATERIAL	COLOR		Dominant wave length Dnm	Spectral halfwidth nm	Forward voltage @20mA(V)		Luminous intensity @20mA(mcd)		Viewing angle 2 1/2 (deg)
		Emitted	Lens			Min.	Max.	Min.	Typ.	
LVY9033/TR2	AlGaInP	Yellow	Water Clear	590	20	1.7	2.6	220	450	20

Note : 1.The forward voltage data did not including $\pm 0.1V$ testing tolerance.
2. The luminous intensity data did not including $\pm 15\%$ testing tolerance.



Typical Electro-Optical Characteristics Curve

VY CHIP

Fig.1 Forward current vs. Forward Voltage

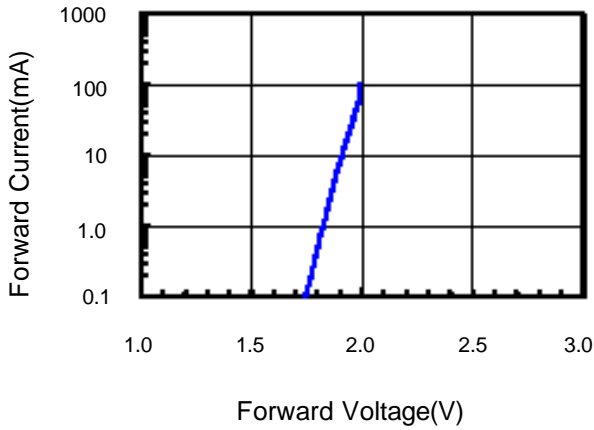


Fig.2 Relative Intensity vs. Forward Current

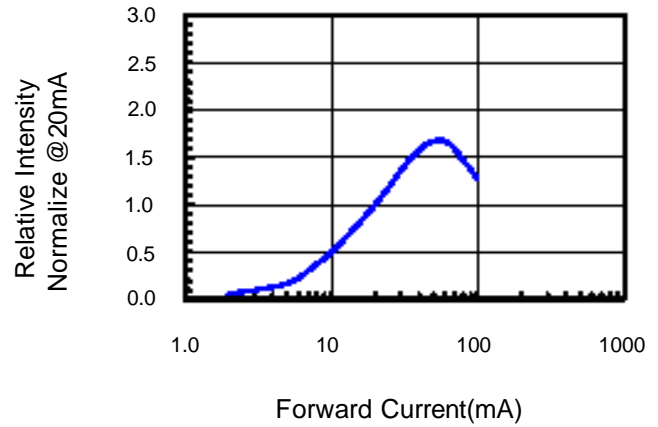


Fig.3 Forward Voltage vs. Temperature

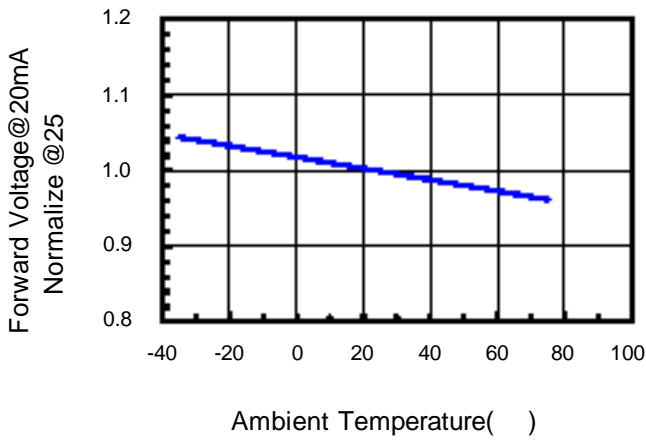


Fig.4 Relative Intensity vs. Temperature

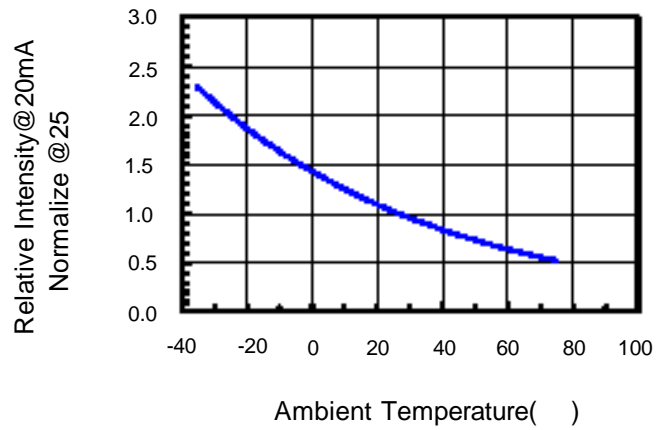
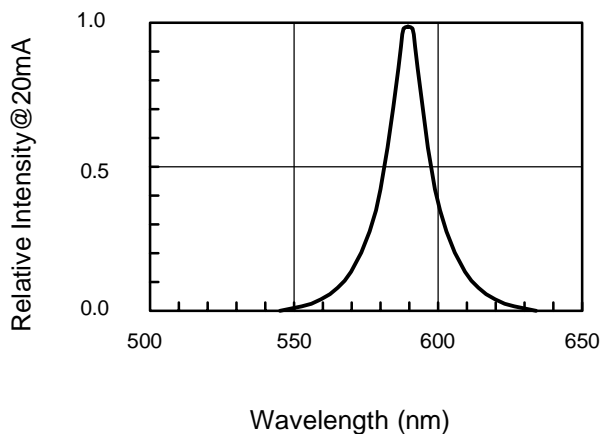


Fig.5 Relative Intensity vs. Wavelength

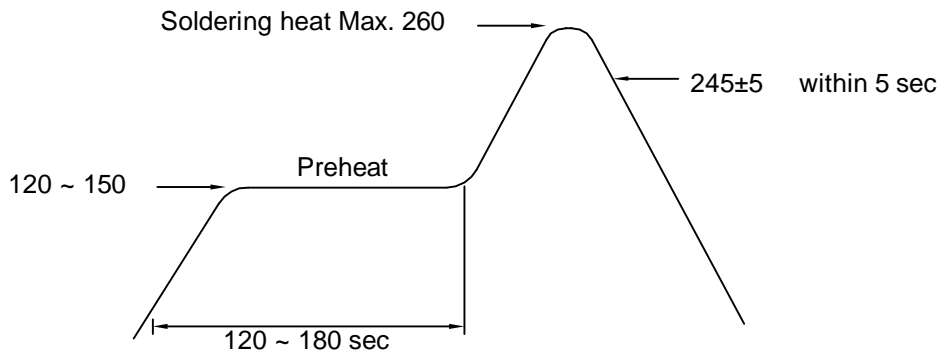




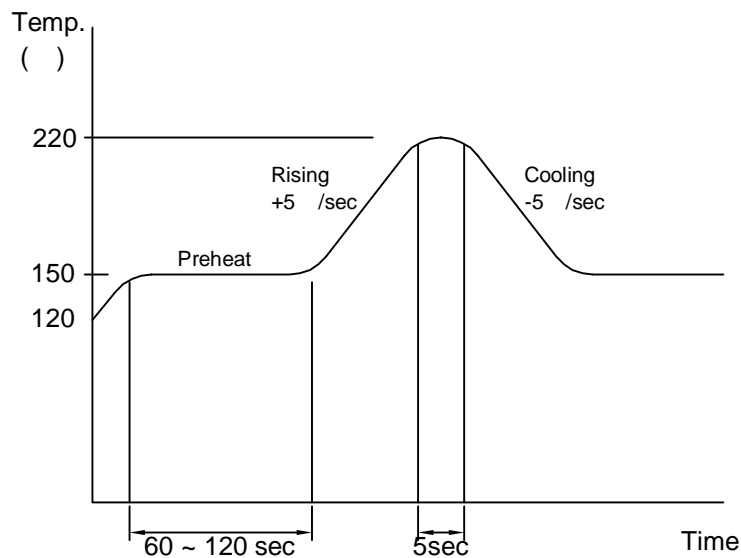
Soldering Iron:

Basic spec is 5 sec when 260 . If temperature is higher, time should be shorter(+10 -1sec).
Power dissipation of iron should be smaller than 15W,and temperature should be controllable.
Surface temperature of the device should be under 230 .

Soldering heat



Reflow Temp/Time





Precautions For Use:

Storage time:

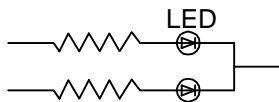
- 1.The operation of Temperatures and RH are : 5 ~35 ,RH60%.
- 2.Once the package is opened, the products should be used within a week. Otherwise, they should be kept in a damp proof box with descanting agent. Considering the tape life, we suggest our customers to use our products within a year(from production date).
- 3.If opened more than one week in an atmosphere 5 ~ 35 ,RH60%, they should be treated at 60 ±5 fo r 15hrs.

Drive Method:

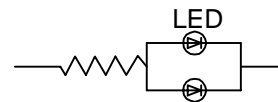
LED is a current operated device, and therefore, requirer some kind of current limiting incorporated into the driver circuit. This current limiting typically takes the form of a current limiting resistor placed in series with the LED.

Consider worst case voltage variations than could occur across the current limiting resistor. The forwr d current should not be allowed to change by more than 40 % of its desired value.

Circuit model A



Circuit model B



(A) Recommended circuit.

(B) The difference of brightness between LED could be found due to the VF-IF characteristics of LED.

Cleaning:

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED.

ESD(Electrostatic Discharge):

Static Electricity or power surge will damage the LED. Use of a conductive wrist band or anti-electrosatic glove is recommended when handing these LED. All devices, equipment and machinery must be properly grounded.



Reliability Test:

Classification	Test Item	Test Condition	Reference Standard
Endurance Test	Operating Life Test	1.Ta=Under Room Temperature As Per Data Sheet Maximum Rating. 2.If=20mA 3.t=1000 hrs (-24hrs, +72hrs)	MIL-STD-750D: 1026 MIL-STD-883D: 1005 JIS C 7021: B-1
	High Temperature Storage Test	1.Ta=105 ±5 2.t=1000 hrs (-24hrs, +72hrs)	MIL-STD-883D:1008 JIS C 7021: B-10
	Low Temperature Storage Test	1.Ta=-40 ±5 2.t=1000 hrs (-24hrs, +72hrs)	JIS C 7021: B-12
	High Temperature High Humidity Storage Test	1.IR-Reflow In-Board, 2 Times 2.Ta=65 ±5 3.RH=90%~95% 4.t=1000hrs±2hrs	MIL-STD-202F:103B JIS C 7021: B-11
Environmental Test	Thermal Shock Test	1.IR-Reflow In-Board,2 times 2.Ta=105 ±5 & -40 ±5 (10min) (10min) 3.total 10 cycles	MIL-STD-202F: 107D MIL-STD-750D: 1051 MIL-STD-883D: 1011
	Solderability Test	1.T.Sol=235 ±5 2.Immersion time 2±0.5sec 3.Immersion rate 25±2.5mm/sec 4.Immersion rate 25±2.5mm/sec 5.Coverage 95% of the dipped surface	MIL-STD-202F: 208D MIL-STD-750D: 2026 MIL-STD-883D: 2003 IEC 68 Part 2-20 JIS C 7021: A-2
	Temperature Cycling	1.105 ~ 25 ~ 55 ~ 25 30mins 5mins 30mins 5mins 2.10 Cyeles	MIL-STD-202F: 107D MIL-STD-750D: 1051 MIL-STD-883D: 1010 JIS C 7021: A-4
	Solderability Test	Ramp-up rate(183 to Peak) +3 second max Temp. maintain at 125(±25) 120 seconds max Temp. maintain above 183 60-150 seconds Peak temperature range 235 +5-0 Time within 5 of actual Peak Temperature(tp) 10-30 seconds Ramp-down rate +6 /second max	MIL-STD-750D:2031.2 J-STD-020



. MAIN RAW MATERIAL LIST

ITEM	MATERIAL	MODEL	SUPPLIER	ORIGIN
1	Yellow Chip (AlGaInP)	ED-M80HYU	光磊科技股份有限公司 (OPTO TECH CORPORATION)	台灣 TAIWAN
2	銀膠 (Silver Epoxy)	826-1	ABLESTIK	美國 USA
3	支架 (Lead Frame)	2005-9	巨貿精密工業股份有限公司 (TTOP CORPORATION)	台灣 TAIWAN
4	gold wire	0.1mil Au	AMERICAN FINE WIRE	美國 USA
5	Epoxy	NT-301H-11000	台灣日東電工股份有限公司 (NITTO DENKO (TAIWAN) CORPORATION)	台灣 TAIWAN