

Silicon NPN Power Transistors

BU133

DESCRIPTION

www.datasheet4u.com

- With TO-3 package
- High voltage ,high speed

APPLICATIONS

- Intended for operating in color TV receiver's chopper supplies

PINNING (See Fig.2)

| PIN | DESCRIPTION |
|-----|-------------|
| 1 | Base |
| 2 | Emitter |
| 3 | Collector |

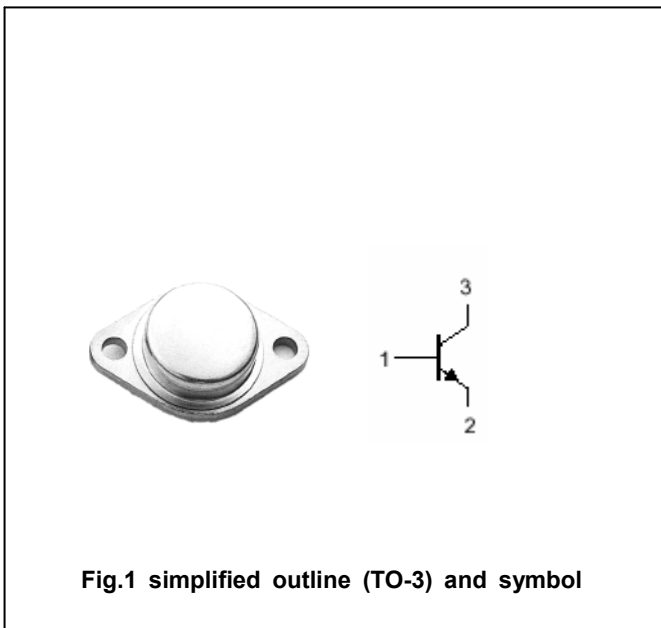


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

| SYMBOL | PARAMETER | CONDITIONS | MAX | UNIT |
|------------------|---------------------------|---------------------|---------|------|
| V _{CBO} | Collector-base voltage | Open emitter | 750 | V |
| V _{CEO} | Collector-emitter voltage | Open base | 250 | V |
| V _{EBO} | Emitter-base voltage | Open collector | 7 | V |
| I _C | Collector current | | 3 | A |
| P _T | Total power dissipation | T _C =50□ | 30 | W |
| T _j | Junction temperature | | 200 | □ |
| T _{stg} | Storage temperature | | -65~200 | □ |

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | VALUE | UNIT |
|----------------------|---------------------------------------------------|-------|------|
| R _{th j-mb} | Thermal resistance from junction to mounting base | 2.33 | □/W |

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CHARACTERISTICS

T_j=25°C unless otherwise specified

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| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP. | MAX | UNIT |
|-----------------------|--------------------------------------|---------------------------------------------|-----|------|-----|------|
| V _{CEO(SUS)} | Collector-emitter sustaining voltage | I _C =0.1A ; I _B =0 | 250 | | | V |
| V _{(BR)EBO} | Emitter-base breakdown voltage | I _E =10mA ; I _C =0 | 7 | | | V |
| V _{CEsat} | Collector-emitter saturation voltage | I _C =2A ; I _B =0.5A | | | 1.5 | V |
| V _{BEsat} | Base-emitter saturation voltage | I _C =2A ; I _B =0.5A | | | 1.4 | V |
| I _{CB0} | Collector cut-off current | V _{CB} =750V ; I _E =0 | | | 0.1 | mA |
| I _{EBO} | Emitter cut-off current | V _{EB} =7V ; I _C =0 | | | 0.1 | mA |
| h _{FE} | DC current gain | I _C =1A ; V _{CE} =5V | 15 | | 80 | |
| f _T | Transition frequency | I _C =0.2A ; V _{CE} =10V | | 8 | | MHz |

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PACKAGE OUTLINE

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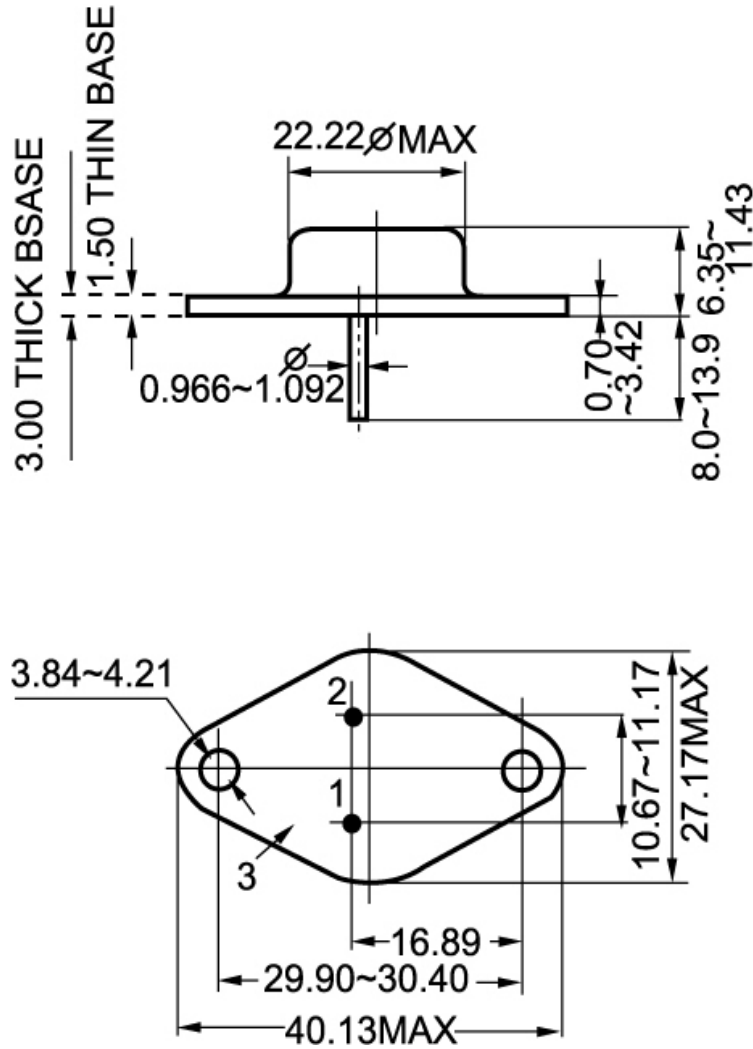


Fig.2 Outline dimensions