

AMPMODU* Mod.I Interconnection System,
3.96 mm. [.156 in.] PITCH

1. SCOPE

- 1.1 This specification covers performance and test requirements for the AMPMODU* Mod.I interconnection system, manufactured by AMP Italy.
- 1.2 When tests or inspections are performed on the subject product line, this document must always be used together with the applicable product drawings.

2. DESCRIPTION

This system provides wire-to-board termination at 3.96 mm. [.156 in.] pitch using the 0.79x1.57 mm. [.031x.062 in.] post technology.

It is composed of :

2.1 Crimped receptacle connector :

Single row, having standard and high pressure contacts for discrete wires covering a range of 17 to 26 AWG.

2.2 Header assembly :

Single row, shrouded and unshrouded, having straight or right angle post contacts inserted into an insulated header which is directly mounted on a printed circuit board.

3. APPLICABLE DOCUMENTS

Applicable portion of the following documents form a part of this specification, to the extent indicated herein.

In the event of conflict between the requirements of this specification and the product drawing, the product drawing shall take precedence.

- IEC 512-2 : Electromechanical components for electronic equipment
Basic testing procedures and measuring methods.
- IEC 130-1 : Connectors for frequencies below 3 MHz, general requirements and
measuring methods.
- IEC 68-2 : Basic environmental testing procedures for electronic equipment and
components.
- AMP Spec.# 114-20040 : Application specification

Product Code: 5185

D1	REDRAWN WITHOUT CHANGES PER EC ET00-0276-99	R.F. <i>APV</i>	06 /SEP/99	C.T.	06 /SEP/99
rev letter	rev. record	DR	Date	CHK	Date
DR. R. FABRIS <i>R. Fabris</i>	31 AUG 99	APVD C. TARTARI <i>C. Tartari</i>	31 AUG 99		31 AUG 99

This specification is a controlled document.

This information is confidential and is disclosed to you on condition that no further disclosure is made by you to other than AMP personnel without written authorization from AMP Italia.

Page 1 of 7

* Trademark of AMP Incorporated

LOC I