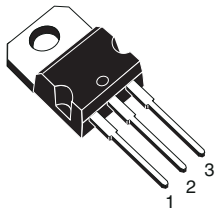
TR136D

High voltage fast-switching NPN power transistor

Preliminary Data

Features

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Integrated antiparallel collector-emitter diode



TO-220

Applications

- Electronic ballast for fluorescent lighting
- Electronic transformer for halogen lamps

Description

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining a satisfactory RBSOA.

Figure 1. Internal schematic diagrams

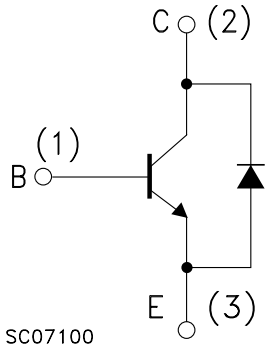


Table 1. Device summary

Order code	Marking	Package	Packing
TR136D	TR136D	TO-220	Tube

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage (V_{BE} = 0)	700	V
V_{CEO}	Collector-emitter voltage (I_B = 0)	400	V
V_{EBO}	Emitter-base voltage (I_C = 0)	9	V
I_C	Collector current	3	A
I_{CM}	Collector peak current (t_P < 5ms)	6	A
I_B	Base current	1.5	A
I_{BM}	Base peak current (t_P < 5ms)	3	A
P_{tot}	Total dissipation at $T_c \leq 25°C$	60	W
T_{stg}	Storage temperature	-65 to 150	°C
T_J	Max. operating junction temperature	150	°C

2 Electrical characteristics

(T_{case} = 25°C unless otherwise specified)

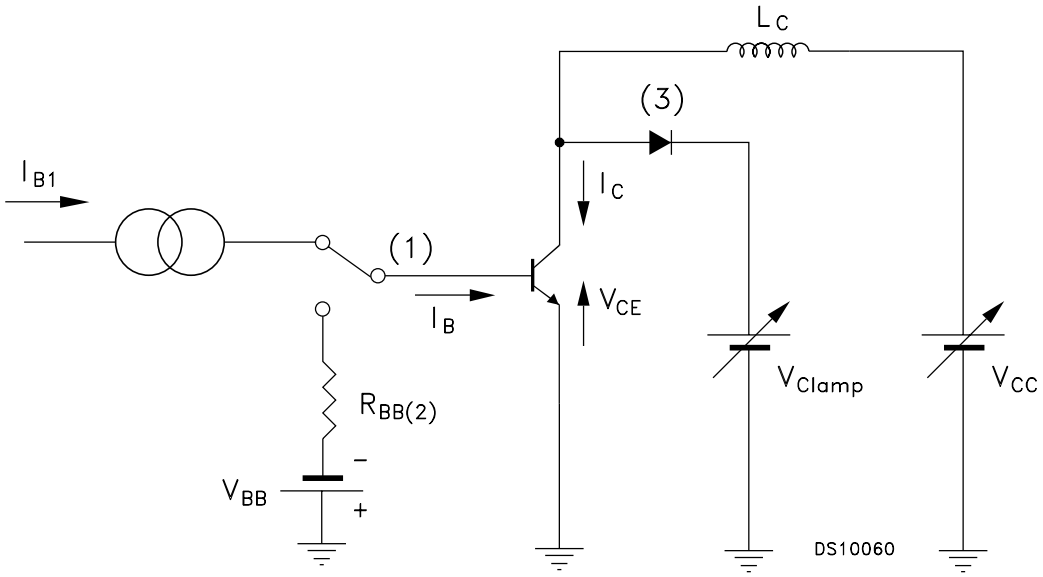
Table 3. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{CEV}	Collector cut-off current (V_{BE} =-1.5V)	V_{CE} =700 V V_{CE} =700 V T_C = 100 °C			1 5	mA mA
I_{EBO}	Emitter cut-off current (I_C =0)	V_{EB} =9 V			1	mA
$V_{CEO(sus)}$ (1)	Collector-emitter sustaining voltage (I_B = 0)	I_C =10mA	400			V
$V_{CE(sat)}$ (1)	Collector-emitter saturation voltage	I_C = 0.5 A I_B = 0.1 A I_C = 0.6 A I_B = 60 mA I_C = 2 A I_B = 0.5 A			0.5 0.7 1	V V V
$V_{BE(sat)}$ (1)	Base-emitter saturation voltage	I_C = 1 A I_B = 0.2 A I_C = 2 A I_B = 0.5 A			1.2 1.6	V V
h_{FE}	DC current gain	I_C = 10 mA V_{CE} =5 V I_C =2 A V_{CE} =5 V	10 10		 20	
t_s t_f	Inductive load Storage time Fall time	I_C = 1A I_{B1} =0.2 A $V_{BE(off)}$ =-5 V R_{BB} = 0 Ω V_{Clamp} = 200 V L = 50 mH (see *Figure 2*)		0.8 0.16		µs µs
V_F	Diode forward voltage	I_F = 1 A			2.5	V

1. Pulsed duration = 300 ms, duty cycle ≤1.5%

2.1 Test circuits

Figure 2. Inductive load switching test circuit



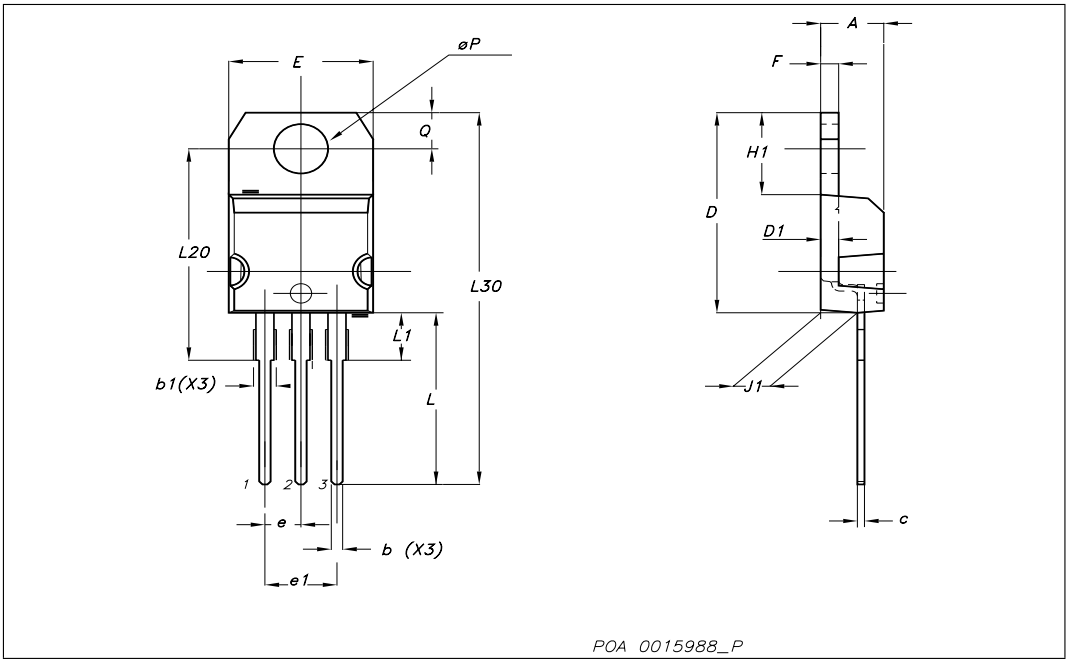
1) Fast electronic switch
2) Non-inductive resistor
3) Fast recovery rectifier

3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

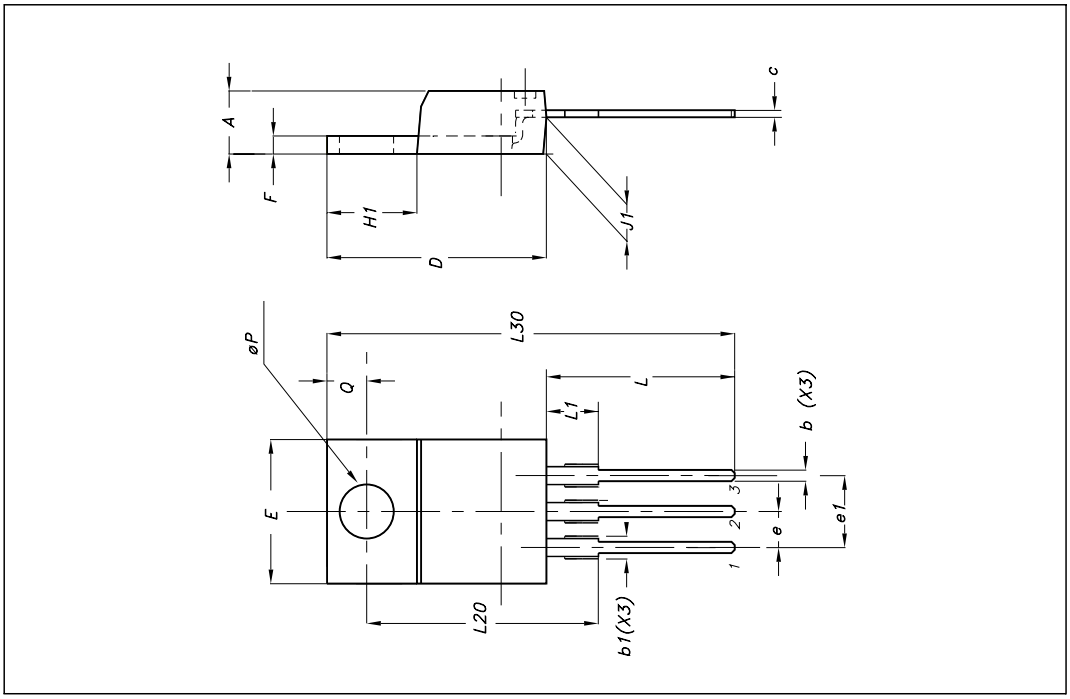
TO-220 mechanical data

Dim	mm			inch		
	Min	Typ	Max	Min	Typ	Max
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
c	0.49		0.70	0.019		0.027
D	15.25		15.75	0.6		0.62
D1		1.27			0.050	
E	10		10.40	0.393		0.409
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.051
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
∅P	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



TO-220 Mechanical Data “Option 1”

DIM.	mm.		
	MIN.	TYP	MAX.
A	4.40		4.60
b	0.61		0.88
b1	1.15		1.70
c	0.37		0.43
D	15.25		15.75
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L		12.87	
L1		3.25	
L20		16.40	
L30		28.90	
øP	3.75		3.85
Q	2.65		2.95



4 Revision history

Table 4. Document revision history

Date	Revision	Changes
08-Oct-2007	1	Initial release.

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