

40V PNP SILICON PLANAR MEDIUM POWER TRANSISTOR IN SOT89

Features

- V_{(BR)CEO} > -40V
- High current capability I_C = -1A
- Low saturation voltage V_{CE(sat)} < -500mV @ -1A
- Complementary NPN type: FCX491A
- "Lead Free", RoHS Compliant (Note 1)

Mechanical Data

- Case: SOT89
- Moisture Sensitivity: Level 1 per J-STD-020
- UL Flammability Rating 94V-0
- Terminals: Matte Tin Finish
- Weight: 0.052 grams (Approximate)

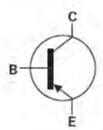
Application

- Power MOSFET gate driving
- Low loss power switching

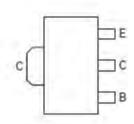
SOT89







Device symbol



Pin-out Top

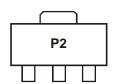
Ordering Information (Note 2)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FCX591ATA	P2	7	12mm	1000
FCX591A-7 (Note 3)	P2	7	12mm	1000

Notes:

- 1. No purposefully added lead.
- 2. For packaging details, go to our website at http://www.diodes.com.
- 3. Halogen and Antimony Free. "Green" devices, Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

Marking Information



P2 = Product Type Marking Code





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-40	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Base Voltage	V _{EBO}	-5	V
Continuous Collector Current	I _C	-1	А
Peak Pulse Current	I _{CM}	-2	А
Peak Base Current	I _B	-200	mA

Thermal Characteristics

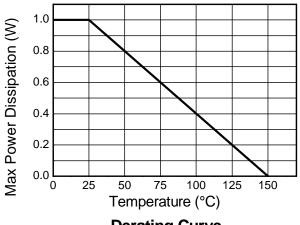
Characteristic	Symbol	Value	Unit
Collector Power Dissipation	P_{D}	1	W
Thermal Resistance, Junction to Ambient Air (Note 4) @ T _A = 25°C	$R_{\theta JA}$	125	°C/W
Operating and Storage Temperature Range	$T_{J,T_{STG}}$	-65 to +150	°C

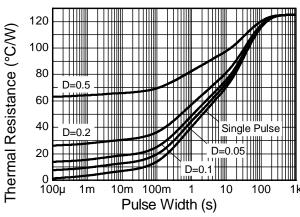
Notes: 4. For the device mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.





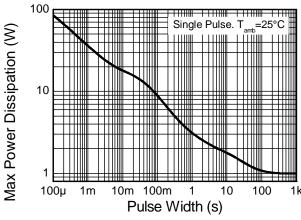
Thermal Characteristics





Derating Curve

Transient Thermal Impedance



Pulse Power Dissipation





Electrical Characteristics @TA = 25°C unless otherwise specified

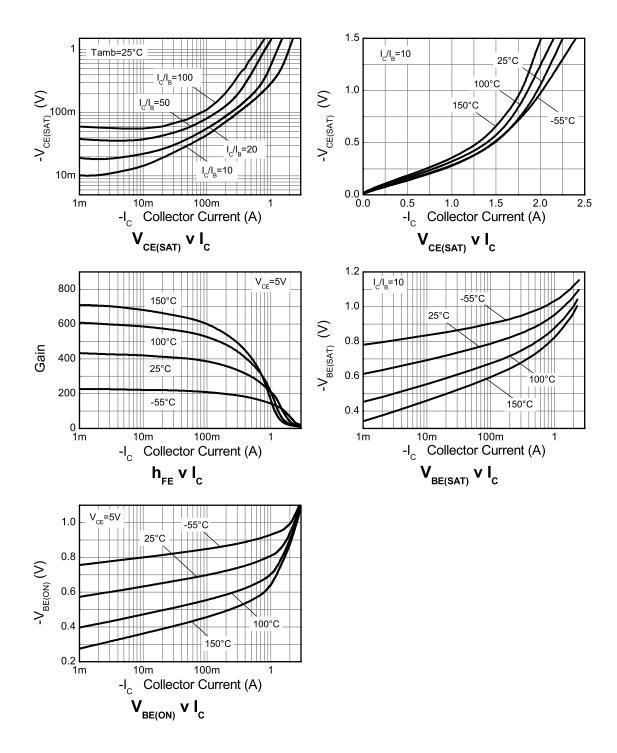
Characteristic	Symbol	Min	Тур.	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-40	-	-	V	$I_{C} = -100 \mu A$
Collector-Emitter Breakdown Voltage (Note 5)	$V_{(BR)CEO}$	-40	-	-	V	$I_C = -10mA$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-5	-	-	V	$I_E = -100 \mu A$
Collector Cutoff Current	I _{CBO}	-	=	-100	nA	V _{CB} = -30V
Emitter Cutoff Current	I _{EBO}	-	-	-100	. nA	$V_{EB} = -4V$
Emitter Cutoff Current	I _{CES}	-	-	-100	. nA	V _{CES} = -30V
DC current transfer Static ratio (Note 5)	hFE	300 300 250 160 30	- - - -	- 800 - - - -	-	I _C = -1mA I _C = -100mA I _C = -500mA, V _{CE} = -5V I _C = -1A I _C = -2A I _C = -100mA, I _B = -1mA
Collector-Emitter Saturation Voltage (Note 5)	V _{CE(sat)}	- -	- -	-0.2 -0.35 -0.5	V	I _C = -500mA, I _B = -20mA I _C = -1A, I _B = -100mA
Base-Emitter Saturation Voltage (Note 5)	$V_{BE(sat)}$	-	-	-1.1	V	$I_C = -1A$, $I_B = -50mA$
Base-Emitter Turn-on Voltage (Note 5)	$V_{BE(on)}$	-	-	-1.0	V	$I_{C} = -1A$, $V_{CE} = -5V$
Transitional Frequency	f⊤	150	-	-	MHz	$I_E = -50 \text{mA}, V_{CE} = -10 \text{V}$ f = 100MHz
Output capacitance	C_{obo}	-	-	10	pF	$V_{CB} = -10V$, $f = 1MHz$,

Notes: 5. Measured under pulsed conditions. Pulse width = 300μ s. Duty cycle $\leq 2\%$.





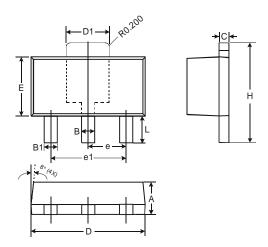
Typical Characteristics





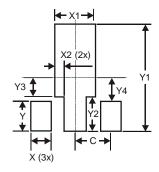


Package Outline Dimensions



SOT89				
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
С	0.35	0.43		
D	4.40	4.60		
D1	1.52	1.83		
Е	2.29	2.60		
е	1.50 Typ			
e1	3.00 Typ			
Н	3.94	4.25		
L	0.89	1.20		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Х	0.900
X1	1.733
X2	0.416
Y	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
С	1.500





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