

Low frequency amplifier

2SD2673

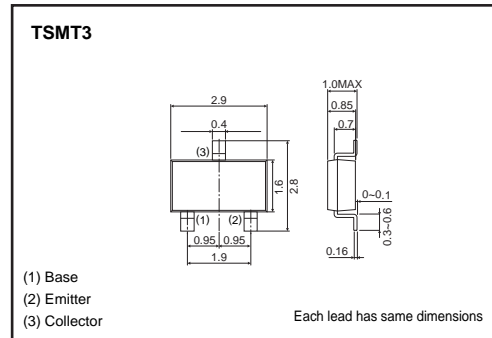
●Application

Low frequency amplifier
Driver

●Features

- 1) A collector current is large. (3A)
- 2) $V_{CE(sat)}$: max. 250mV
At $I_C = 1.5A / I_B = 30mA$

●External dimensions (Unit : mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V_{CB0}	30	V
Collector-emitter voltage	V_{CEO}	30	V
Emitter-base voltage	V_{EBO}	6	V
Collector current	I_C	3	A
	I_{CP}	6	A* ¹
Power dissipation	P_C	500	mW
		1* ²	W
Junction temperature	T_j	150	°C
Range of storage temperature	T_{stg}	-55 to +150	°C

*1 Single pulse, $P_w=1ms$

*2 Mounted on a 25×25×1.0mm Ceramic substrate

●Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	3000
2SD2673		○

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CB0}	30	—	—	V	$I_C=10\mu A$
Collector-emitter breakdown voltage	BV_{CEO}	30	—	—	V	$I_C=1mA$
Emitter-base breakdown voltage	BV_{EBO}	6	—	—	V	$I_E=10\mu A$
Collector cutoff current	I_{CBO}	—	—	100	nA	$V_{CB}=30V$
Emitter cutoff current	I_{EBO}	—	—	100	nA	$V_{EB}=6V$
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	120	250	mV	$I_C=1.5A, I_B=30mA$
DC current gain	h_{FE}	270	—	680	—	$V_{CE}=2V, I_C=200mA^*$
Transition frequency	f_T	—	200	—	MHz	$V_{CE}=2V, I_E=-200mA, f=100MHz^*$
Collector output capacitance	C_{ob}	—	40	—	pF	$V_{CB}=10V, I_E=0A, f=1MHz$

* Pulsed

Transistors

●Electrical characteristic curves

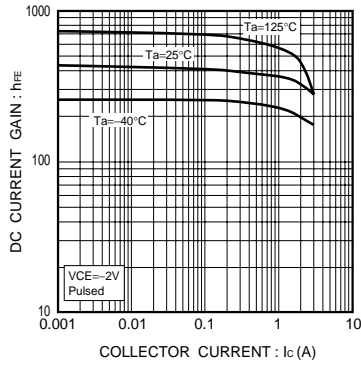


Fig.1 DC current gain vs. collector current

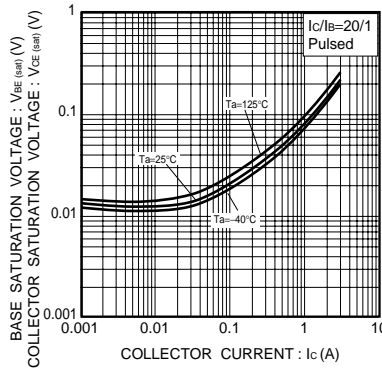


Fig.2 Collector-emitter saturation voltage base-emitter saturation voltage vs. collector current

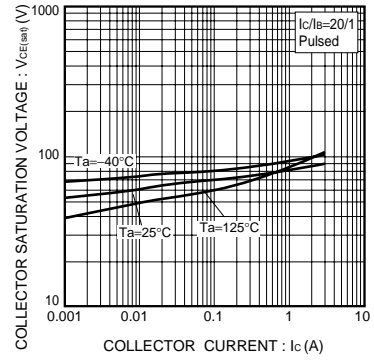


Fig.3 Collector-emitter saturation voltage vs. collector current

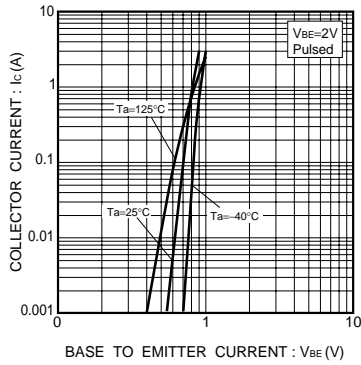


Fig.4 Grounded emitter propagation characteristics

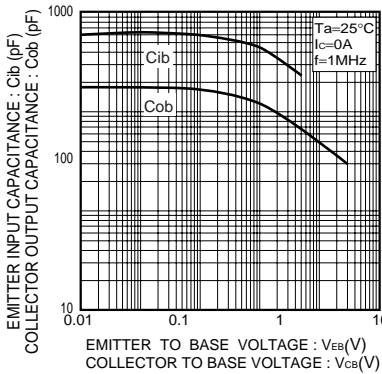


Fig.5 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage

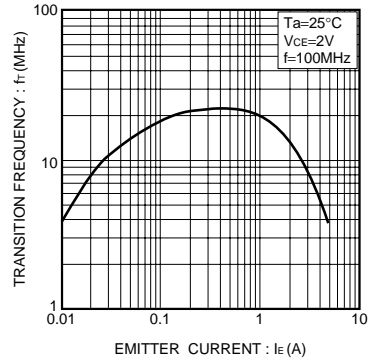


Fig.6 Gain bandwidth product vs. emitter current

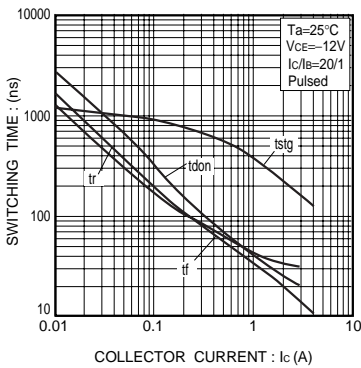


Fig.7 Switching time

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