### TOSHIBA Transistor Silicon NPN Epitaxial Type

## **TTC007**

# High-Speed Switching Applications DC-DC Converter Applications

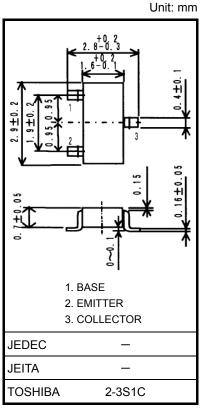
• High DC current gain:  $h_{FE}$  = 400 to1000 ( $I_C$  = 0.1 A)

• Low collector-emitter saturation voltage: V<sub>CE(sat)</sub> = 0.12 V (max)

• High-speed switching : t<sub>f</sub> = 85 ns (typ.)

## **Absolute Maximum Ratings (Ta = 25°C)**

| Characteristics             |          | Symbol           | Rating     | Unit |  |
|-----------------------------|----------|------------------|------------|------|--|
| Collector-base voltage      |          | $V_{CBO}$        | 100        | V    |  |
| Collector-emitter voltage   |          | V <sub>CEO</sub> | 50         | V    |  |
| Emitter-base voltage        |          | V <sub>EBO</sub> | 7          | V    |  |
| Collector current           | DC       | IC               | 1          | Α    |  |
|                             | Pulse    | I <sub>CP</sub>  | 2          |      |  |
| Base current                |          | ΙΒ               | 0.1        | Α    |  |
| Collector power dissipation | t = 10 s | PC               | 1.1        | W    |  |
|                             | DC       | (Note 1)         | 0.7        |      |  |
| Junction temperature        |          | Tj               | 150        | °C   |  |
| Storage temperature range   |          | T <sub>stg</sub> | −55 to 150 | °C   |  |



Weight: 0.01 g (typ.)

Note1: Mounted on FR4 board (glass epoxy; 645 mm<sup>2</sup>,1.6 mm thick; Cu area: 645 mm<sup>2</sup>)

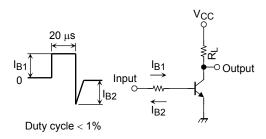
Note2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## Electrical Characteristics (Ta = 25°C)

| Characteristic            |                    | Symbol                | Test Condition                                       | Min | Тур. | Max  | Unit |  |
|---------------------------|--------------------|-----------------------|--|-----|------|------|------|--|
| Collector cut-off current |                    | I <sub>CBO</sub>      | V <sub>CB</sub> = 100 V, I <sub>E</sub> = 0          | _   | _    | 100  | nA   |  |
| Emitter cut-off cur       | rent               | I <sub>EBO</sub>      | V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0            | _   | _    | 100  | nA   |  |
| Collector-emitter b       | oreakdown voltage  | V (BR) CEO            | I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0           | 50  | _    | _    | V    |  |
| DC current gain           |                    | h <sub>FE (1)</sub>   | V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.1 A        | 400 | _    | 1000 | -    |  |
|                           |                    | h <sub>FE (2)</sub>   | V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.3 A        | 200 | _    | _    |      |  |
| Collector-emitter s       | saturation voltage | V <sub>CE</sub> (sat) | I <sub>C</sub> = 0.3 A, I <sub>B</sub> = 6 mA        | _   | _    | 0.12 | V    |  |
| Base-emitter satu         | ration voltage     | V <sub>BE (sat)</sub> | I <sub>C</sub> = 0.3 A, I <sub>B</sub> = 6 mA        | _   | _    | 1.1  | V    |  |
| Collector output ca       | apacitance         | C <sub>ob</sub>       | V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1MHz | _   | 5    | _    | pF   |  |
| Switching time            | Rise time          | t <sub>r</sub>        | See Figure 1   | _   | 35   | _    |      |  |
|                           | Storage time       | t <sub>stg</sub>      | $V_{CC}$ = 30 V, $R_{L}$ = 100 $\Omega$              | _   | 680  | _    | ns   |  |
|                           | Fall time          | t <sub>f</sub>        | I <sub>B1</sub> = I <sub>B2</sub> = 10 mA            | _   | 85   | _    |      |  |

## Marking



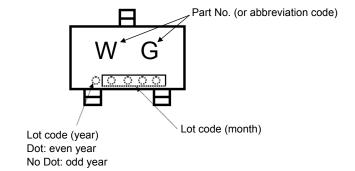
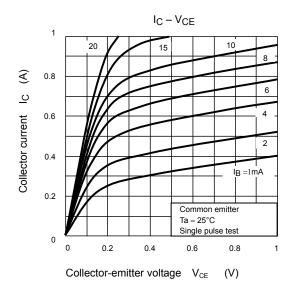
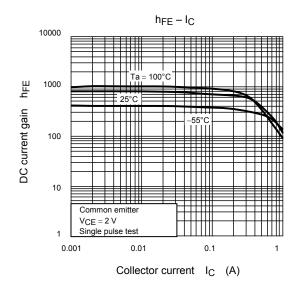
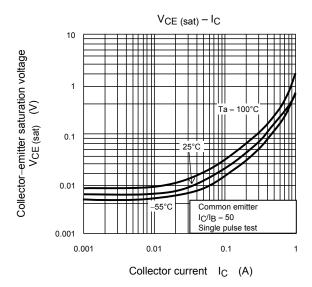
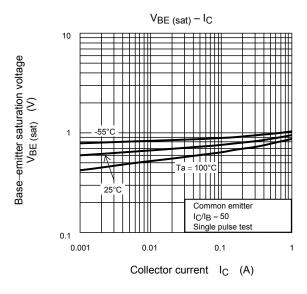


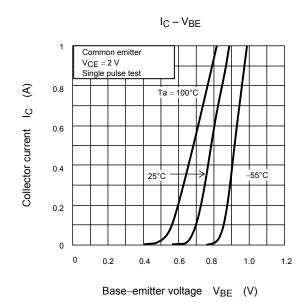
Figure 1. Switching Time Test Circuit





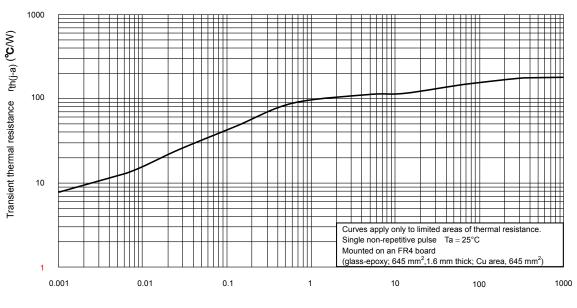






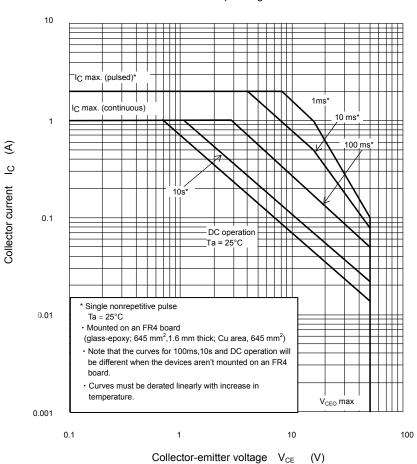
3 2010-02-08





## Pulse width $t_W$ (s)

### Safe Operating Area



4 2010-02-08

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