

Silicon PNP Power Transistors

2SB762 2SB762A

DESCRIPTION

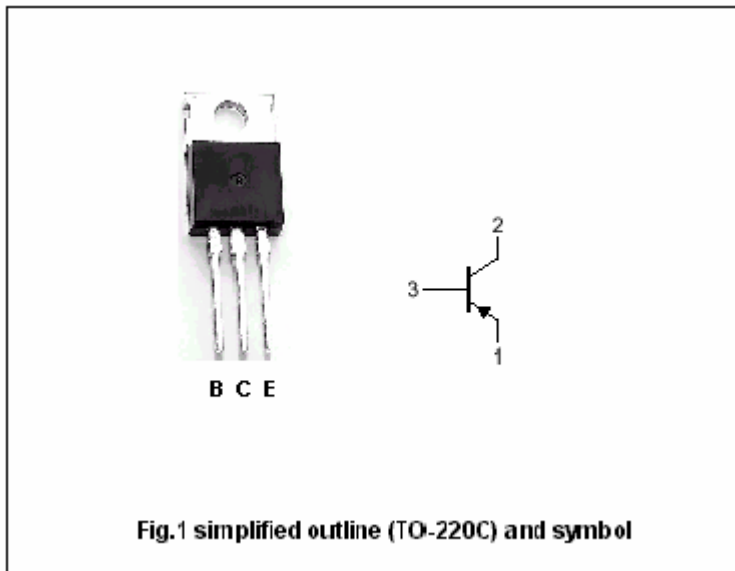
- With TO-220C package
- Complement to type 2SD857/857A
- Low collector saturation voltage

APPLICATIONS

- For audio frequency power amplifier applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Tc=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	2SB762	-60	V
		2SB762A	-80	
V _{CEO}	Collector-emitter voltage	2SB762	-60	V
		2SB762A	-80	
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-4	A
I _{CM}	Collector current-peak		-8	A
P _C	Collector power dissipation	T _C =25	40	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO}	Collector-emitter breakdown voltage	2SB762	I _C =-30mA; I _B =0	-60		V
		2SB762A		-80		
V _{CEsat}	Collector-emitter saturation voltage	I _C =-4A; I _B =-0.4 A			-1.5	V
V _{BE}	Base-emitter on voltage	I _C =-3A; V _{CE} =-4V			-2.0	V
I _{CES}	Collector cut-off current	2SB762			-400	μ A
		2SB762A				
I _{CEO}	Collector cut-off current	2SB762			-700	μ A
		2SB762A				
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-7	mA
h _{FE-1}	DC current gain	I _C =-1A; V _{CE} =-4V	40		250	
h _{FE-2}	DC current gain	I _C =-3A; V _{CE} =-4V	15			

Switching times

t _{on}	Turn-on time	I _C =-4A; I _{B1} =-I _{B2} =-0.4 A		0.3		μ s
t _{off}	Turn-off time			1.3		μ s

◆ h_{FE-1} classifications

R	Q	P
40-90	70-150	120-250

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance: ± 0.10 mm)