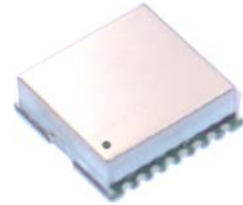


### Features

- +3 dBm Output Level at 1950 MHz
- 2nd Harmonic : < -20 dBc
- Spurious Level : < -70 dBc
- Lock Time : < 30ms
- 40 mA Current Consumption
- Strip-line Resonator

### Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



### Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	1935.0	1950.0	1965.0
Output Power	dBm	+1	+3	+5
Supply Voltage	V	4.75	5.00	5.25
Current Consumption	mA			40
Channel Step Size	kHz		10	
2nd Harmonics	dBc			-20
Spurious Level	dBc			-70
Lock Time	ms			30
Reference Frequency	MHz		13	
Reference Input Level	dBm		-	
Phase Noise (C / N)	@ 1 kHz		-73	
	@ 10 kHz		-100	
	@ 100 kHz		-118	
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V<sub>CC</sub> = 5.0 V, Freq. = 1950 MHz, 50 ohm system.

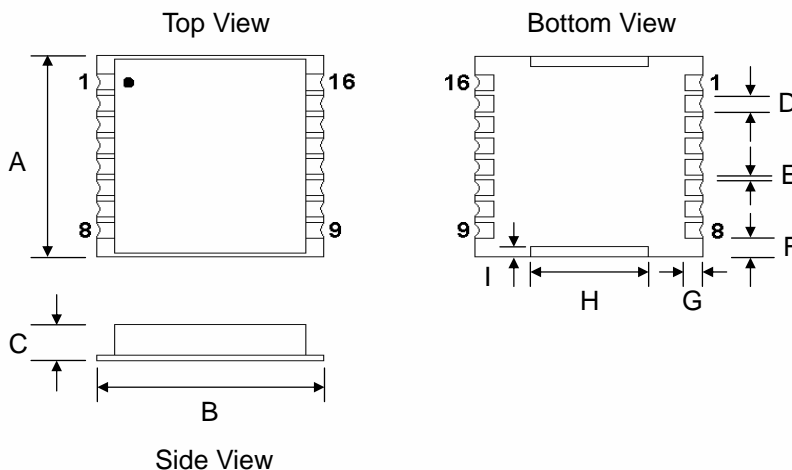
### More Information

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### Outline Drawing



Pin Configuration	
1	DATA
2	CLOCK
3	ENABLE
4	Ground
9	VCC (VCO)
13	RF OUT
15	VCP (PLL)
16	LOCK DETECT
Others	Ground

Dimension (mm)	
A	19.0
B	19.0
C	3.0
D	1.5
E	0.5
F	1.75
G	1.35
H	15.0
I	0.9
Tolerance: ± 0.2	