

# Dual N-channel MOSFET with schottky diode

ELM14918AA-N

## General description

ELM14918AA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

## Features

- |                              |                                 |                   |
|------------------------------|---------------------------------|-------------------|
| Q1                           | Q2                              | Schottky diode    |
| • $V_{ds}=30V$               | $V_{ds}=30V$                    | • $V_{ds(V)}=30V$ |
| • $I_d=9.3A$                 | $I_d=8.3A$ ( $V_{gs}=10V$ )     | • $I_f=3A$        |
| • $R_{ds(on)} < 14.5m\Omega$ | $< 18m\Omega$ ( $V_{gs}=10V$ )  | • $V_f < 0.5V@1A$ |
| • $R_{ds(on)} < 16m\Omega$   | $< 27m\Omega$ ( $V_{gs}=4.5V$ ) |                   |

## Maximum absolute ratings

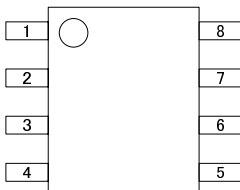
Parameter	Symbol	Max. Q1	Max. Q2	Max.Schottky	Unit	Note
Drain-source voltage	$V_{ds}$	30	30		V	
Gate-source voltage	$V_{gs}$	$\pm 12$	$\pm 20$		V	
Continuous drain current	$T_a=25^\circ C$	9.3	8.3		A	1
	$T_a=70^\circ C$	7.4	6.7			
Pulsed drain current	$I_{dm}$	40	40		A	2
Schottky reverse voltage	$V_{ka}$			30	V	
Continuous forward current	$T_a=25^\circ C$			3.0	A	1
	$T_a=70^\circ C$			2.2		
Pulsed diode forward current	$I_{fm}$			20	A	2
Power dissipation	$T_a=25^\circ C$	2.00	2.00	2.00	W	1
	$T_a=70^\circ C$	1.28	1.28	1.28		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	-55 to 150	-55 to 150	$^\circ C$	

## Thermal characteristics

Parameter (Q1,Q2)		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$t \leq 10s$	$R\theta_{ja}$	53.0	62.5	$^\circ C/W$	1
Maximum junction-to-ambient	Steady-state		81.9	110.0	$^\circ C/W$	
Maximum junction-to-lead	Steady-state	$R\theta_{jl}$	30.5	40.0	$^\circ C/W$	3
Parameter (Schottky)		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$t \leq 10s$	$R\theta_{ja}$	50.4	62.5	$^\circ C/W$	1
Maximum junction-to-ambient	Steady-state		86.0	110.0	$^\circ C/W$	
Maximum junction-to-lead	Steady-state	$R\theta_{jl}$	26.6	40.0	$^\circ C/W$	3

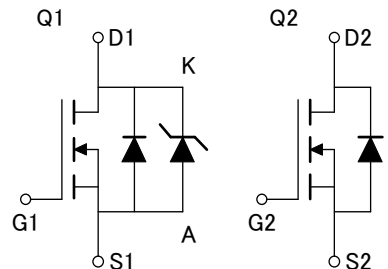
## Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	DRAIN2
2	DRAIN2
3	GATE1
4	SOURCE1/ANODE
5	DRAIN1/SOURCE2/CATHODE
6	DRAIN1/SOURCE2/CATHODE
7	DRAIN1/SOURCE2/CATHODE
8	GATE2

## Circuit



# Dual N-channel MOSFET with schottky diode

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### ■ Electrical characteristics (Q1)

T<sub>a</sub>=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BV <sub>dss</sub>	I <sub>d</sub> =250 μA, V <sub>gs</sub> =0V	30			V
Zero gate voltage drain current (Set by Schottky leakage)	I <sub>dss</sub>	V <sub>r</sub> =30V		0.007	0.050	mA
		V <sub>r</sub> =30V, T <sub>j</sub> =125°C		3.2	10.0	
		V <sub>r</sub> =30V, T <sub>j</sub> =150°C		12.0	20.0	
Gate-body leakage current	I <sub>gss</sub>	V <sub>ds</sub> =0V, V <sub>gs</sub> =±12V			100	nA
Gate threshold voltage	V <sub>gs(th)</sub>	V <sub>ds</sub> =V <sub>gs</sub> , I <sub>d</sub> =250 μA	0.6	1.1	2.0	V
On state drain current	I <sub>d(on)</sub>	V <sub>gs</sub> =4.5V, V <sub>ds</sub> =5V	40			A
Static drain-source on-resistance	R <sub>ds(on)</sub>	V <sub>gs</sub> =10V		11.7	14.5	mΩ
		I <sub>d</sub> =9.3A	T <sub>j</sub> =125°C	15.4	19.0	
		V <sub>gs</sub> =4.5V, I <sub>d</sub> =8.8A		13.1	16.0	mΩ
Forward transconductance	G <sub>fs</sub>	V <sub>ds</sub> =5V, I <sub>d</sub> =9.3A	30	37		S
Diode+Schottky forward voltage	V <sub>sd</sub>	I <sub>s</sub> =1A		0.46	0.50	V
Max. body-diode+Schottky continuous current	I <sub>s</sub>				3.5	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	C <sub>iss</sub>			3740	4488	pF
Output capacitance (FET+Schottky)	C <sub>oss</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =15V, f=1MHz		295		pF
Reverse transfer capacitance	C <sub>rss</sub>			186		pF
Gate resistance	R <sub>g</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =0V, f=1MHz		0.86	1.10	Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Q <sub>g</sub>			30.5	37.0	nC
Gate-source charge	Q <sub>gs</sub>	V <sub>gs</sub> =4.5V, V <sub>ds</sub> =15V, I <sub>d</sub> =9.3A		4.5		nC
Gate-drain charge	Q <sub>gd</sub>			8.5		nC
Turn-on delay time	t <sub>d(on)</sub>			6.0	9.0	ns
Turn-on rise time	t <sub>r</sub>	V <sub>gs</sub> =10V, V <sub>ds</sub> =15V		8.2	12.0	ns
Turn-off delay time	t <sub>d(off)</sub>	R <sub>l</sub> =1.6 Ω, R <sub>gen</sub> =3 Ω		54.5	75.0	ns
Turn-off fall time	t <sub>f</sub>			10.5	15.0	ns
Body diode+Schottky reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =9.3A, dI/dt=100A/μs		23.5	28.0	ns
Body diode+Schottky reverse recovery charge	Q <sub>rr</sub>	I <sub>f</sub> =9.3A, dI/dt=100A/μs		13.3	16.0	nC

#### NOTE :

- The value of R<sub>θja</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with T<sub>a</sub>=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The R<sub>θja</sub> is the sum of the thermal impedance from junction to lead R<sub>θjl</sub> and lead to ambient.
- The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
- These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25°C. The SOA curve provides a single pulse rating.
- The Schottky appears in parallel with the MOSFET body diode, even though it is a separate chip. Therefore, we provide the net forward drop, capacitance and recovery characteristics of the MOSFET and Schottky. However, the thermal resistance is specified for each chip separately.

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## Typical electrical and thermal characteristics (Q1)

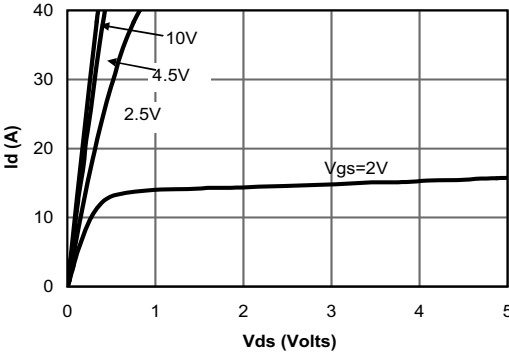


Fig 1: On-Region Characteristics

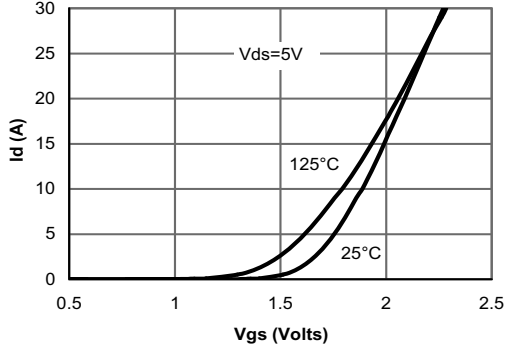


Figure 2: Transfer Characteristics

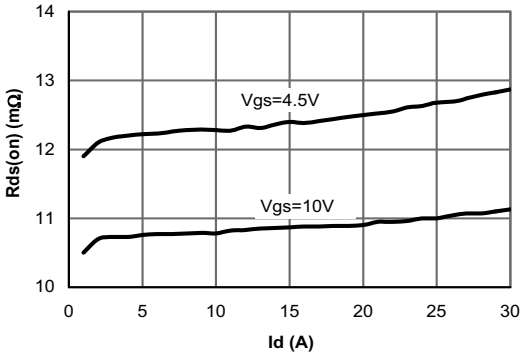


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

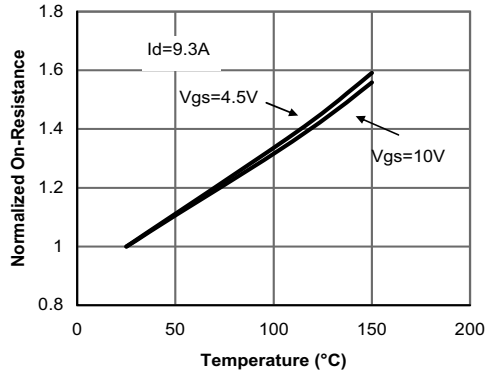


Figure 4: On resistance vs. Junction Temperature

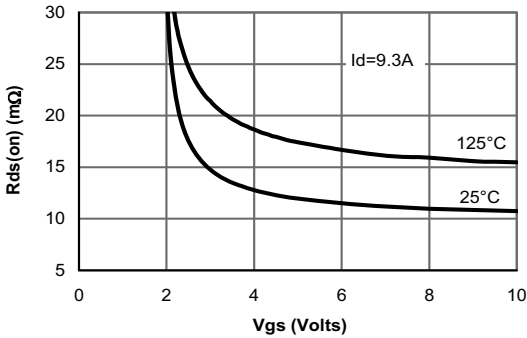


Figure 5: On resistance vs. Gate-Source Voltage

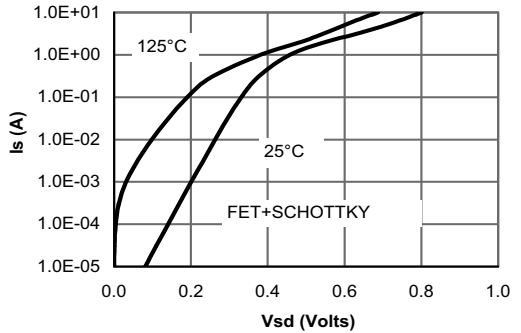


Figure 6: Body-Diode Characteristics (Note 6)

# Dual N-channel MOSFET with schottky diode

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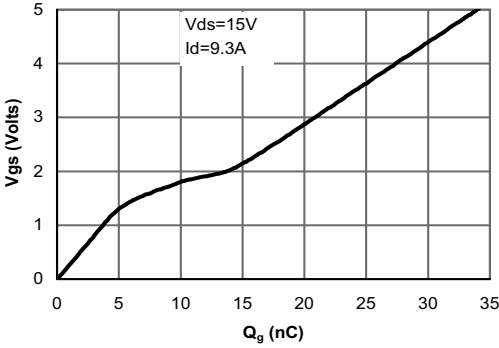


Figure 7: Gate-Charge Characteristics

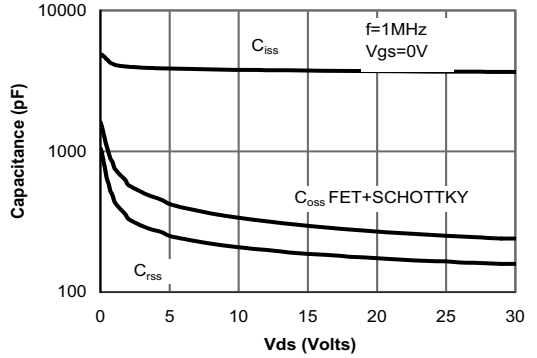


Figure 8: Capacitance Characteristics

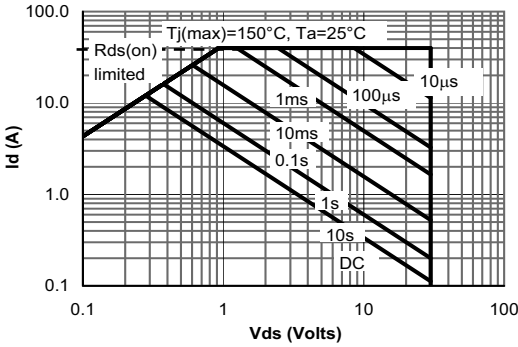


Figure 9: Maximum Forward Biased Safe Operating Area (Note 5)

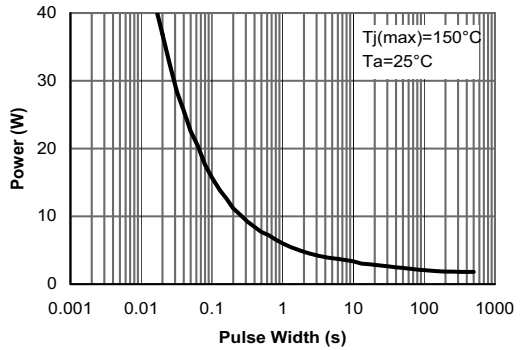


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note 5)

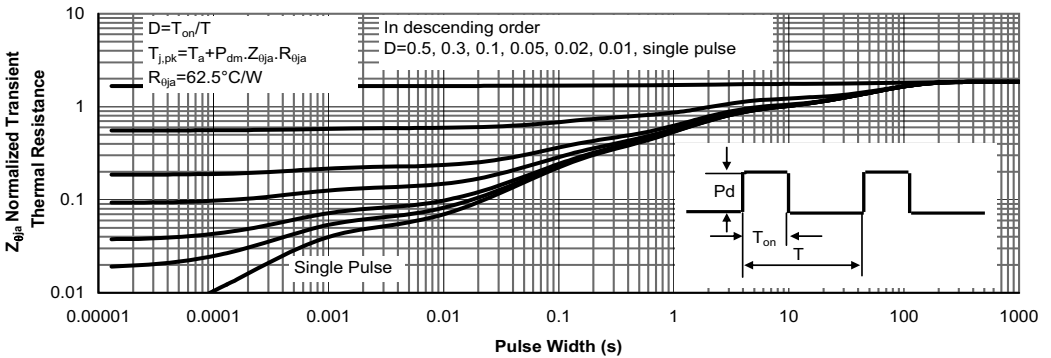


Figure 11: Normalized Maximum Transient Thermal Impedance

# Dual N-channel MOSFET with schottky diode

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### ■ Electrical characteristics (Q2)

T<sub>a</sub>=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BV <sub>dss</sub>	I <sub>d</sub> =250 μA, V <sub>gs</sub> =0V	30			V
Zero gate voltage drain current	I <sub>dss</sub>	V <sub>ds</sub> =24V V <sub>gs</sub> =0V T <sub>j</sub> =55°C		0.004	1.000 5.000	μA
Gate-body leakage current	I <sub>gss</sub>	V <sub>ds</sub> =0V, V <sub>gs</sub> =±20V			100	nA
Gate threshold voltage	V <sub>gs(th)</sub>	V <sub>ds</sub> =V <sub>gs</sub> , I <sub>d</sub> =250 μA	1.0	1.8	3.0	V
On state drain current	I <sub>d(on)</sub>	V <sub>gs</sub> =4.5V, V <sub>ds</sub> =5V	30			A
Static drain-source on-resistance	R <sub>ds(on)</sub>	V <sub>gs</sub> =10V I <sub>d</sub> =8.3A T <sub>j</sub> =125°C		14.9	18.0	mΩ
		V <sub>gs</sub> =4.5V, I <sub>d</sub> =7A		22.0	27.0	mΩ
Forward transconductance	G <sub>fs</sub>	V <sub>ds</sub> =5V, I <sub>d</sub> =8.3A		23		S
Diode forward voltage	V <sub>sd</sub>	I <sub>s</sub> =1A		0.45	0.50	V
Max. body-diode continuous current	I <sub>s</sub>				3	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	C <sub>iss</sub>			1040	1250	pF
Output capacitance	C <sub>oss</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =15V, f=1MHz		180		pF
Reverse transfer capacitance	C <sub>rss</sub>			110		pF
Gate resistance	R <sub>g</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =0V, f=1MHz		0.70	0.85	Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge (10V)	Q <sub>g</sub>			19.20	24.00	nC
Total gate charge	Q <sub>g</sub>	V <sub>gs</sub> =10V, V <sub>ds</sub> =15V, I <sub>d</sub> =8.3A		9.36	12.00	nC
Gate-source charge	Q <sub>gs</sub>			2.60		nC
Gate-drain charge	Q <sub>gd</sub>			4.20		nC
Turn-on delay time	t <sub>d(on)</sub>			5.2	7.5	ns
Turn-on rise time	t <sub>r</sub>	V <sub>gs</sub> =10V, V <sub>ds</sub> =15V		4.4	6.5	ns
Turn-off delay time	t <sub>d(off)</sub>	R <sub>l</sub> =1.8Ω, R <sub>gen</sub> =3Ω		17.3	25.0	ns
Turn-off fall time	t <sub>f</sub>			3.3	5.0	ns
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =8.5A, dI/dt=100A/μs		16.7	21.0	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>f</sub> =8.5A, dI/dt=100A/μs		6.7	10.0	nC

#### NOTE :

1. The value of R<sub>θja</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with T<sub>a</sub>=25°C. The value in any given applications depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The R<sub>θja</sub> is the sum of the thermal impedance from junction to lead R<sub>θjl</sub> and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25°C. The SOA curve provides a single pulse rating.

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## Typical electrical and thermal characteristics (Q2)

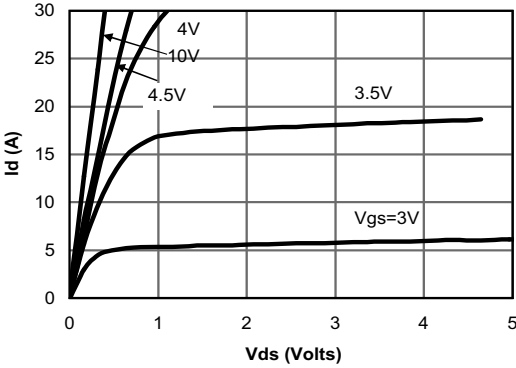


Fig 1: On-Region Characteristics

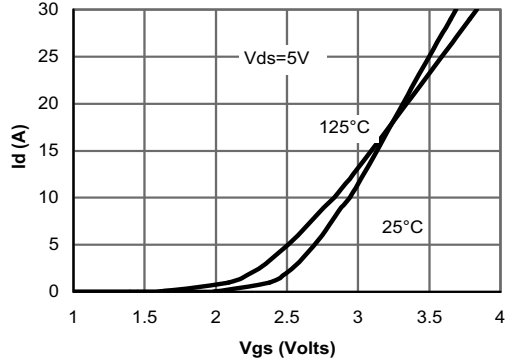


Figure 2: Transfer Characteristics

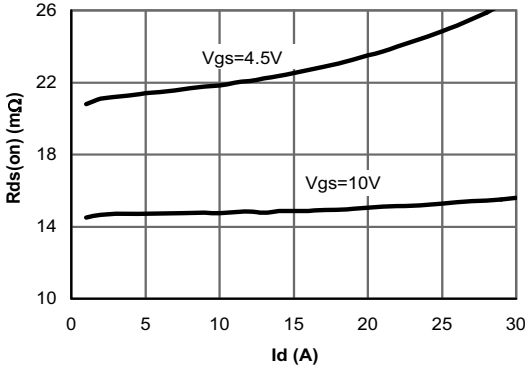


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

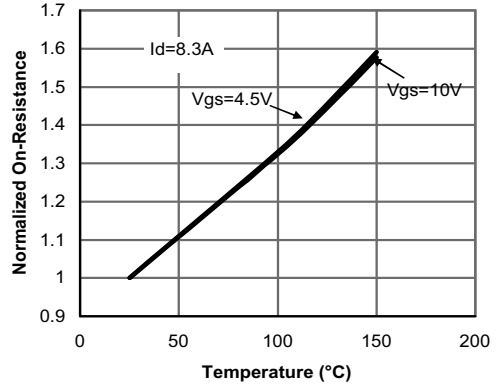


Figure 4: On resistance vs. Junction Temperature

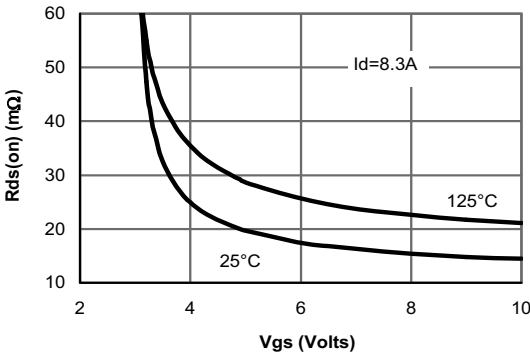


Figure 5: On resistance vs. Gate-Source Voltage

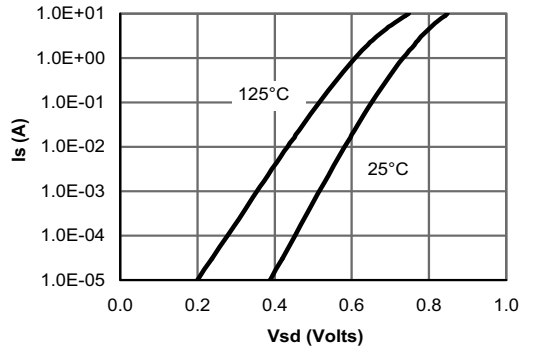


Figure 6: Body-Diode Characteristics

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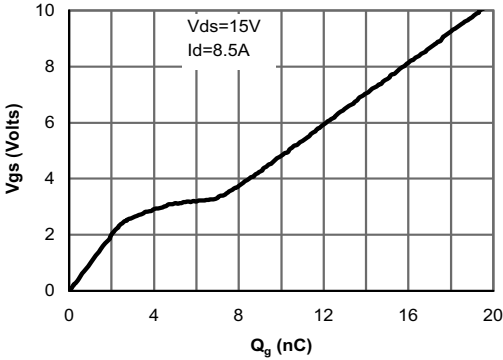


Figure 7: Gate-Charge Characteristics

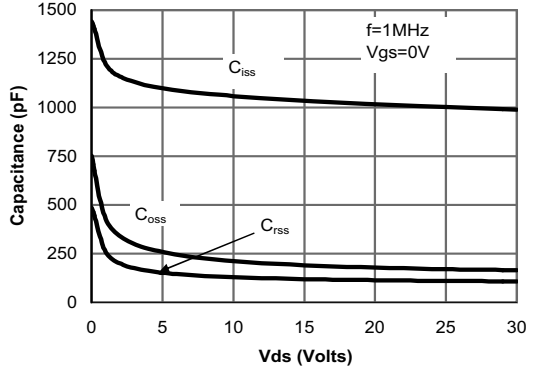


Figure 8: Capacitance Characteristics

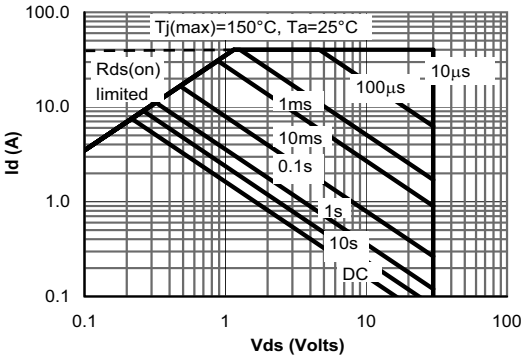


Figure 9: Maximum Forward Biased Safe Operating Area (Note 5)

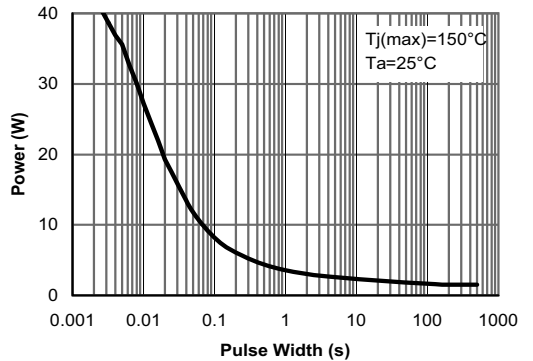


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note 5)

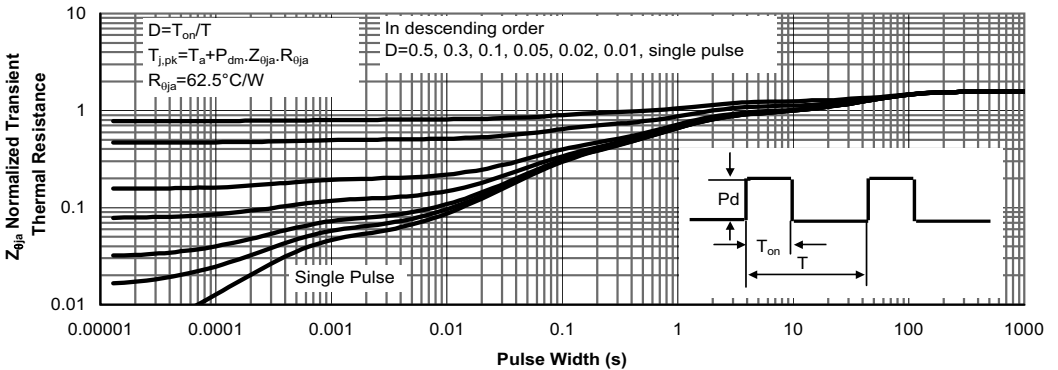


Figure 11: Normalized Maximum Transient Thermal Impedance