

# BR805 THRU BR810

## SINGLE-PHASE SILICON BRIDGE RECTIFIER

VOLTAGE: 50-1000V

CURRENT: 8.0A

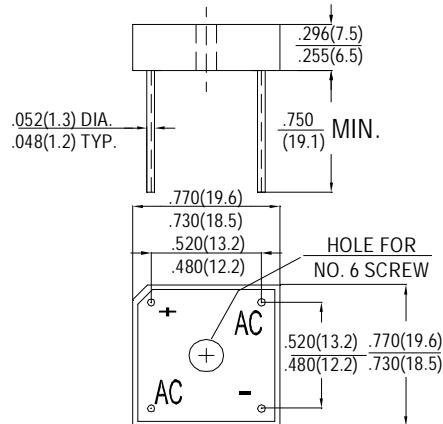
### FEATURES

- Surge overload ratings-125 Amperes
- Low forward voltage drop

### MECHANICAL DATA

- **Case:** Metal or plastic shell with plastic encapsulation
- **Epoxy:** UL 94V-0 rate flame retardant
- **Lead:** MIL-STD- 202E, Method 208 guaranteed
- **Polarity:** Symbols molded or marked on body
- **Mounting:** Thru hole for 6# screw
- **Weight:** 6.9 grams

### KBPC-8/10



Dimensions in inches and (millimeters)

### MAXIMUM RATINGS AND ELECTRONICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	SYMBOL	BR805	BR81	BR82	BR84	BR86	BR88	BR810	units
Maximum Recurrent Peak Reverse Voltage	<b>V<sub>RRM</sub></b>	50	100	200	400	600	800	1000	<b>V</b>
Maximum RMS Bridge Input Voltage	<b>V<sub>RMS</sub></b>	35	70	140	280	420	560	700	<b>V</b>
Maximum DC Blocking Voltage	<b>V<sub>DC</sub></b>	50	100	200	400	600	800	1000	<b>V</b>
Maximum Average Forward rectified Output Current at T <sub>C</sub> =75°C	<b>I<sub>o</sub></b>	8.0							<b>A</b>
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rate load (JEDEC method)	<b>I<sub>FSM</sub></b>	250							<b>A</b>
Maximum Forward Voltage Drop per element at 4.0A DC	<b>V<sub>F</sub></b>	1.1							<b>V</b>
Maximum DC Reverse Current at Rated DC Blocking Voltage per element	@ T <sub>A</sub> =25°C	<b>I<sub>R</sub></b>	10						<b>μA</b>
	@ T <sub>A</sub> =100°C		500						
I <sup>2</sup> t Rating for Fusing (t<8.3ms)	<b>I<sup>2</sup>t</b>	166							<b>A<sup>2</sup>s<sub>ec</sub></b>
Typical Junction Capacitance (Note 1)	<b>C<sub>J</sub></b>	200							<b>pF</b>

Notes: 1. Measured at 1MHz and applied reverse voltage of 4.0 volts

2. Thermal Resistance from Junction to Ambient and from junction to lead mounted on P.C.B. with 0.5×0.5"(13×13mm) copper pads.