LMV321/LMV358/LMV324

GENERAL PURPOSE, LOW VOLTAGE, RAIL-TO-RAIL OUTPUT OPERATIONAL AMPLIFIERS

Features

(For V^+=5V and V^-=0V typical unless otherwise noted)

- Guaranteed 2.7V and 5V performance
- Crossover distortion eliminated
- Operating temperature range (-40°C to +85°C)
- Gain-bandwidth product 1 MHz
- Low supply current
  - LMV321 110 µA Typ
  - LMV358 190 µA Typ
  - LMV324 340 µA Typ
- Rail-to-rail output swing @ 10 kΩ
  - V^+ -10 mV
  - V^- +10 mV
- Input Common Mode Voltage Range (-0.2 to V^+-0.8V)
- Manufactured in standard CMOS process
- SOT353, SOT25, MSOP-8L, SOP-8L and TSSOP-14L: Available in "Green" Molding Compound (No Br, Sb)
- Lead-free Finish/ RoHS Compliant (Note 4)

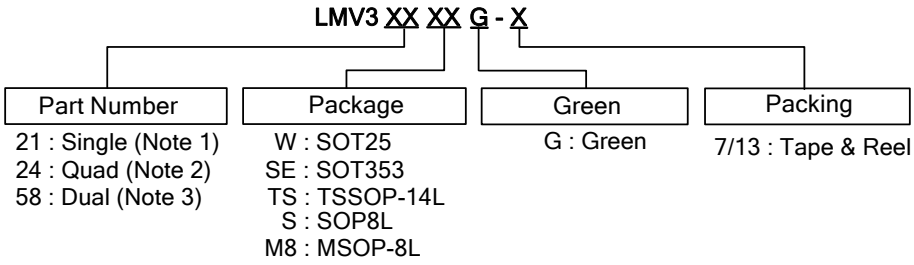
General Description

The LMV321/LMV358/LMV324 are low voltage (2.7V to 5.5V) single, dual and quad operational amplifiers. The LMV321/LMV358/LMV324 are designed to effectively reduce cost and space at low voltage levels. These devices have the capability of rail-to-rail output swing and input common-mode voltage range includes ground. They can also achieve an efficient speed-to-power ratio, utilizing 1 MHz bandwidth and 1 V/µs slew rate at a low supply current. Reducing noise pickup and increasing signal integrity can be achieved by placing the device close to the signal source. The LMV321 is available in 5-Pin SOT353/SOT25 packages that reduce space on pc boards and portable electronic devices. The LMV324 is available in the TSSOP-14L package. The LMV358 is available in the MSOP-8L and SOP-8L packages.

Applications

- Active filters
- General purpose low voltage applications
- General purpose portable devices

Ordering Information

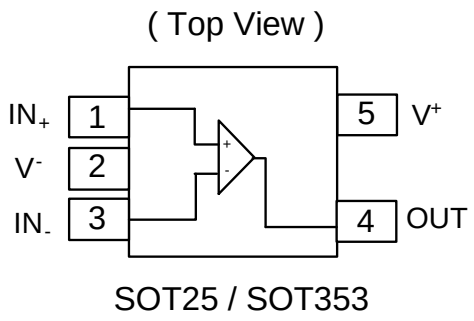


Device	Package Code	Packaging (Note 5)	7"/13" Tape and Reel	
			Quantity	Part Number Suffix
LMV321WG-7	W	SOT25	3000/Tape & Reel	-7
LMV321SEG-7	SE	SOT353	3000/Tape & Reel	-7
LMV324TSG-13	TS	TSSOP-14L	2500/Tape & Reel	-13
LMV358SG-13	S	SOP-8L	2500/Tape & Reel	-13
LMV358M8G-13	M8	MSOP-8L	2500/Tape & Reel	-13

Notes:
1. LMV321 is only available for SOT25 and SOT353.
2. LMV324 is only available for TSSOP-14L.
3. LMV358 is only available for SOP-8L and MSOP-8L.
4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html
5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

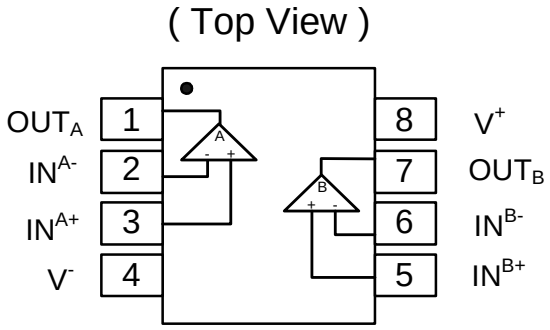
Pin Assignments

(1) SOT25 / SOT353



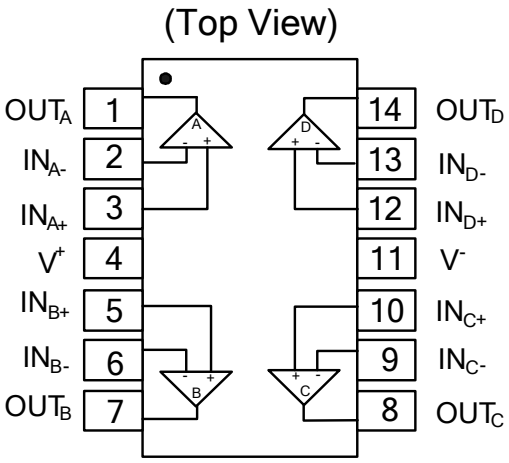
SOT25 / SOT353

(2) SOP-8L / MSOP-8L



SOP-8L / MSOP-8L

(3) TSSOP-14L



TSSOP-14L

Absolute Maximum Ratings (Note 6)

Symbol	Description		Rating	Unit
ESD HBM	Human Body Model ESD Protection	LMV321	4.0	KV
		LMV358	4.0	
		LMV324	4.5	
ESD MM	Machine Model ESD Protection	LMV321	350	V
		LMV358	350	
		LMV324	250	
	Differential Input Voltage		±Supply Voltage	V
V^+ -V^-	Supply Voltage		5.5	V
	Output Short Circuit to V^+		(Note 7)	
	Output Short Circuit to V^-		(Note 8)	
T_{ST}	Storage Temperature		-65 to 150	°C
T_J	Maximum Junction Temperature		150	°C

Operating Ratings (Note 6)

Symbol	Description	Rating	Unit
V^+ -V^-	Supply Voltage	2.7 to 5.5	V
T_A	Operating Ambient Temperature Range	-40 to +85	°C

Notes: 6. Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

Electrical Characteristics

2.7V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = 25°C, V^+ = 2.7V, V^- = 0V, V_{CM} = 1.0V, V_O = V^+/2 and R_L > 1 MΩ.

Symbol	Parameter	Test Conditions	Min (Note 10)	Typ. (Note 9)	Max (Note 10)	Unit
V_{OS}	Input Offset Voltage			1.7	7	mV
TCV_{OS}	Input Offset Voltage Average Drift			5		µV/°C
I_B	Input Bias Current			10		nA
I_{OS}	Input Offset Current			5	50	nA
CMRR	Common Mode Rejection Ratio	0V ≤ V_{CM} ≤ 1.7V	50	63		dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V^+ ≤ 5V V_O = 1V	50	60		dB
V_{CMR}	Input Common-Mode Voltage Range	For CMRR ≥ 50dB	0	-0.2		V
				1.9	1.7	
V_O	Output Swing	R_L = 10 kΩ to 1.35V	V^+ - 100	V^+ - 20		mV
				20	100	
I_S	Supply Current	LMV321 Single amplifier		110	140	µA
		LMV358 Both amplifiers		190	340	µA
		LMV324 All four amplifiers		340	680	µA

2.7V AC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = 25°C, V^+ = 2.7V, V^- = 0V, V_{CM} = 1.0V, V_O = V^+/2 and R_L > 1 MΩ.

Symbol	Parameter	Test Conditions	Min (Note 10)	Typ. (Note 9)	Max (Note 10)	Unit
GBWP	Gain-Bandwidth Product	C_L = 200 pF		1		MHz
Φm	Phase Margin			60		Deg
Gm	Gain Margin			10		dB
e_n	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{nV}{\sqrt{Hz}}$

Electrical Characteristics (Continued)

5V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = 25°C, V^+ = 5V, V^- = 0V, V_{CM} = 2.0V, V_O = V^+/2 and R_L > 1 MΩ. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Test Conditions	Min (Note 10)	Typ. (Note 9)	Max (Note 10)	Unit
V_{OS}	Input Offset Voltage			1.7	7 **9**	mV
TCV_{OS}	Input Offset Voltage Average Drift			5		µV/°C
I_B	Input Bias Current			15	250 **500**	nA
I_{OS}	Input Offset Current			5	50 **150**	nA
CMRR	Common Mode Rejection Ratio	0V ≤ V_{CM} ≤ 4.0V	50	65		dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V^+ ≤ 5V V_O = 1V, V_{CM} = 1V	50	60		dB
V_{CMR}	Input Common-Mode Voltage Range	For CMRR ≥ 50dB	0	-0.2		V
				4.2	4.0	
A_V	Large Signal Voltage Gain	R_L = 2 kΩ (Note 11)	15 **10**	100		V/mV
V_O	Output Swing	R_L = 2 kΩ to 2.5V	V^+ - 300 **V^+ - 400**	V^+ - 50		mV
				50	300 **400**	mV
		R_L = 10 kΩ to 2.5V	V^+ - 100 **V^+ - 200**	V^+ - 10		mV
				10	180 **280**	mV
I_O	Output Short Circuit Current	Sourcing, V_O = 0V	5	60		mA
		Sinking, V_O = 5V	10	90		mA
I_S	Supply Current	LMV321 Single amplifier		110	140	µA
		LMV358 Both amplifiers		190	340 **600**	µA
		LMV324 All four amplifiers		340	680 **1100**	µA
θ_{JA}	Thermal Resistance Junction-to-Ambient	SOT353 (Note 12)		330		°C/W
		SOT25 (Note 12)		250		°C/W
		TSSOP-14L (Note 12)		100		°C/W
		MSOP-8L (Note 12)		203		°C/W
		SOP-8L (Note 12)		150		°C/W

Electrical Characteristics (Continued)

5V AC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = 25°C, V^+ = 5V, V^- = 0V, V_{CM} = 2.0V, VO = V^+/2 and R_L > 1 MΩ. **Boldface** limits apply at the temperature extremes.

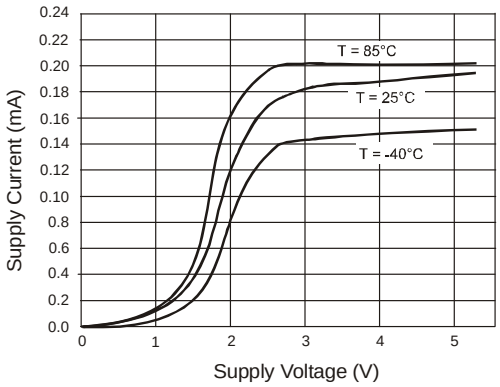
Symbol	Parameter	Test Conditions	Min (Note 10)	Typ. (Note 9)	Max (Note 10)	Unit
SR	Slew Rate	(Note 13)		1		V/µs
GBWP	Gain-Bandwidth Product	C_L = 200 pF		1		MHz
Φ_m	Phase Margin			60		Deg
G_m	Gain Margin			10		dB
e_n	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{nV}{\sqrt{H_Z}}$

Notes:
7. Shorting output to V+ will adversely affect reliability.
8. Shorting output to V- will adversely affect reliability.
9. Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not guaranteed on shipped production material.
10. All limits are guaranteed by testing or statistical analysis.
11. R_L is connected to V-. The output voltage is $0.5V \le V_O \le 4.5V$.
12. All numbers are typical, and apply for packages soldered directly onto a PC board in still air.
13. Connected as voltage follower with 3V step input. Number specified is the slower of the positive and negative slew rates.

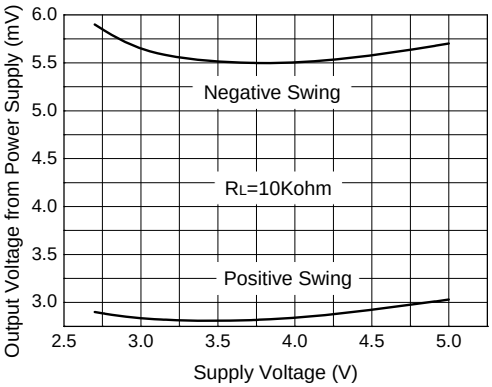
Typical Performance Characteristics

Unless otherwise specified, Vs=+5V, single supply, T_A=25°C

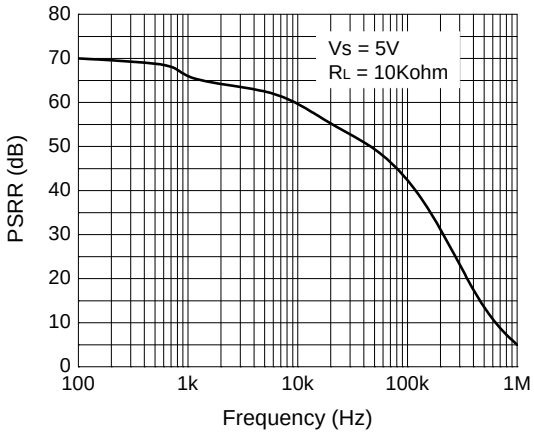
Supply Current vs. Supply Voltage



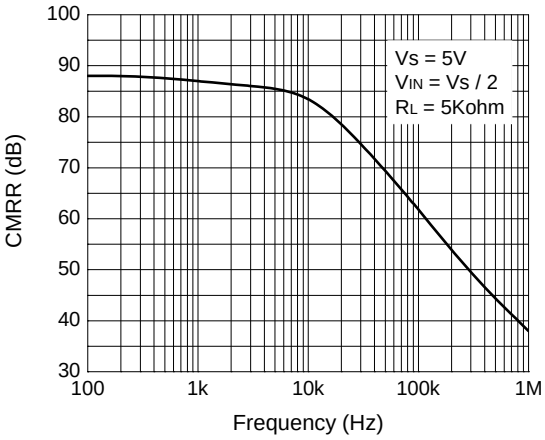
Output Voltage Swing vs. Supply Voltage



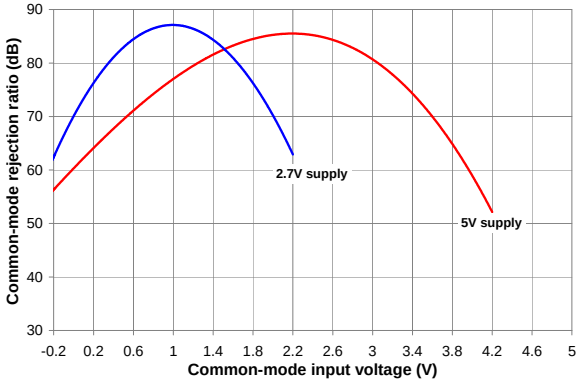
PSRR vs. Frequency



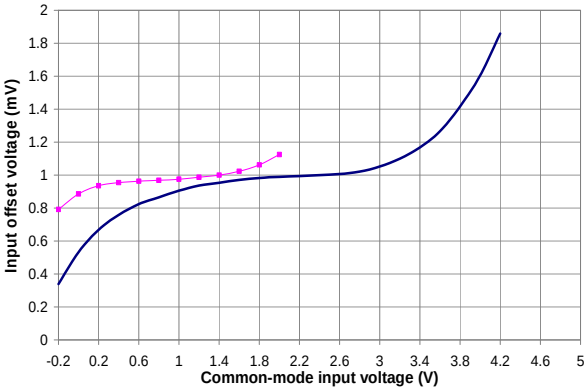
CMRR vs. Frequency



CMRR vs. Input Common Mode Voltage

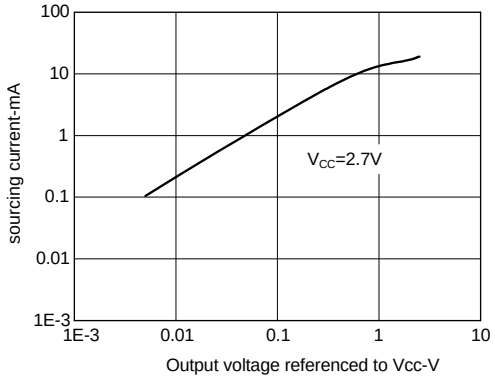


ΔVos vs. CMR

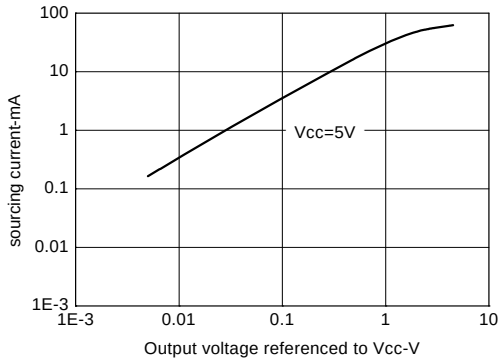


Typical Performance Characteristics (Continued)

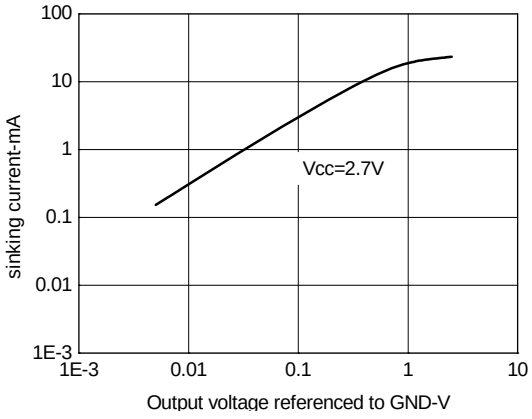
Sourcing Current vs. Output Voltage (2.7V)



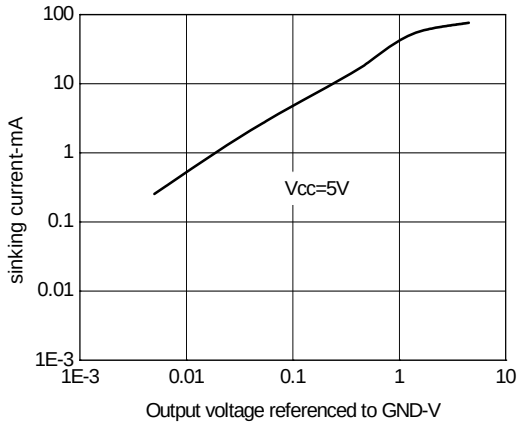
Sourcing Current vs. Output Voltage (5V)



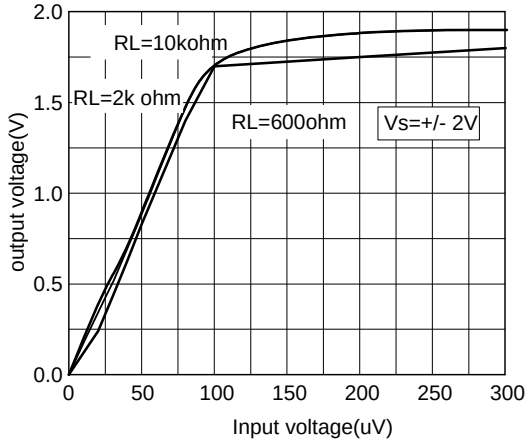
Sinking Current vs. Output Voltage (2.7V)



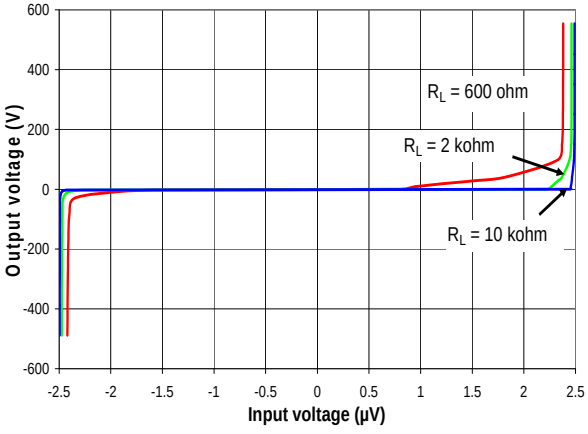
Sinking Current vs. Output Voltage (5V)



Input Voltage vs. Output Voltage

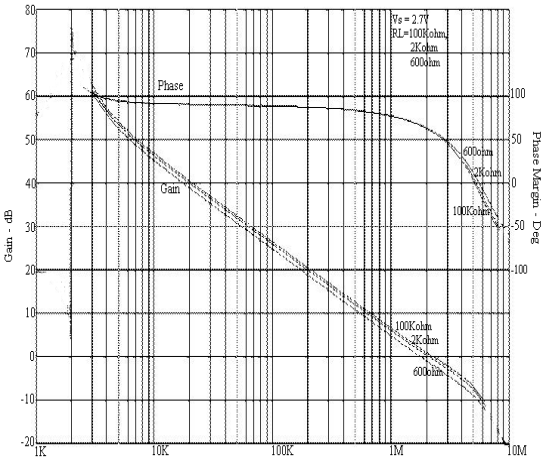


Output voltage vs. input voltage

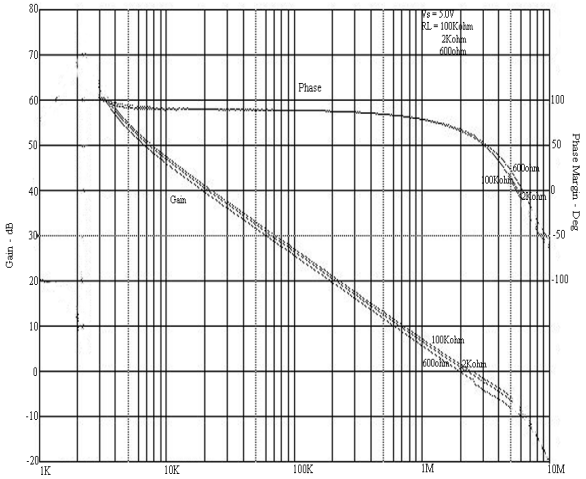


Typical Performance Characteristics (Continued)

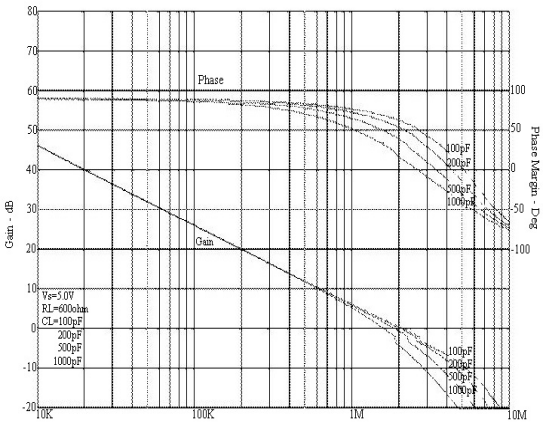
Frequency Response vs. Resistive Load (2.7V)



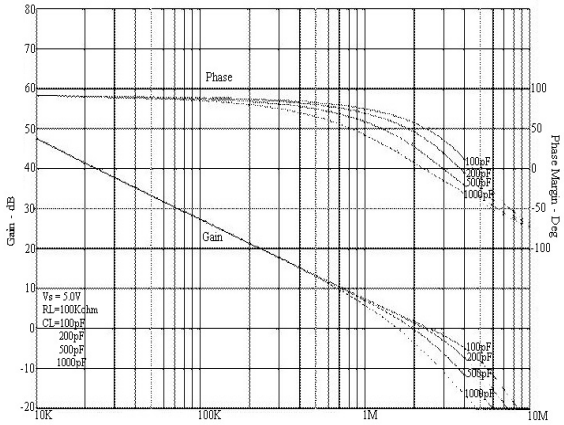
Frequency Response vs. Resistive Load (5V)



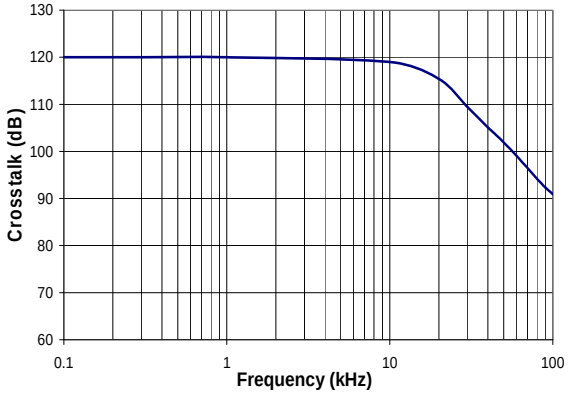
Frequency Response vs. Capacitive Load (2.7V)



Frequency Response vs. Capacitive Load (5V)

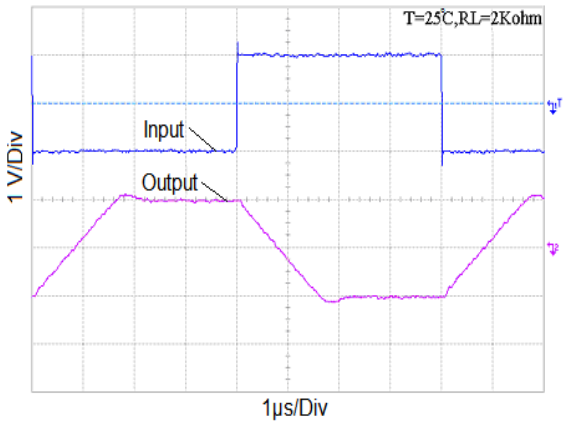


Crosstalk vs. Frequency

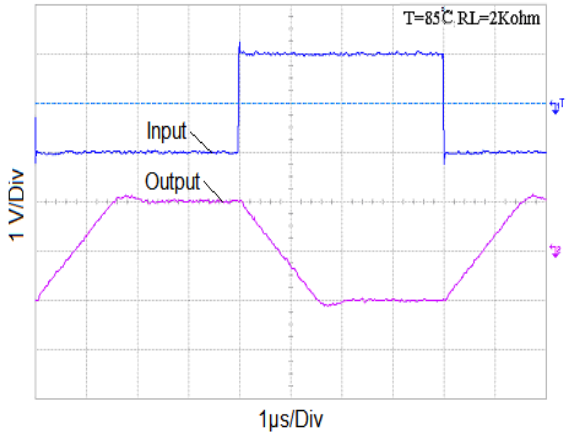


Typical Performance Characteristics (Continued)

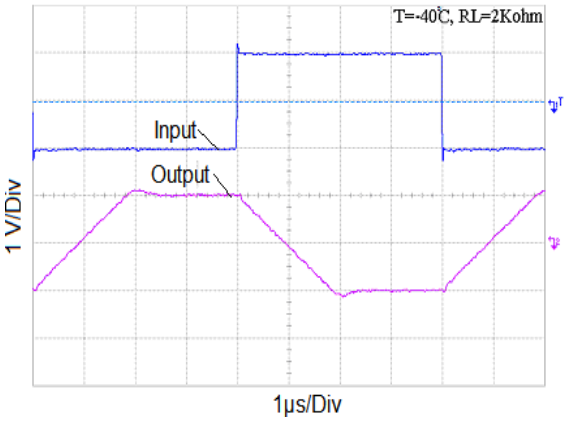
Inverting Large Signal Pulse Response



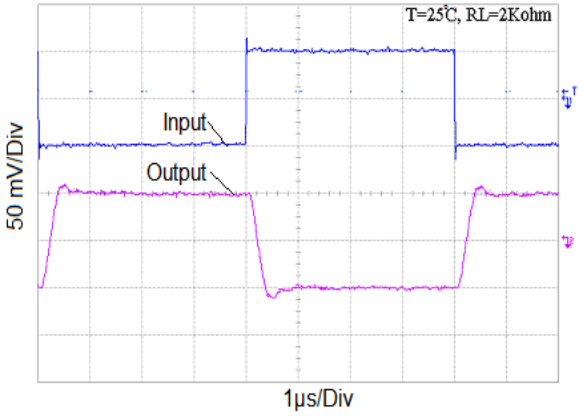
Inverting Large Signal Pulse Response



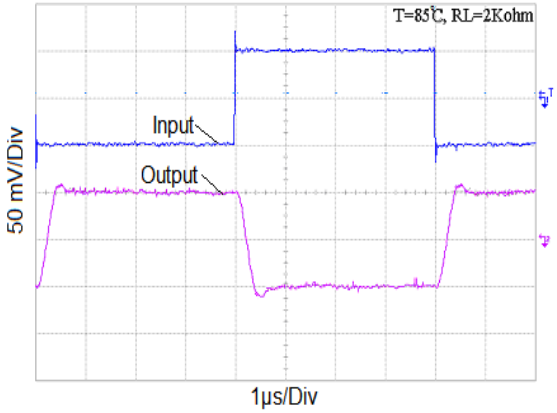
Inverting Large Signal Pulse Response



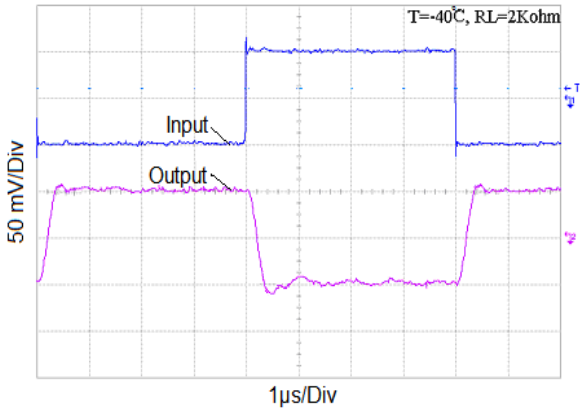
Inverting Small Signal Pulse Response



Inverting Small Signal Pulse Response

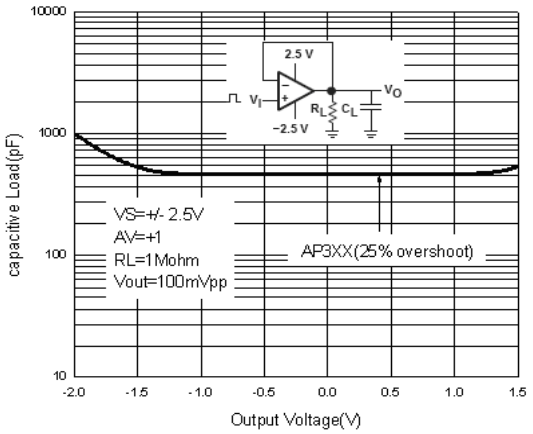


Inverting Small Signal Pulse Response

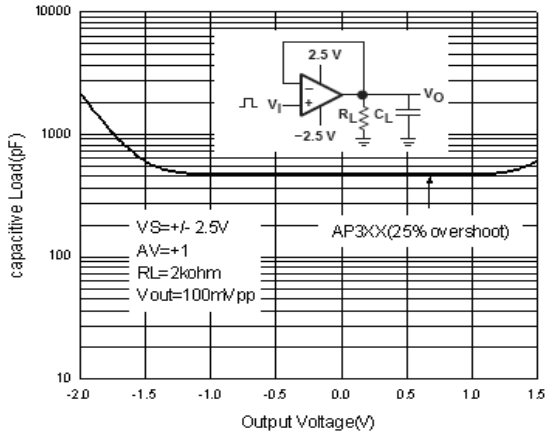


Typical Performance Characteristics (Continued)

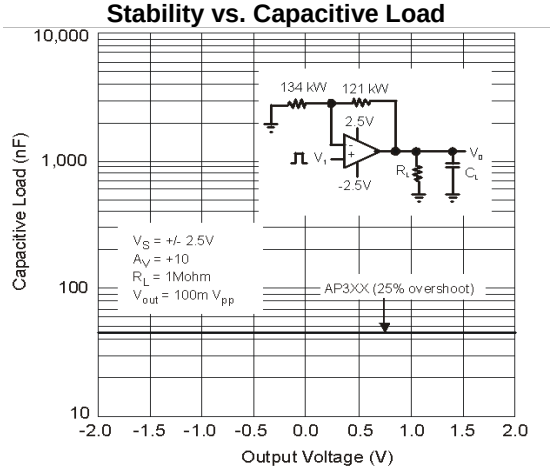
Stability vs. Capacitive Load



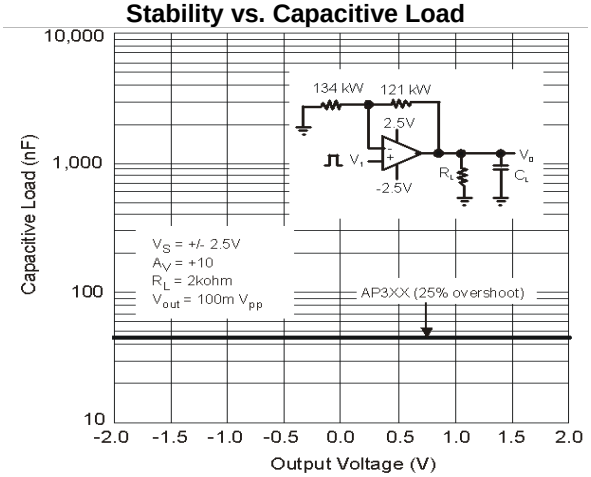
Stability vs. Capacitive Load



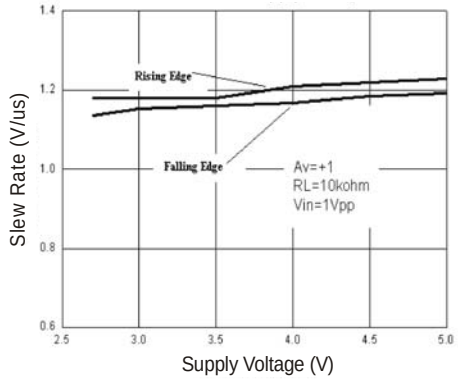
Stability vs. Capacitive Load



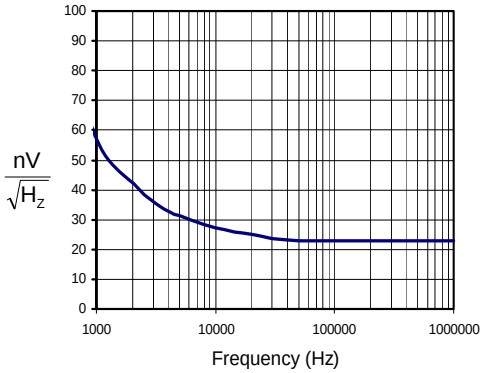
Stability vs. Capacitive Load



Slew Rate vs. Supply Voltage



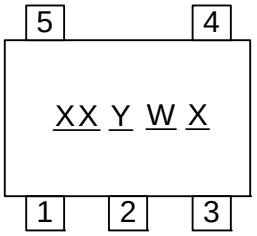
Input Voltage Noise



Marking Information

(1) SOT25 / SOT353

(Top View)

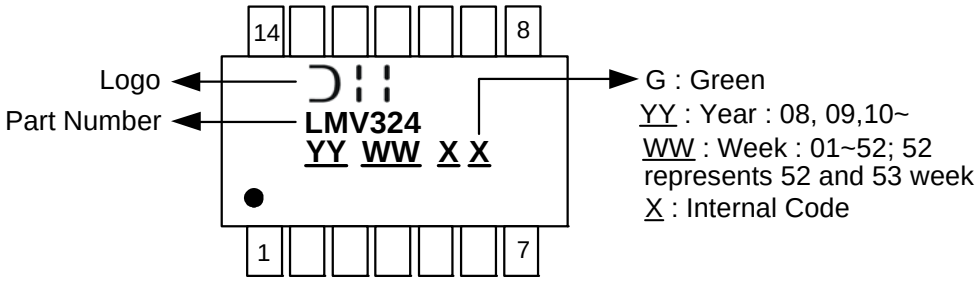


XX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week;
z represents 52 and 53 week
X : A~Z : Green

Device	Package type	Identification Code
LMV321W	SOT25	BX
LMV321SE	SOT353	BY

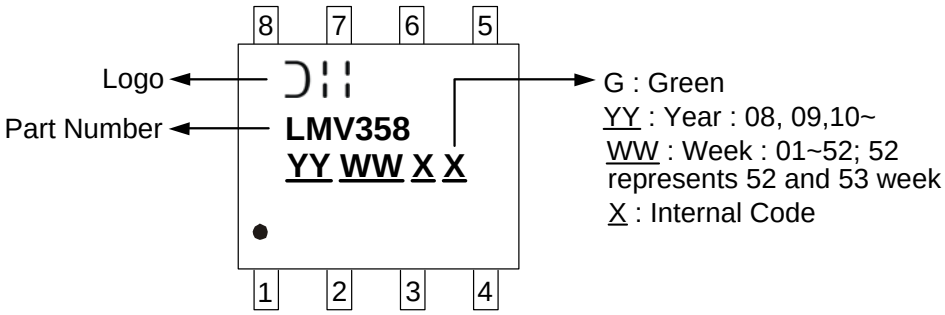
(2) TSSOP-14L

(Top View)



(3) SOP-8L

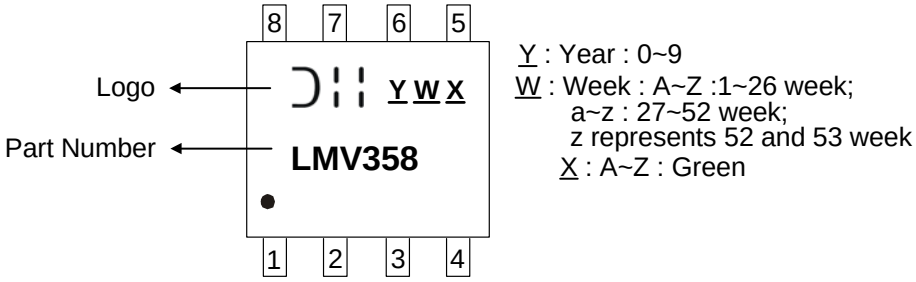
(Top view)



Marking Information (Continued)

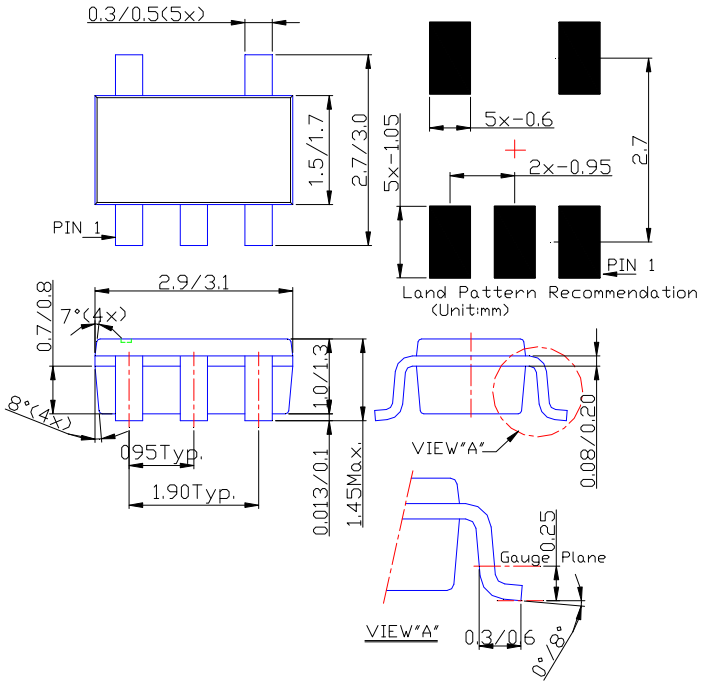
(4) MSOP-8L

(Top view)



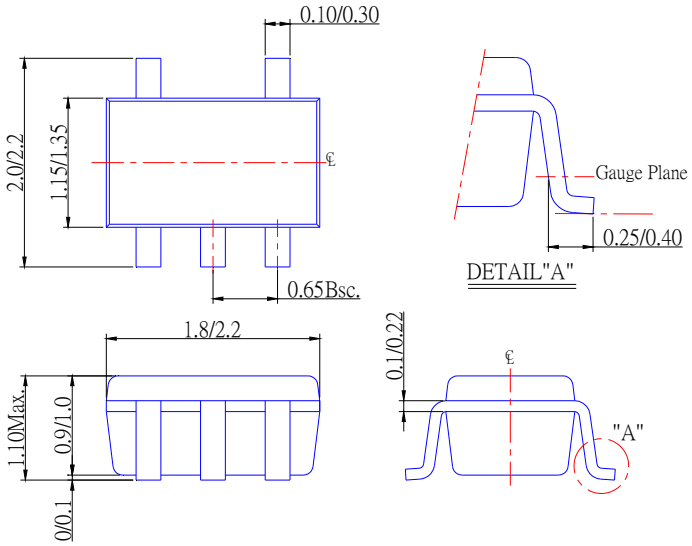
Package Information (All Dimensions in mm)

(1) Package Type: SOT25

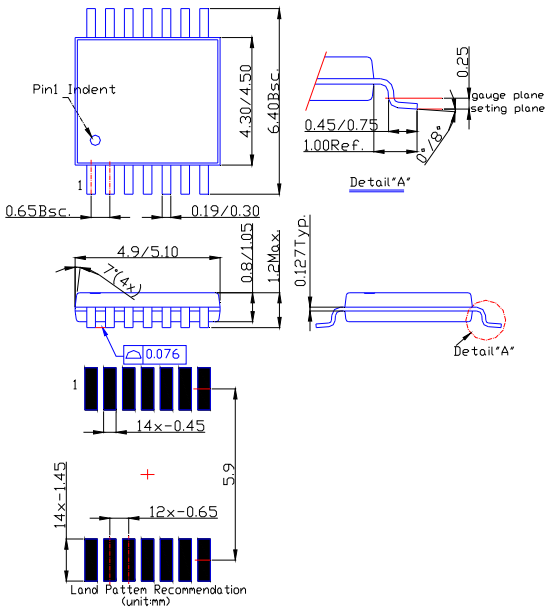


Package Information (Continued)

(2) Package Type: SOT353

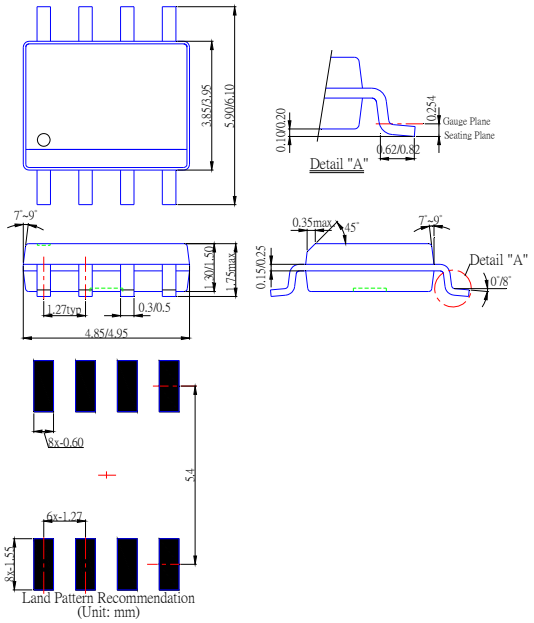


(3) Package Type: TSSOP-14L

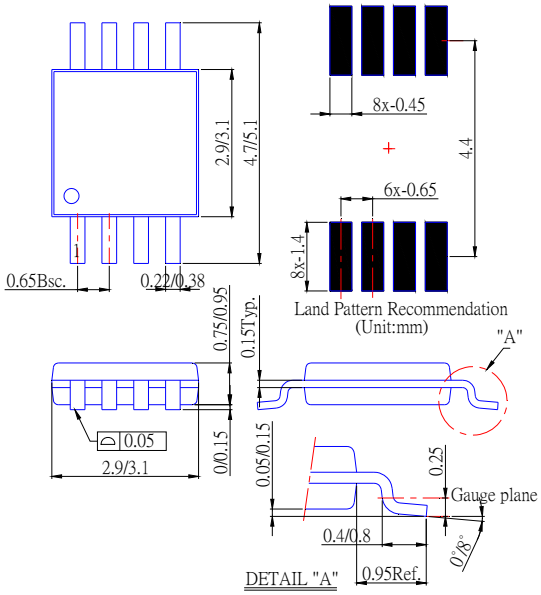


Package Information (Continued)

(4) Package Type: SOP-8L



(5) Package Type: MSOP-8L



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