

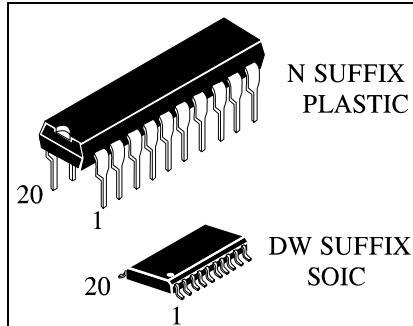
Octal d-type latch (3-state)

KK74VHCT373 is designed for using in up-to-date high performance computers, high-level electronic equipment for mass application.

KK74VHCT373 is identical in pinout to the ICs KK74HC373A, KK74HCT373A, KK74AC373A, KK74ACT373A.

Input voltage levels are compatible with standard TTL-levels. Output voltage levels are compatible with input levels of C-MOS, N-MOS and TTL ICs.

KK74VHCT373



ORDERING INFORMATION

KK74VHCT373N Plastic

KK74VHCT373DW SOIC

T_A = -40° to 85° C for all packages

Features:

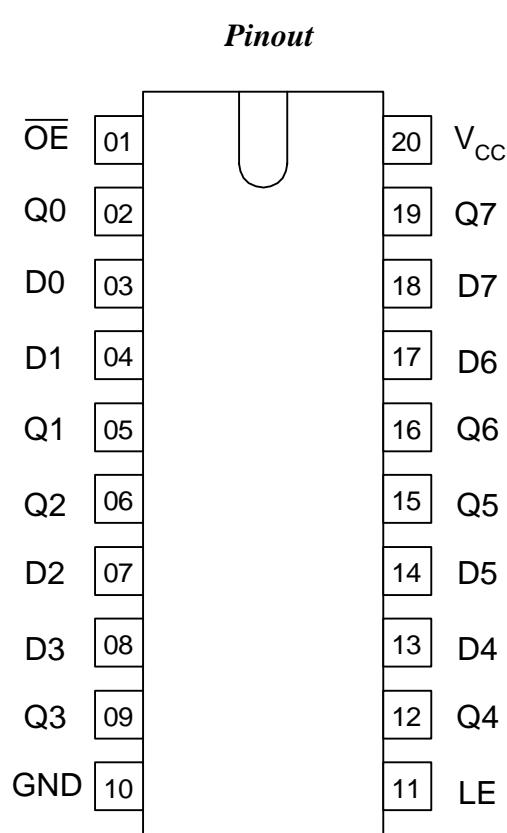
- Supply voltage range 4.5 to 5.5 V.
- Output current 8 mA.
- Low consumption current: 0.2 µA (typical value) at T_A = 25 °C.
- Latchup current not less than 300 mA at T_A = 85 °C.
- Tolerable value of static potential not less than 2000 V as per human body model (HBM) and not less than 200 V as per machine model (MM).
- Ambient operation temperature minus 40 to plus 85 °C.
- Balanced signal propagation delay.
- Ensures voltage exceeding mode on input

KK74VHCT373 truth table

Input			Output
\overline{OE}	LE	D	Q
L	H	H	H
L	H	L	L
L	L	X	Q_0
H	X	X	Z

Note –

H - high voltage level;
L - low voltage level;
X - any voltage level (low or high);
 Q_0 - storage of the previous state;
Z - output in the third state

Pins description in KK74VHCT373


Pin No.	Symbol	Description
01	\overline{OE}	<i>Input OUTPUT ENABLE</i>
02	Q_0	Data output
03	D_0	Data input
04	D_1	Data input
05	Q_1	Data output
06	Q_2	Data output
07	D_2	Data input
08	D_3	Data input
09	Q_3	Data output
10	GND	Common output
11	LE	Recording enable input
12	Q_4	Data output
13	D_4	Data input
14	D_5	Data input
15	Q_5	Data output
16	Q_6	Data output
17	D_6	Data input
18	D_7	Data input
19	Q_7	Data output
20	V_{CC}	Supply output from voltage source

Absolute maximum conditions*

Parameter, unit	Symbol	Value	
		min	max
Supply voltage, V	V _{CC}	-0.5	7.0
Input voltage, V	V _{in}	-0.5	7.0
Output voltage, V	V _{out}	-0.5	V _{CC} + 0.5B
Output voltage, V	V _{out1}	-0.5	7.0
Input diode current, mA	I _{ik}	-	-20
Current of common output and supply output, mA	I _{cc}		±75
Output current, mA	I _{out}		±25
Output diode current, mA	I _{ok}		±20
Dissipated power, mW	P _d		180

*Under absolute maximum conditions operation of microcircuit is not guaranteed. Operation is guaranteed under maximum conditions

Maximum conditions

Parameter, unit	Symbol	Value	
		min	max
Supply voltage, V	V _{CC}	4.5	5.5
Input voltage, V	V _{in}	0	V _{CC}
Output voltage, V	V _{out}	0	V _{CC}
Output voltage, V	V _{out1}	0	5.5*
Output current, mA	I _{out}	-	±8.0
Input rise and fall time, ns/V	t _{LH} , t _{HL}	0	20

* - For ICs without third state on outputs – V_{CC} = 0V, for ICs with third state on outputs – outputs should be in third state

DC electrical characteristics

Symbol I	Parameter	Test conditions	V _{CC} , V	Value				Unit	
				25 °C		-40 to 85 °C			
				min	max	min	max		
V _{IH}	High input voltage	V _O ≤ 0.1 V or V _O ≥ V _{CC} - 0.1	4.5 – 5.5	2.0	-	2.0	-	V	
V _{IL}	Low input voltage	V _O ≤ 0.1 V or V _O ≥ V _{CC} - 0.1	4.5 – 5.5	-	0.8	-	0.8		
V _{OH}	High output voltage	V _I = V _{IH} or V _{IL} I _O = -50 μA	4.5	4.42	-	4.4	-		
			5.5	5.42	-	5.4	-		
		V _I = V _{IH} or V _{IL} ; I _O = -8 mA	4.5	3.94	-	3.80	-		
V _{OL}	Low output voltage	V _I = V _{IH} or V _{IL} I _O = 50 μA	4.5	-	0.09	-	0.1		
			5.5	-	0.09	-	0.1		
		V _I = V _{IH} or V _{IL} I _O = 8 mA	4.5	-	0.36	-	0.44		
I _{OZ}	Output current in "off" state	V _I = 2.0V V _O = V _{CC} or 0V	5.5	-	±0.25	-	±2.5	uA	
I _I	Input current	V _I = 0 V or V _{CC}	5.5		±0.1		±1.0		
I _{IH1}	High level input current	V _I = 5.5V	0	-	±0.1	-	±1.0		
I _{CC}	Consumption current	V _I = V _{CC} or 0V	5.5	-	4.0	-	40.0		
I _{CCT}	TTL-input consumption current	V _I = 3.4 V	5.5	-	1.35	-	1.5	mA	

AC electrical characteristics ($t_{LH} = t_{HL} = 3.0$ ns, $R_L = 1$ kOhm)

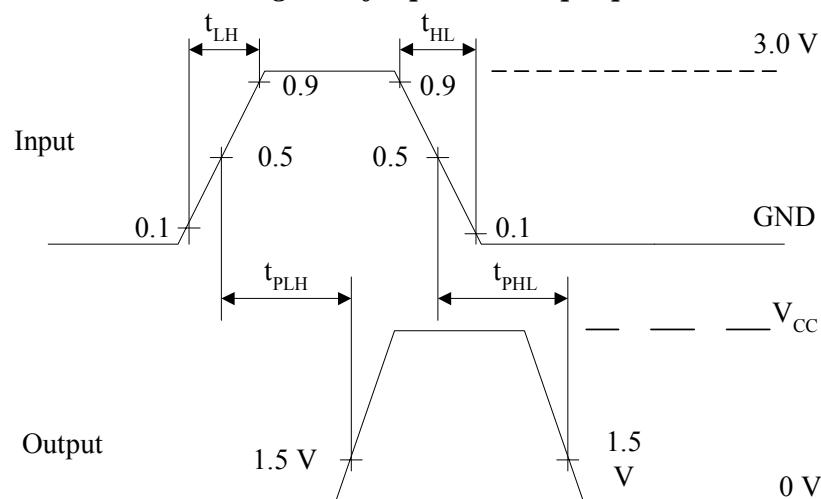
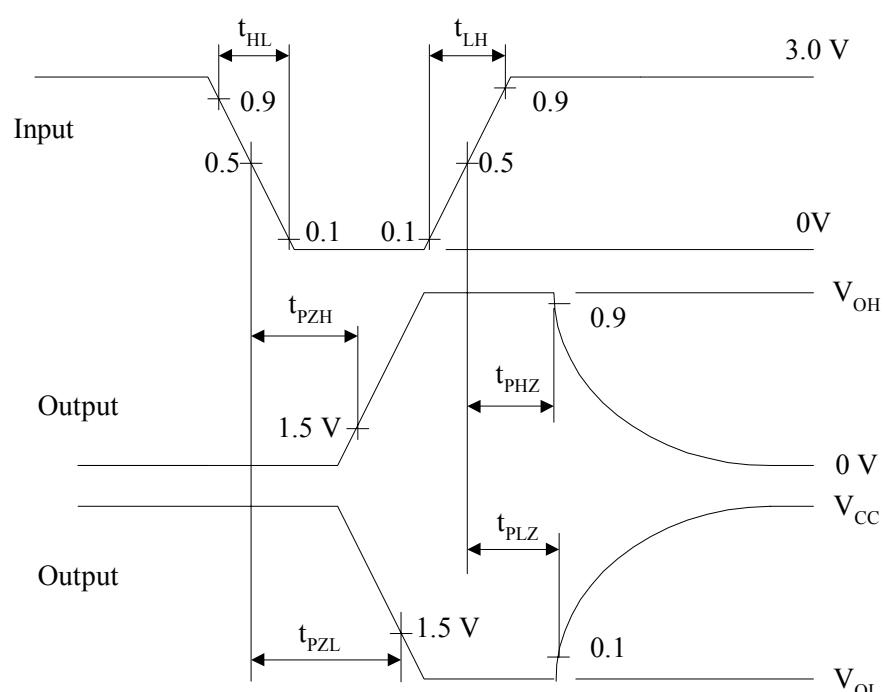
Symbol	Parameter	Test conditions	V_{CC} , V	C_L , pF	Value				Unit	
					25 °C		-40 to 85 °C			
					min	max	min	max		
t_{PHL}, t_{PLH}	Propagation delay time when switching «on», «off» from input LE to output Q	Fig 1	5.0 ± 0.5	15	—	12.3	—	13.5	ns	
				50	—	13.3	—	14.5		
t_{PHL}, t_{PLH}	From input D to output Q	Fig 1	5.0 ± 0.5	15	—	8.5	—	9.5	ns	
				50	—	9.5	—	10.5		
t_{PHZ}, t_{PLZ}	Propagation delay time under transition from high, low level into «off» state	Fig 2	5.0 ± 0.5	50	—	11.2	—	12.0	ns	
t_{PZH}, t_{PZL}	Propagation delay time under transition from «off» state into high, low level	Fig 2	5.0 ± 0.5	15	—	10.9	—	12.5	ns	
				50	—	11.9	—	13.5		
t_{SU}	Time of setting signal D relativey to LE	Fig 3	5.0 ± 0.5	15	1.5	—	1.5	—	ns	
				50	1.5	—	1.5	—		
t_H	Retention time, D signal to LE	Fig 3	5.0 ± 0.5	15	3.5	—	3.5	—		
				50	3.5	—	3.5	—		
t_W	Pulse duration of LE signal	Fig 3	5.0 ± 0.5	15	6.5	—	8.5	—		
				50	6.5	—	8.5	—		
t_{OSLH}, t_{OSHSL}	Propagation delays difference between outputs	—	5.5 ± 0.5	50	—	1.0	—	1.0		

Capacitance characteristics

Symbol	Parameter	Test conditions	V_{CC} , V	Value		Unit	
				25 °C			
				min	max		
C_I	Input capacity	-	5.0		10	pF	
C_O	Output capacity	-	5.0		12	pF	
C_{PD}	Dynamic capacity	$V_I = 0$ V or V_{CC}	5.0		50	pF	

Noise characteristics ($C_L = 50 \text{ pF}$)

Symbol	Parameter	V_{CC}, V	Value		Unit
			min	max	
V_{OLP}	Positive noise of low output voltage	5.0	-	1.6	V
V_{OLV}	Negative noise of low output voltage	5.0	-1.6	-	
V_{IHD}	Input dynamic high voltage	5.0	2.0		
V_{ILD}	Input dynamic low voltage	5.0		0.8	

-- Time diagram of input and output pulses

Fig. 1

Fig.2

Time diagram of input pulses

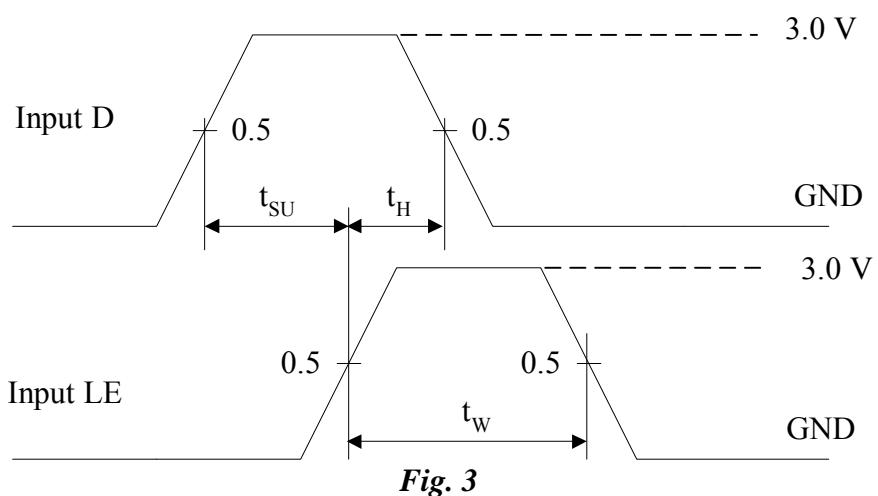
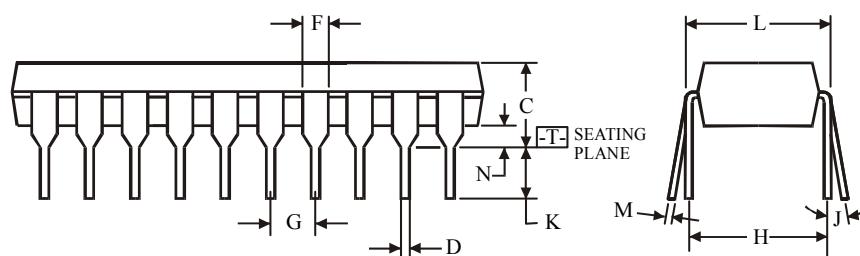
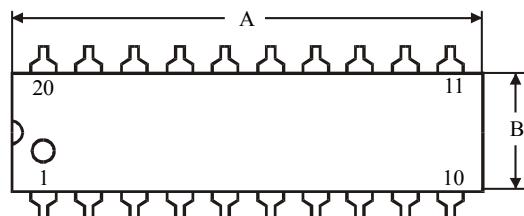


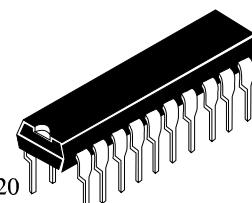
Fig. 3

**N SUFFIX PLASTIC DIP
(MS - 001AD)**


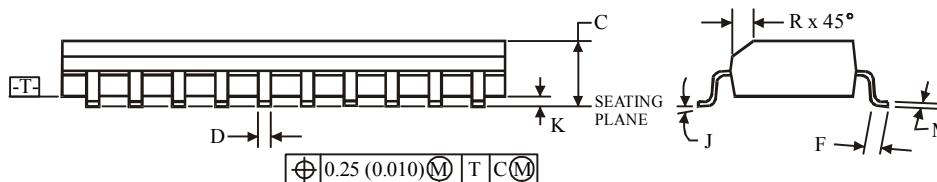
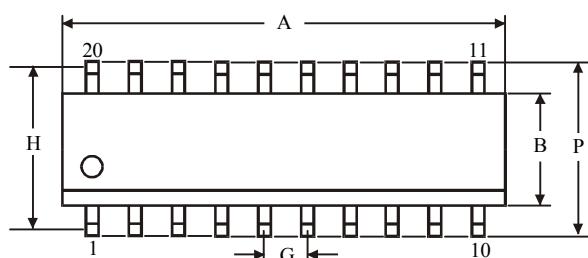
NOTES: $\oplus 0.25$ (0.010) \ominus T

- Dimensions "A", "B" do not include mold flash or protrusions.

Maximum mold flash or protrusions 0.25 mm (0.010) per side.



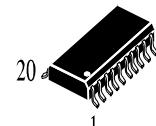
	Dimension, mm	
Symbol	MIN	MAX
A	24.89	26.92
B	6.1	7.11
C		5.33
D	0.36	0.56
F	1.14	1.78
G		2.54
H		7.62
J	0°	10°
K	2.92	3.81
L	7.62	8.26
M	0.2	0.36
N	0.38	

**D SUFFIX SOIC
(MS - 013AC)**


$\oplus 0.25$ (0.010) \ominus T \ominus C \ominus M

NOTES:

- Dimensions A and B do not include mold flash or protrusion.
- Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B - 0.25 mm (0.010) per side.



	Dimension, mm	
Symbol	MIN	MAX
A	12.6	13
B	7.4	7.6
C	2.35	2.65
D	0.33	0.51
F	0.4	1.27
G		1.27
H		9.53
J	0°	8°
K	0.1	0.3
M	0.23	0.32
P	10	10.65
R	0.25	0.75