

Silicon NPN Power Transistors

2SD1792

DESCRIPTION

- With ITO-220 package
- Switching power transistor
- DARLINGTON

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

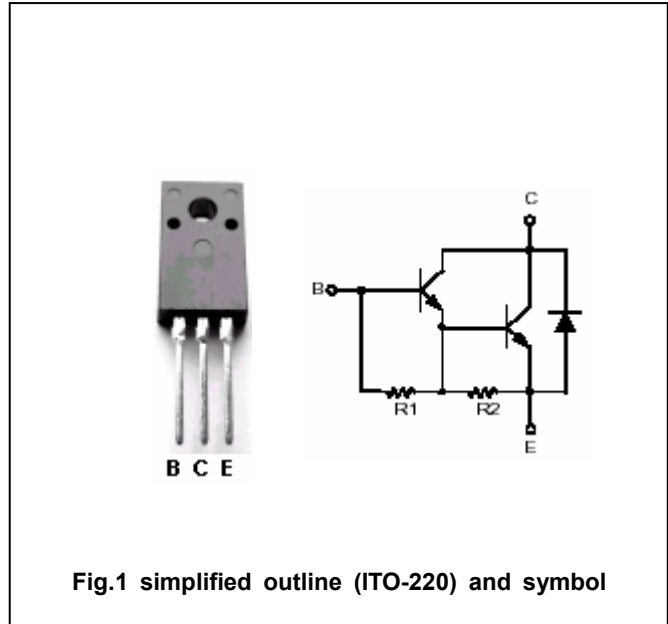


Fig.1 simplified outline (ITO-220) and symbol

Absolute maximum ratings($T_a=25^\circ$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	200	V
V_{CEO}	Collector-emitter voltage	Open base	200	V
V_{EBO}	Emitter-base voltage	Open collector	7	V
I_C	Collector current		7	A
I_{CM}	Collector current-Peak		10	A
I_B	Base current		0.5	A
I_{BM}	Base current-Peak		1.0	A
P_T	Total power dissipation	$T_C=25^\circ$	30	W
T_j	Junction temperature		150	$^\circ$
T_{stg}	Storage temperature		-55~150	$^\circ$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal resistance junction to case	4.16	$^\circ/W$

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A; I _B =5mA			1.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =3A; I _B =5mA			2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =200V; I _E =0			0.1	mA
I _{CEO}	Collector cut-off current	V _{CE} =200V; I _B =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			5	mA
h _{FE}	DC current gain	I _C =3A; V _{CE} =3V	1500		30000	
f _T	Transition frequency	I _C =0.7A; V _{CE} =10V		20		MHz

Switching times

t _{on}	Turn-on time	I _C =3A; I _{B1} =I _{B2} =5mA, R _L =10Ω V _{BB2} =4V			2.0	μs
t _s	Storage time				12	μs
t _f	Fall time				5.0	μs

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PACKAGE OUTLINE

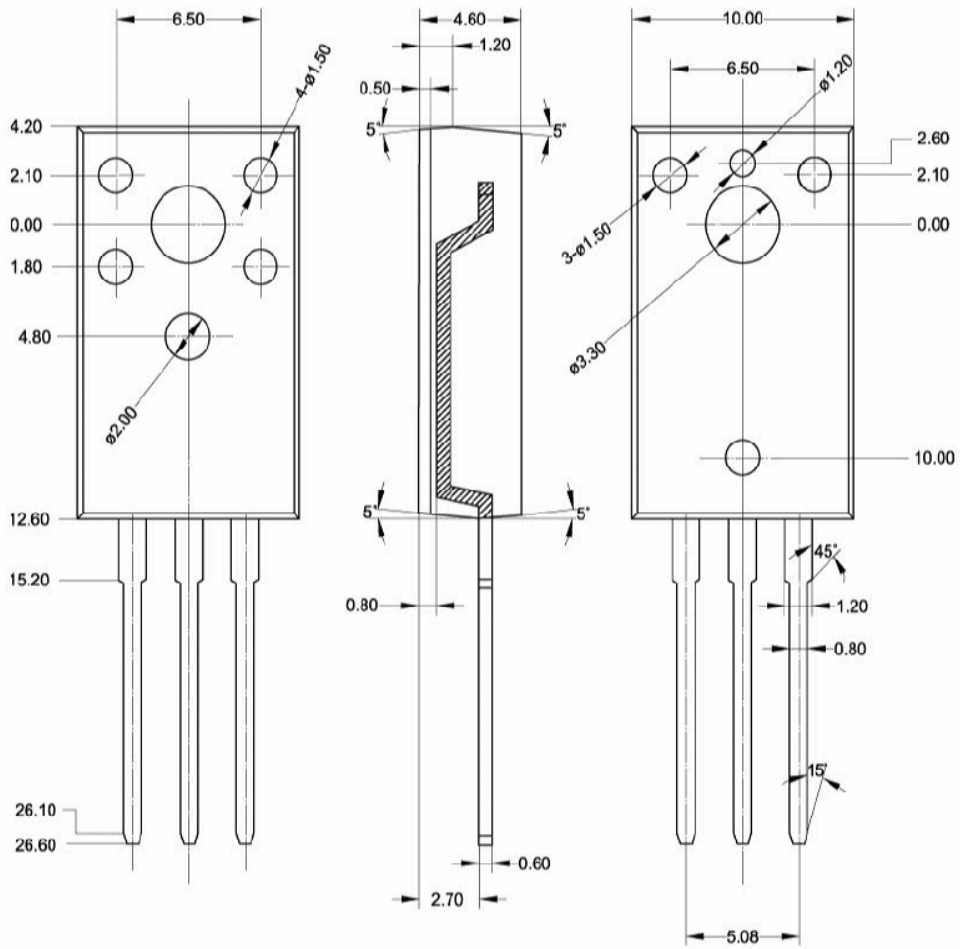


Fig.2 Outline dimensions (unindicated tolerance: ± 0.20 mm)