

## Silicon NPN Power Transistors

2SD817

## DESCRIPTION

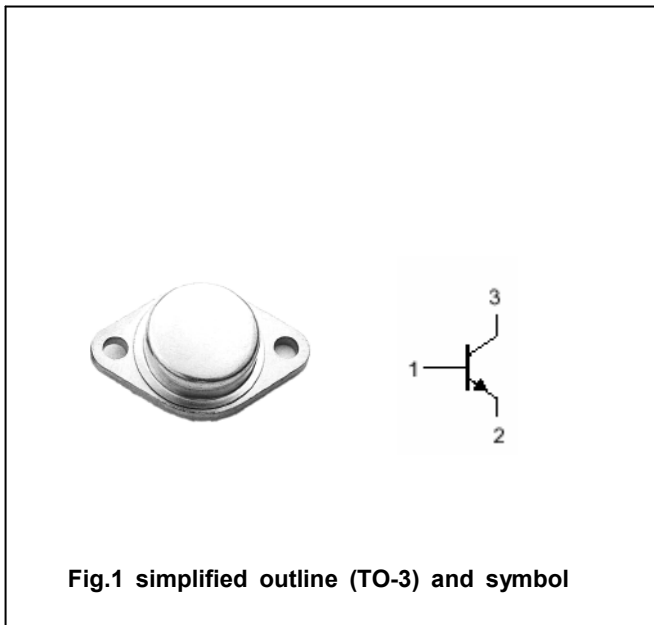
- With TO-3 package
- High voltage ,high reliability
- Wide area of safe operation

## APPLICATIONS

- High voltage power switching TV horizontal deflection output applications

## PINNING(see fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings( $T_a = \square$ )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	600	V
$V_{EBO}$	Emitter-base voltage	Open collector	6	V
$I_C$	Collector current		1.5	A
$P_C$	Collector power dissipation	$T_C = 25 \square$	50	W
$T_j$	Junction temperature		150	$\square$
$T_{stg}$	Storage temperature		-55~150	$\square$

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =1mA; I <sub>C</sub> =0	6			V
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =10mA; I <sub>B</sub> =0	600			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =1.2A; I <sub>B</sub> =0.3A			5.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =1.2A; I <sub>B</sub> =0.3A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =800V; I <sub>E</sub> =0			10	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =6V; I <sub>C</sub> =0			10	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =0.3A; V <sub>CE</sub> =5V	10		30	

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PACKAGE OUTLINE



Fig.2 Outline dimensions