

**Silicon NPN Power Transistors**

**2SD849**

**DESCRIPTION**

- With TO-3 package
- High voltage ,high speed

**APPLICATIONS**

- Line-operated horizontal deflection output applications

**PINNING(see fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

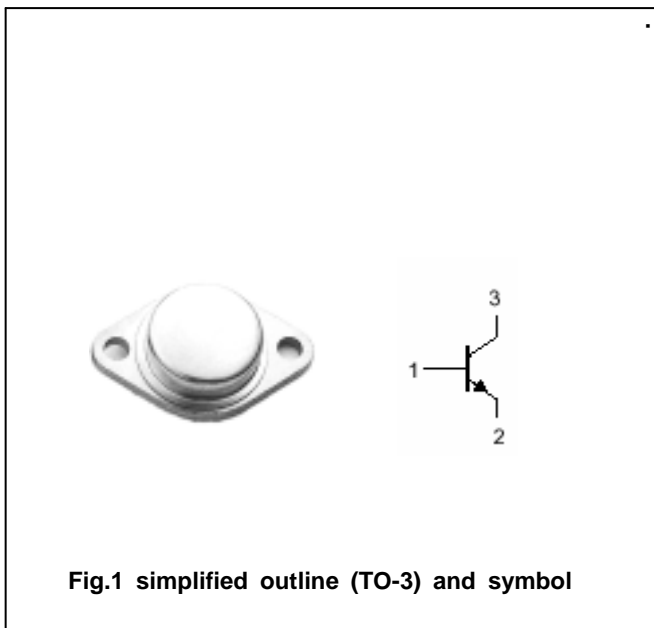


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings(Ta= )**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	600	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		3	A
$I_{CM}$	Collector current-peak		5	A
$P_T$	Total power dissipation	$T_C=90$	25	W
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-65~150	

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## 2SD849

## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =0.1A; I <sub>B</sub> =0	600			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =10mA; I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =3 A; I <sub>B</sub> =1A			5.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =3 A; I <sub>B</sub> =1A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =750V; I <sub>E</sub> =0			0.1	mA
		V <sub>CB</sub> =1500V; I <sub>E</sub> =0			1.0	
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =0.5A ; V <sub>CE</sub> =5V	8			
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =3A ; V <sub>CE</sub> =10V	4		12	
t <sub>f</sub>	Fall time	I <sub>C</sub> =3 A; I <sub>Bend</sub> =1A; L <sub>B</sub> =20 μ H			0.9	μ s
t <sub>s</sub>	Storage time				13	μ s

PACKAGE OUTLINE

