KHB4D5N60P/F/F2

N CHANNEL MOS FIELD EFFECT TRANSISTOR

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for switching mode power supplies.

FEATURES

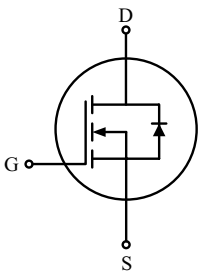
- $V_{DSS(Min.)}$= 600V, I_D= 4.5A
- Drain-Source ON Resistance :
  $R_{DS(ON)}$=2.5 Ω @V_{GS} =10V
- Qg(typ.) =17nC

MAXIMUM RATING (Tc=25℃)

CHARACTERISTIC		SYMBOL	RATING		UNIT
			KHB4D5N60P	KHB4D5N60F KHB4D5N60F2	
Drain-Source Voltage		V_{DSS}	600		V
Gate-Source Voltage		V_{GSS}	±30		V
Drain Current	@T_C=25℃	I_D	4.5	4.5*	A
	@T_C=100℃		2.8	2.8*	
	Pulsed (Note1)	I_{DP}	18	18*	
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	260		mJ
Repetitive Avalanche Energy (Note 1)		E_{AR}	10.6		mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5		V/ns
Drain Power Dissipation	Tc=25℃	P_D	106	36	W
	Derate above25℃		0.85	0.29	W/℃
Maximum Junction Temperature		T_j	150		℃
Storage Temperature Range		T_{stg}	-55～150		℃
Thermal Characteristics					
Thermal Resistance, Junction-to-Case		R_{thJC}	1.18	3.47	℃/W
Thermal Resistance, Case-to-Sink		R_{thCS}	0.5	-	℃/W
Thermal Resistance, Junction-to-Ambient		R_{thJA}	62.5	62.5	℃/W

* : Drain current limited by maximum junction temperature.

PIN CONNECTION



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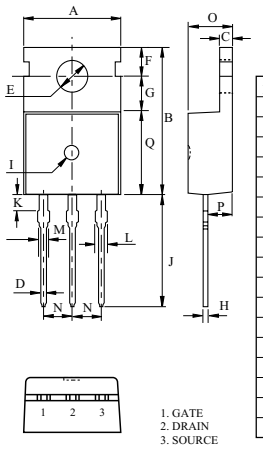
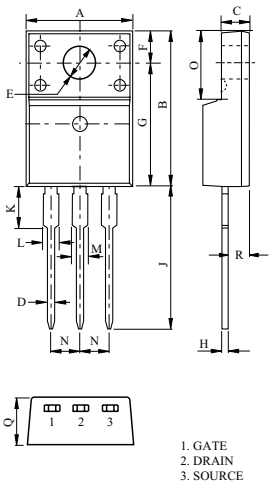


DIM	MILLIMETERS
A	9.9 ± 0.2
B	15.95 MAX
C	1.3+0.1/-0.05
D	0.8 ± 0.1
E	ø3.6 ± 0.2
F	2.8 ± 0.1
G	3.7
H	0.5+0.1/-0.05
I	ø1.5
J	13.08 ± 0.3
K	1.46
L	1.4 ± 0.1
M	1.27 ± 0.1
N	2.54 ± 0.2
O	4.5 ± 0.2
P	2.4 ± 0.2
Q	9.2 ± 0.2

TO-220AB

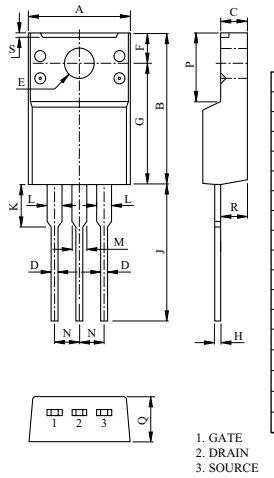
KHB4D5N60F



DIM	MILLIMETERS
A	10.16 ± 0.2
B	15.87 ± 0.2
C	2.54 ± 0.2
D	0.8 ± 0.1
E	ø3.18 ± 0.1
F	3.3 ± 0.1
G	12.57 ± 0.2
H	0.5 ± 0.1
J	13.0 MAX
K	3.23 ± 0.1
L	1.47 MAX
M	1.47 MAX
N	2.54 ± 0.2
O	6.68 ± 0.2
Q	4.7 ± 0.2
R	2.76 ± 0.2

TO-220IS (1)

KHB4D5N60F2



DIM	MILLIMETERS
A	10.0±0.3
B	15.0±0.3
C	2.70±0.3
D	0.76+0.09/-0.05
E	Φ3.2±0.2
F	3.0±0.3
G	12.0±0.3
H	0.5+0.1/-0.05
J	13.6±0.5
K	3.7±0.2
L	1.2+0.25/-0.1
M	1.5+0.25/-0.1
N	2.54±0.1
P	6.8±0.1
Q	4.5±0.2
R	2.6±0.2
S	0.5 Typ

TO-220IS

ELECTRICAL CHARACTERISTICS (Tc=25℃)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D=250㎂, V_{GS}=0V	600	-	-	V
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_j$	I_D=250㎂, Referenced to 25℃	-	0.6	-	V/℃
Drain Cut-off Current	I_{DSS}	V_{DS}=600V, V_{GS}=0V,	-	-	±10	㎂
Gate Threshold Voltage	V_{th}	V_{DS}=V_{GS}, I_D=250㎂	2	-	4	V
Gate Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V	-	-	±100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=2.25A	-	2.2	2.5	Ω
Dynamic						
Total Gate Charge	Q_g	V_{DS}=480V, I_D=4.5A V_{GS}=10V (Note4,5)	-	17	21	nC
Gate-Source Charge	Q_{gs}		-	3	-	
Gate-Drain Charge	Q_{gd}		-	7.3	-	
Turn-on Delay time	$t_{d(on)}$	V_{DD}=300V R_L=67 Ω R_G=25 Ω (Note4,5)	-	15	35	ns
Turn-on Rise time	t_r		-	35	80	
Turn-off Delay time	$t_{d(off)}$		-	70	150	
Turn-off Fall time	t_f		-	80	170	
Input Capacitance	C_{iss}	V_{DS}=25V, V_{GS}=0V, f=1.0MHz	-	655	850	pF
Output Capacitance	C_{oss}		-	66	86	
Reverse Transfer Capacitance	C_{rss}		-	8	11	
Source-Drain Diode Ratings						
Continuous Source Current	I_S	V_{GS}<V_{th}	-	-	4.5	A
Pulsed Source Current	I_{SP}		-	-	18	
Diode Forward Voltage	V_{SD}	I_S=4.5A, V_{GS}=0V	-	-	1.5	V
Reverse Recovery Time	t_{rr}	I_S=4.5A, V_{GS}=0V, dIs/dt=100A/㎲	-	350	-	ns
Reverse Recovery Charge	Q_{rr}		-	2.7	-	㎌

Note 1) Repetivity rating : Pulse width limited by junction temperature.

Note 2) L = 25mH, I_S=4.5A, V_{DD}=50V, R_G = 25 Ω , Starting T_j = 25 ℃.

Note 3) I_S ≤4.5A, dI/dt≤200A/㎲, V_{DD} ≤BV_{DSS}, Starting T_j = 25℃.

Note 4) Pulse Test : Pulse width ≤ 300㎲, Duty Cycle ≤ 2%.

Note 5) Essentially independent of operating temperature.

Fig1. I_D - V_{DS}

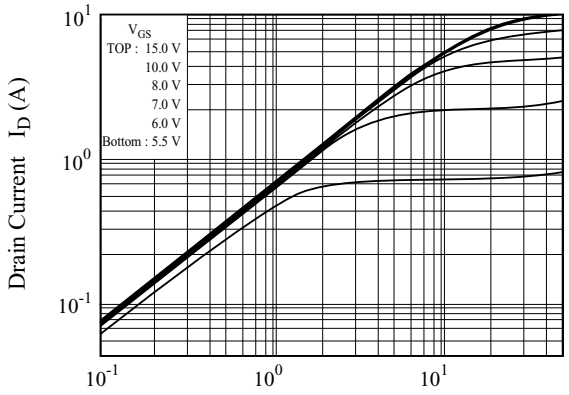


Fig2. I_D - V_{GS}

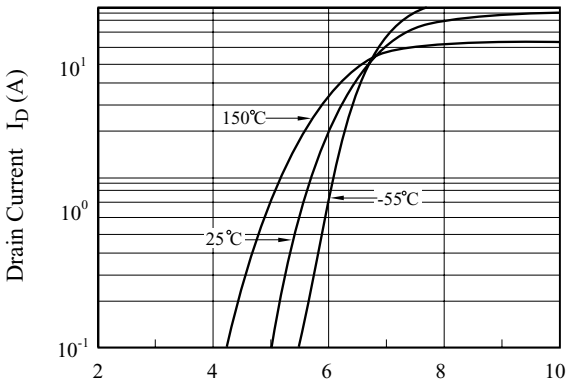


Fig3. BV_{DSS} - T_j

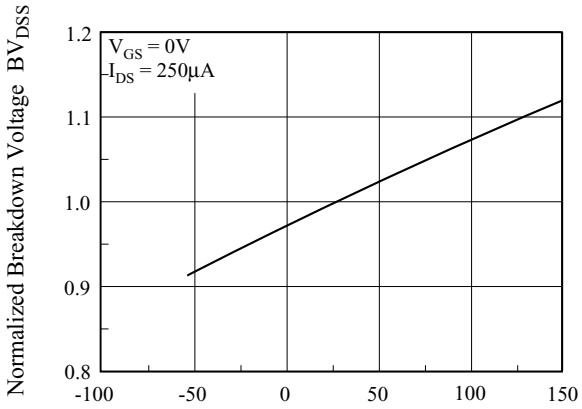


Fig4. $R_{DS(ON)}$ - I_D

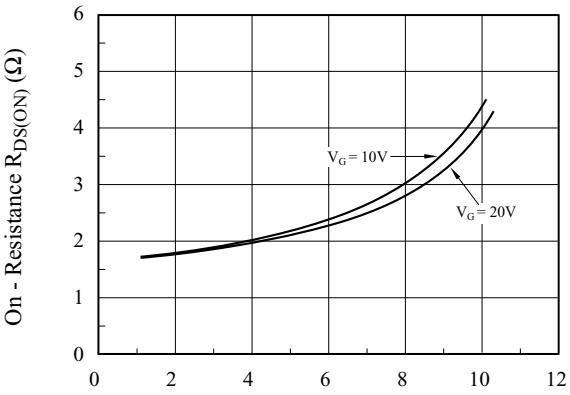


Fig5. I_S - V_{SD}

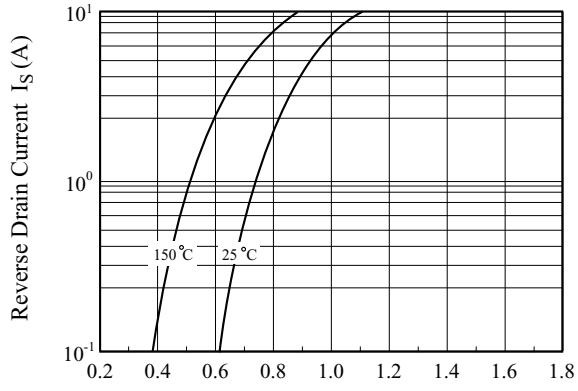


Fig6. $R_{DS(ON)}$ - T_j

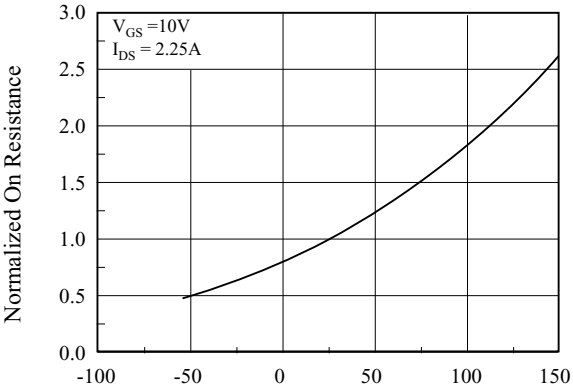


Fig7. C - V_{DS}

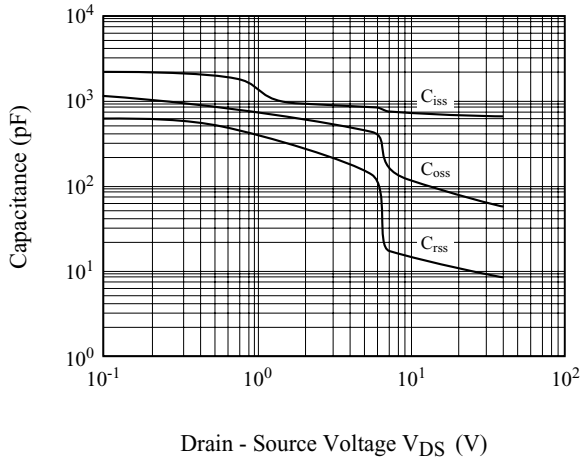


Fig8. Q_g- V_{GS}

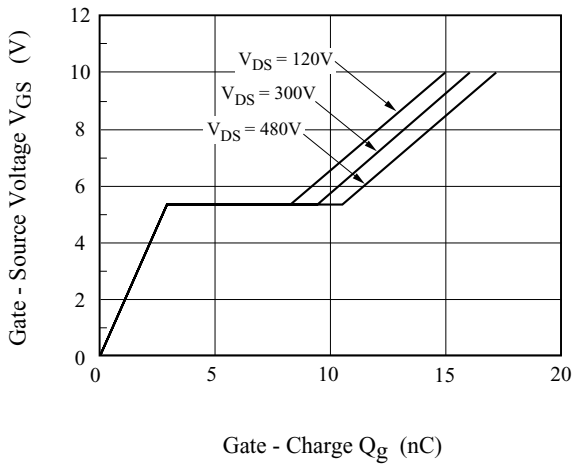


Fig9. Safe Operation Area

(KHB4D5N60P)

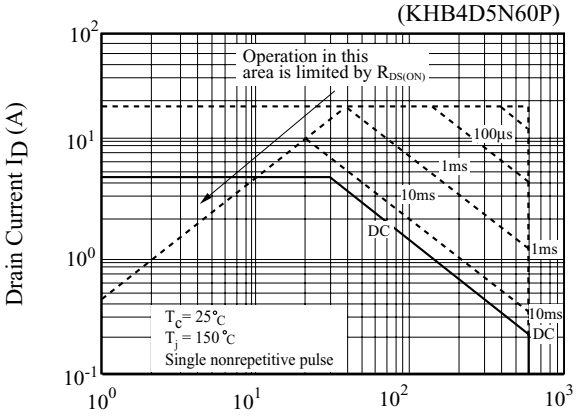


Fig10. Safe Operation Area

(KHB4D5N60F,KHB4D5N60F2)

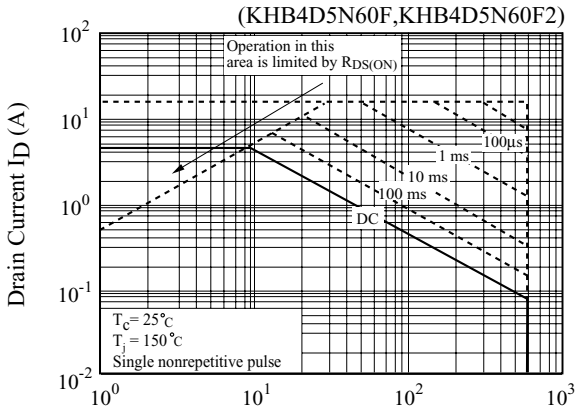


Fig11. I_D - T_j

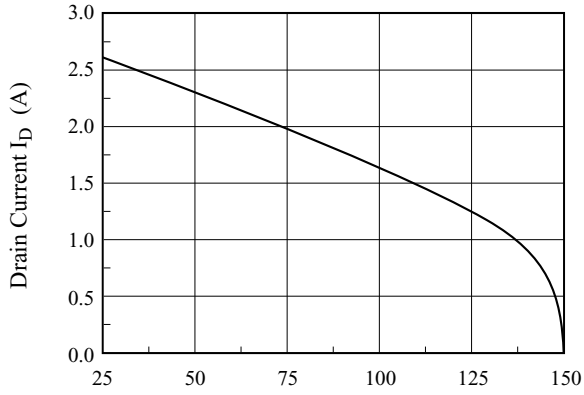


Fig12. Transient Thermal Response Curve

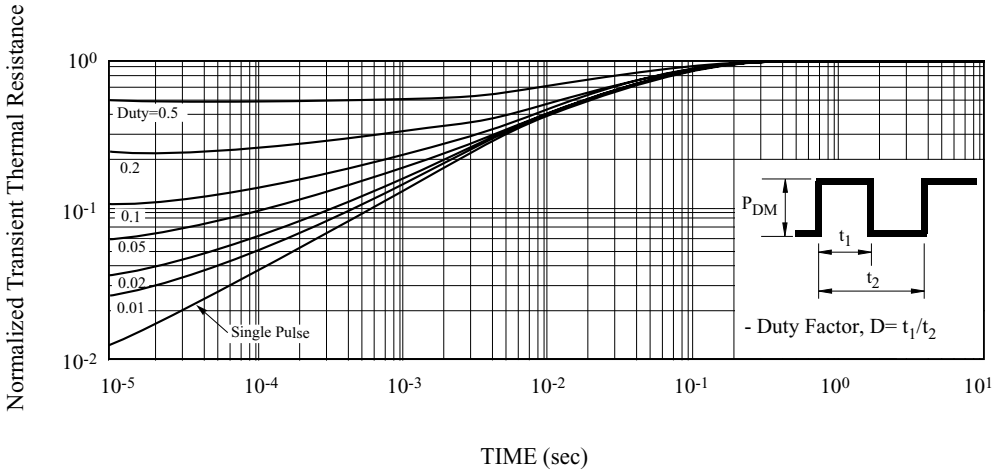


Fig13. Transient Thermal Response Curve

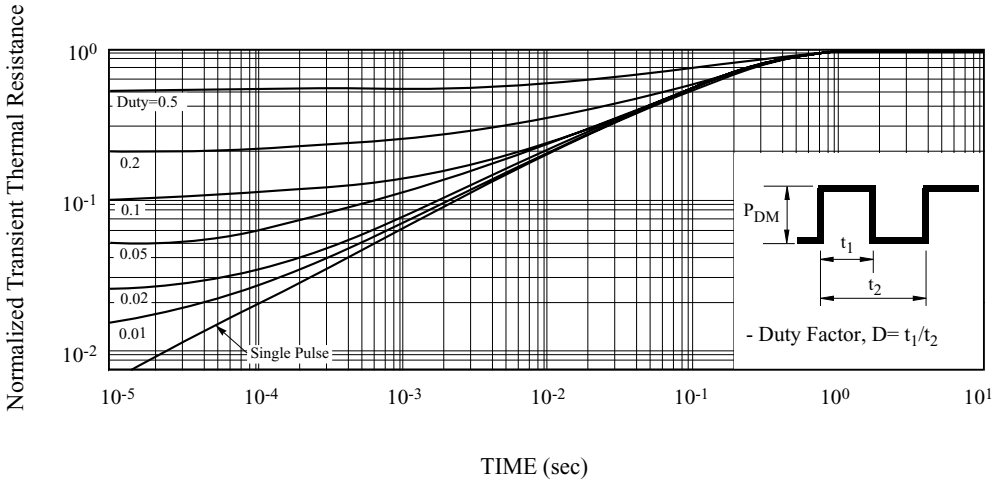


Fig14. Gate Charge

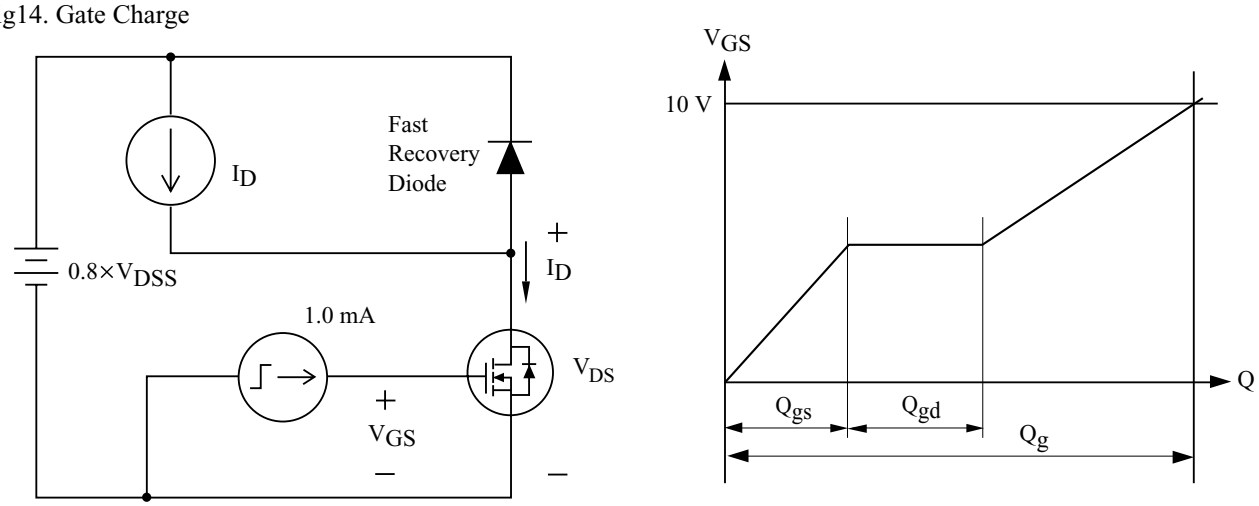


Fig15. Single Pulsed Avalanche Energy

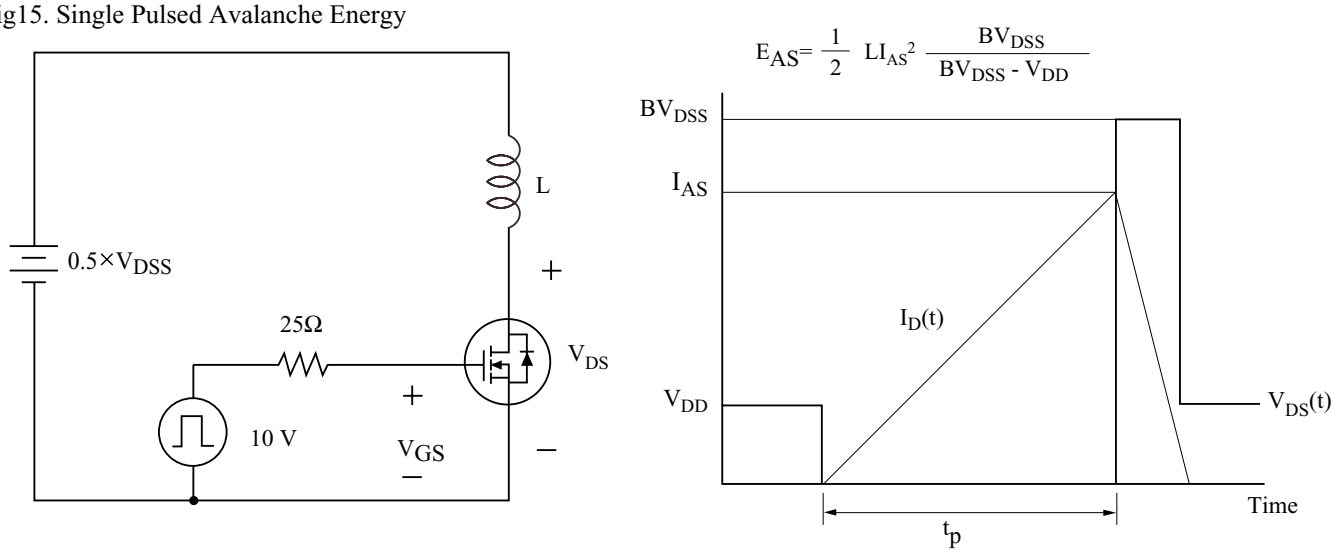
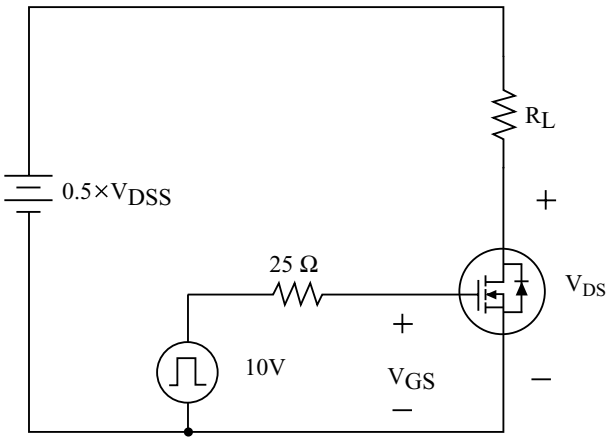


Fig16. Resistive Load Switching



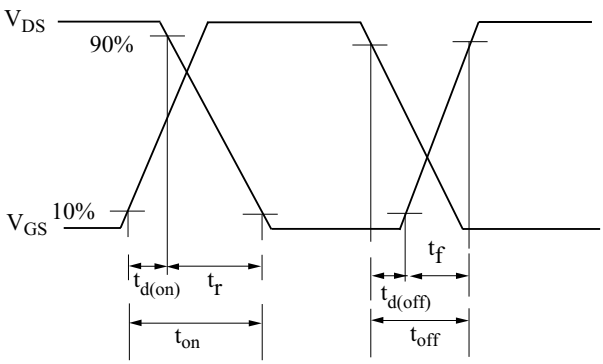


Fig17. Source - Drain Diode Reverse Recovery and dv /dt