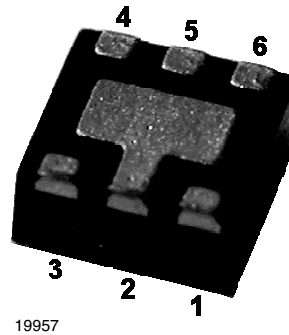


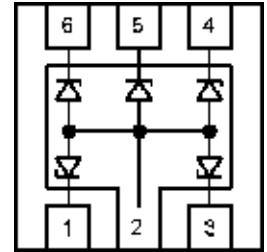
5-line ESD Protection Diode Array in LLP75

Features

- Ultra compact LLP75 package
- 5-line ESD-protection
- Low leakage current
- ESD protection to IEC 61000-4-2 ± 15 kV (Air)
- ESD protection to IEC 61000-4-2 ± 15 kV (Contact)
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



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Mechanical Data

Case: LLP75-6A (plastic package)

Lead (Pb)-free; non magnetic

Molding Compound Flammability Rating:

UL 94 V-0

Terminals: High temperature soldering guaranteed: 260 °C/10 sec. at terminals

Weight: 5 mg

Packaging Codes/Options:

GS18 = 10 k per 13" reel (8 mm tape), 10 k/box

GS08 = 3 k per 7" reel (8 mm tape), 15 k/box

Marking:



Square = Pin 1 marking

S6 = Date Code (Example only)

AE = Type Code for VESD05A5A-HS3

Absolute Maximum Ratings

T_{amb} = 25 °C unless otherwise specified

Parameter	Symbol	Value	Unit
ESD Air Discharge per IEC 61000-4-2	V _{ESD}	± 15	kV
ESD Contact Discharge per IEC 61000-4-2	V _{ESD}	± 15	kV
Peak pulse power 8/20 μs waveform	P _{PPM}	33	W
Peak pulse current 8/20 μs waveform	I _{PPM}	2.5	A

Thermal Characteristics

T_{amb} = 25 °C unless otherwise specified

Parameter	Symbol	Value	Unit
Operating Temperature	T _J	- 40 to + 125	°C
Storage Temperature	T _{STG}	- 55 to + 150	°C

Electrical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Reverse Stand-Off Voltage	at max. reverse current BiAs (Pin 1, 3-6 to Pin 2)	V_{RWM}	5			V
Max. Reverse current	at $V_R = 5\text{ V}$	I_R		< 0.01	0.1	μA
Max. Clamping voltage	at $I_{PP} = 2.5\text{ A}$ BiAs (Pin 1, 3-6 to Pin 2) acc. IEC 61000-4-5	V_C		12	13	V
Max. Clamping voltage	at $I_{PP} = 2.5\text{ A}$ BiSy ¹⁾ acc. IEC 61000-4-5	V_C		14	15.5	V
Max. Forward Clamping voltage	at $I_{PP} = 2.5\text{ A}$ BiAs (Pin 2 to Pin 1, 3-6) acc. IEC 61000-4-5	V_F		3.2	4	V
Max. Peak pulse current	BiAs and BiSy acc. IEC 61000-4-5 see Fig. 1	I_{PPM}	2.5			A
Reverse Breakdown Voltage	at $I_R = 1\text{ mA}$ BiAs ¹⁾	V_{BR}	6.0	6.7	7.5	V
Capacitance	at $V_R = 0\text{ V}$; $f = 1\text{ MHz}$	C_D		13	15	pF
Forward voltage	at $I_F = 1\text{ A}$; $t_p < 300\text{ }\mu\text{s}$	V_F		1.8	2.1	V
ESD-Clamping voltage (Over- / undershoot)	at $\pm 8\text{ kV}$ ESD-pulse acc. IEC 61000-4-2	V_{C-ESD}		± 90		V

¹⁾ BiAs and BiSy are explained in the application notes on page 4

Typical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

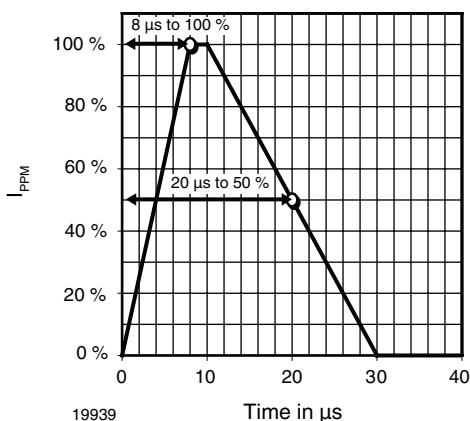


Figure 1. 8/20 μs Peak Pulse Current wave form
acc. IEC 61000-4-5

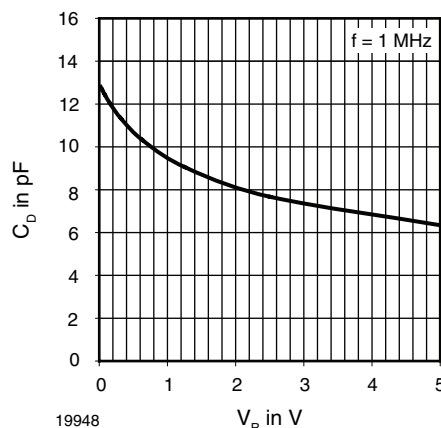


Figure 2. Typical Capacitance C_D vs. Reverse Voltage V_R

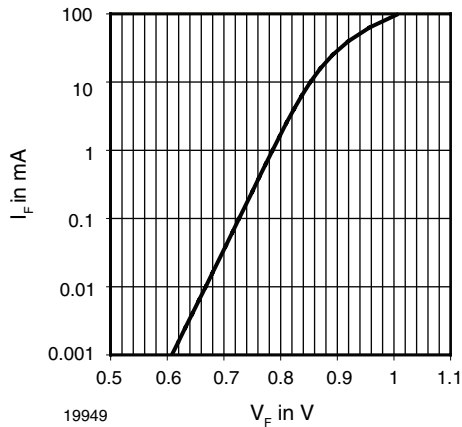


Figure 3. Typical Forward Current I_F vs. Forward Voltage V_F

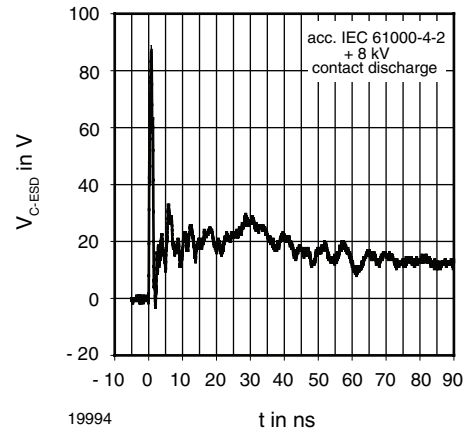


Figure 6. Typical Clamping performance at +8 kV contact discharge (acc. IEC 61000-4-2)

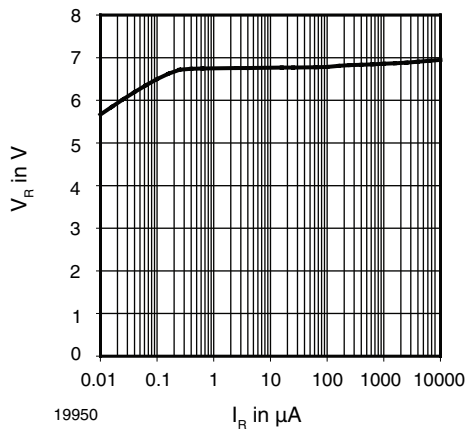


Figure 4. Typical Reverse Voltage V_R vs. Reverse Current I_R

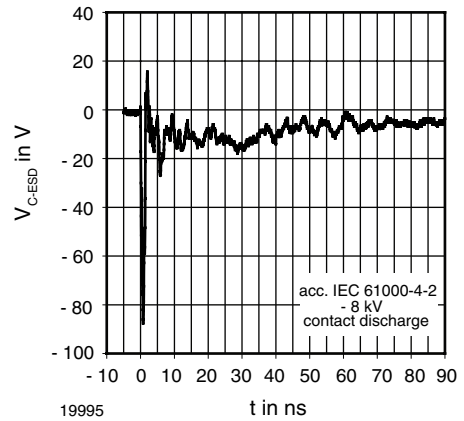


Figure 7. Typical Clamping performance at -8 kV contact discharge (acc. IEC 61000-4-2)

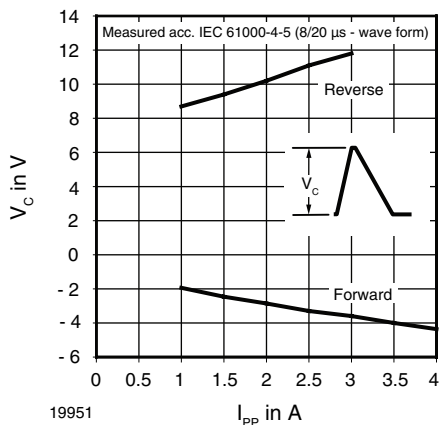


Figure 5. Typical Clamping Voltage vs. Peak Pulse Current I_{PP}

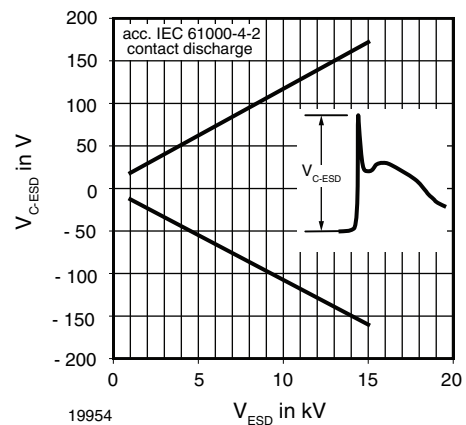
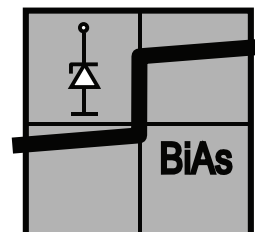
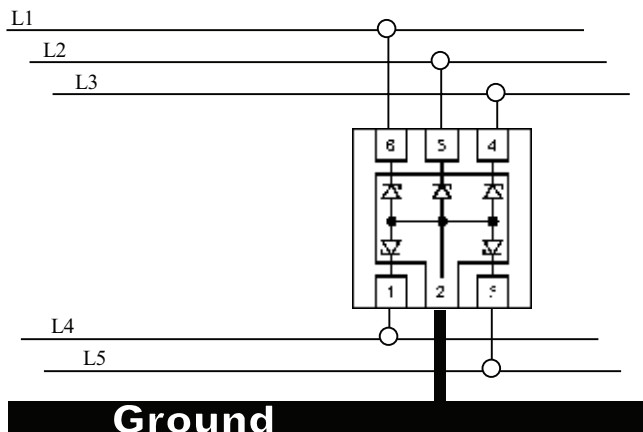


Figure 8. Typical max. Clamping Voltage at ESD contact discharge (acc. IEC 61000-4-2)

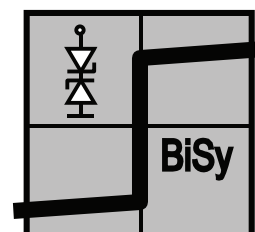
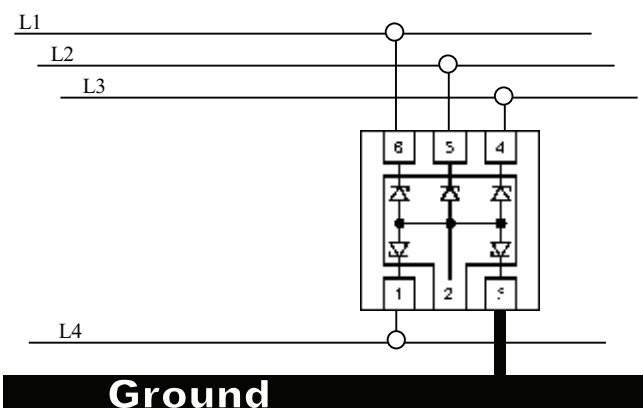
Application Note:

- a) With the **VESD05A5A-HS3** 5 different signal or data lines can be clamped to ground. Due to the different clamping levels in forward and reverse direction the **VESD05A5A-HS3** clamping behavior is **B**idirectional and **A**symmetrical (**BiAs**).



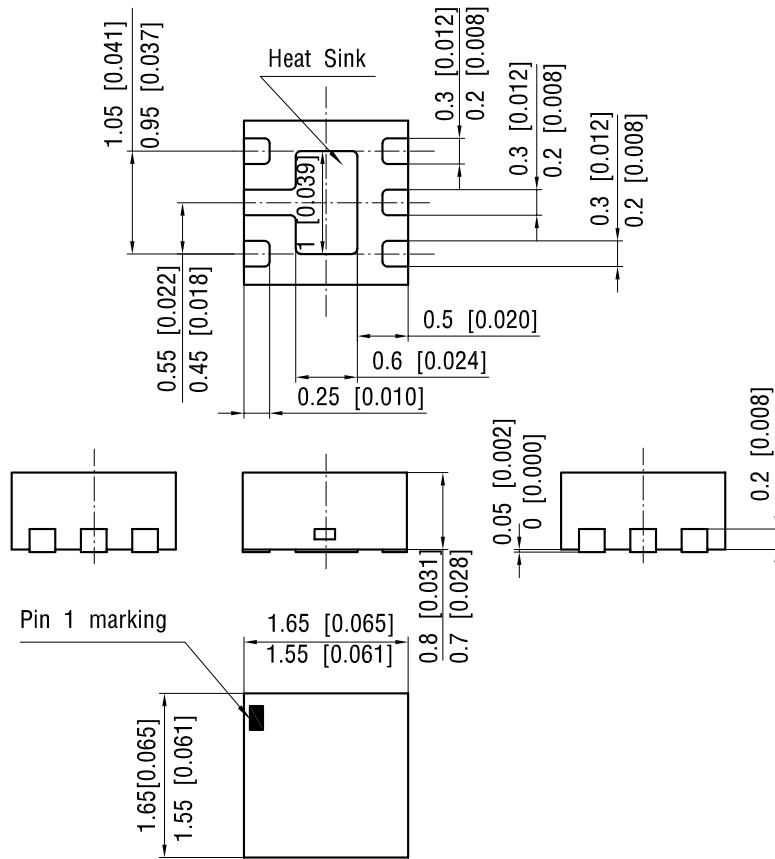
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- b) If symmetrical clamping behaviour is required the **VESD05A5A-HS3** can also be used as a **B**idirectional **S**ymmetrical protection device protecting up to 4 lines. In this case pin no. 2 must not be connected.

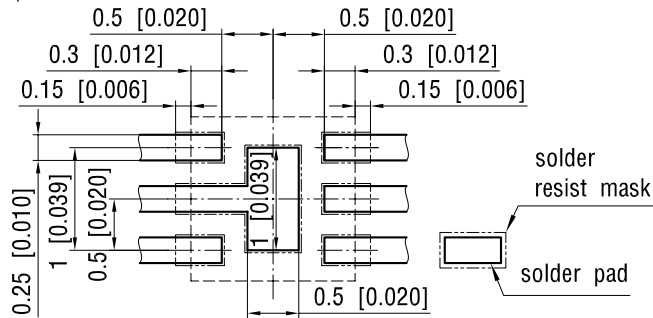


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Package Dimensions in mm (Inches) LLP75-6A



foot print recommendation:



19986

Created - Date: 20.December.2004
Rev. b - Date: 12.January.2006

Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design
and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



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