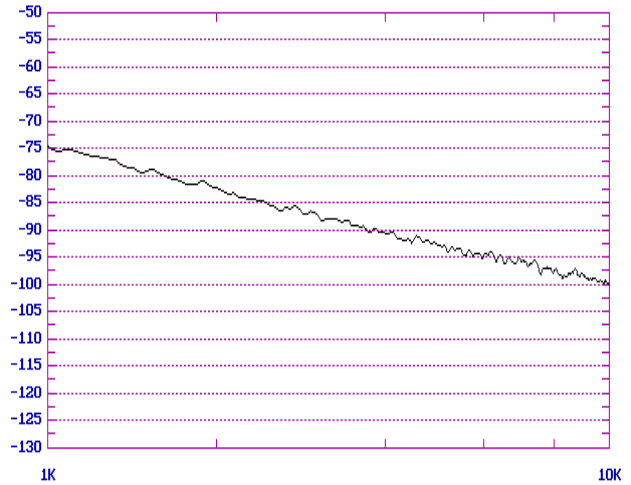




PHASE NOISE (1 Hz BW, typical)

$\mathcal{L}(f)$ (dBc/Hz)



OFFSET (Hz)

FEATURES
<ul style="list-style-type: none"> • Frequency Range: 1990- 2380 MHz • Tuning Voltage: 0.5-15 Vdc • MINI-14H-LOW- Style Package
APPLICATIONS
<ul style="list-style-type: none"> • Digital Radios • Earthstations • Satellite Modems

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Oscillation Frequency Range	1990 - 2380	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-100	dBc/Hz
Harmonic Suppression (2nd, typ.)	-6	dBc
Tuning Voltage	0.5-15	Vdc
Tuning Sensitivity (avg.)	51	MHz/V
Power Output	10.25±2.75	dBm
Load Impedance	50	Ω
Input Capacitance (max.)	25	pF
Pushing	<5	MHz/V
Pulling (14 dB Return Loss, Any Phase)	<15	MHz
Operating Temperature Range	-30 to 85	$^{\circ}\text{C}$
Package Style	MINI-14H-LOW	

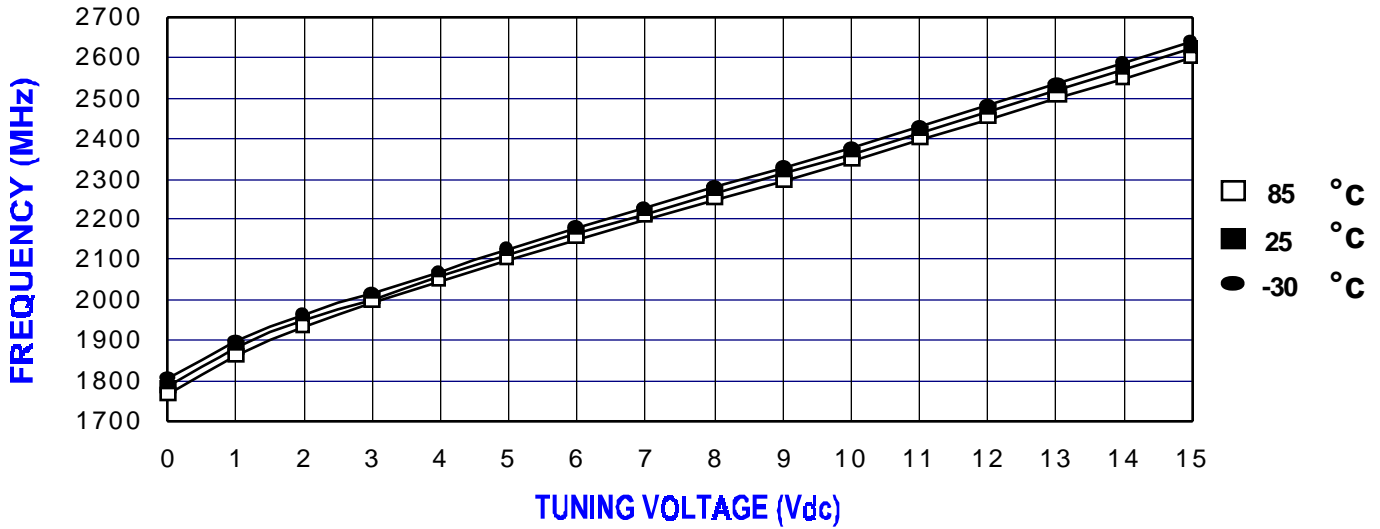
POWER SUPPLY REQUIREMENTS	VALUE	UNITS
Supply Voltage (Vcc, nom.)	9	Vdc
Supply Current (Icc, typ.)	18	mA

All specifications are typical unless otherwise noted and subject to change without notice.

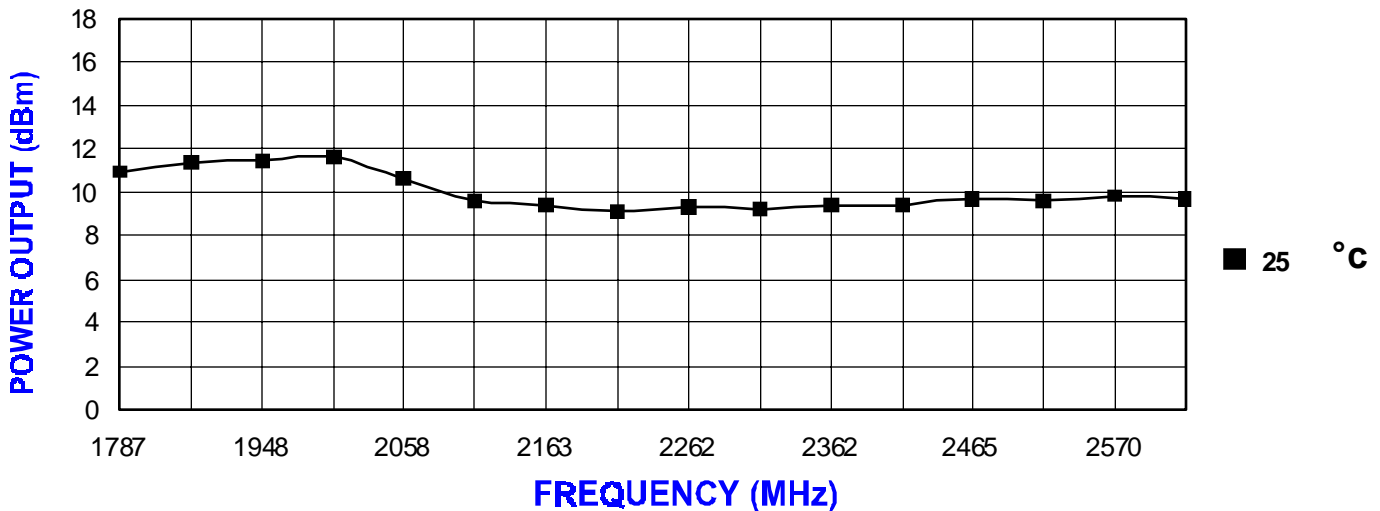
APPLICATION NOTES
<ul style="list-style-type: none"> • AN-100/1 : Mounting and Grounding of VCOs • AN-102 : Proper Output Loading of VCOs • AN-107 : How to Solder Z-COMM VCOs

NOTES:

TUNING CURVE, typ.

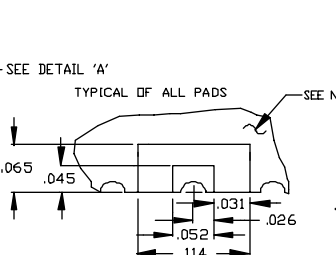
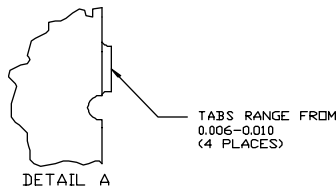
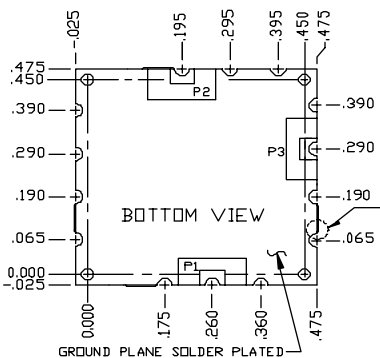


POWER CURVE, typ.



PHYSICAL DIMENSIONS

MINI-14H-LOW
415-0077 REV. A
(DRAWING NOT TO SCALE)



NOTES:

1. THE INSIDE RADIUS OF ALL 14 HALF HOLES AT THE PERIMETER OF THE BOARD ARE SOLDER PLATED TO PROVIDE A SURFACE FOR THE ATTACHMENT OF THE VCO TO A MOTHERBOARD. IN 11 LOCATIONS, WITH 3 PADS BEING USED FOR ELECTROMECHANICAL INTERFACE. 14 SOLDER LOCATIONS REQUIRED.
2. THE SURFACE OF THE SHIELD IS TIN PLATED AND MAY BE SOLDERED TO.
3. THE SHIELD'S BASE METAL IS BRASS. THE GROUND PLANE IS GROUND AND ATTACHES TO A GROUND TRACK ON THE UPPER SIDE OF THE BOARD AS WELL AS THE SHIELD BY PTH.
4. UNLESS OTHERWISE NOTED ALL DIMENSIONS ARE IN INCHES.
5. UNLESS OTHERWISE NOTED ALL TOLERANCES ARE AS FOLLOWS:
TOLERANCES
.XXX = ± .010

P1=VT
P2=RF OUT
P3=Vcc

