

Toshiba Intelligent Power Device Silicon Monolithic Power MOS Integrated circuit

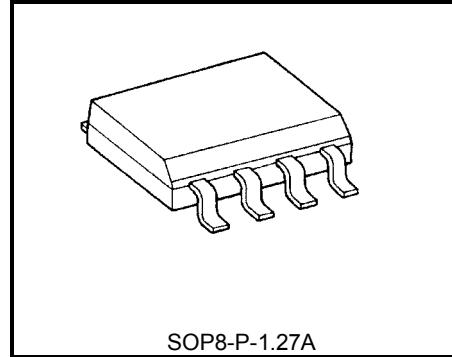
TPD1038F

**Motor, Solenoid, Lamp Drivers
High Side Power Switch**

TPD1038F is a monolithic power IC for high-side switches. The IC has a vertical MOS FET output which can be directly driven from a CMOS or TTL logic circuit (eg, an MPU). The device offers intelligent self-protection and diagnostic functions.

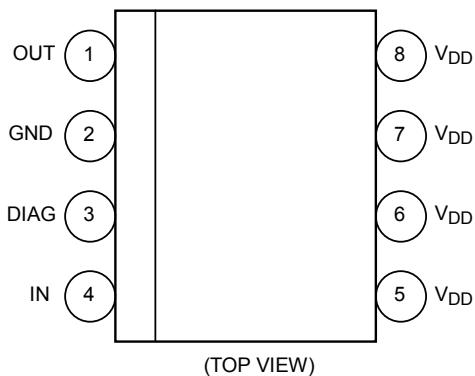
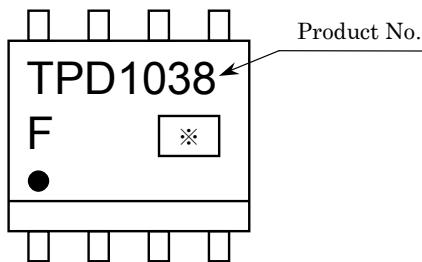
Features

- A monolithic power IC with a structure combining a control block (Bi-CMOS) and a vertical power MOS FET on a single chip.
- One side of load can be grounded to a high-side switch.
- Can directly drive a power load from a microprocessor.
- Built-in protection against overheating and load short circuiting.
- Incorporates a diagnosis function that allows diagnosis output to be read externally at load short-circuiting, opening, or overheating.
- Up to $-(50-V_{DD}) \sim -(60-V_{DD})$ of counterelectromotive force from an L load can be applied.
- Low on Resistance : $R_{DS(ON)}=120\text{m}\Omega$ (max) (@ $V_{DD} = 12\text{ V}$, $T_a = 25^\circ\text{C}$, $I_o = 2\text{ A}$)
- 8-pin SOP package for surface mounting can be packed in tape.



SOP8-P-1.27A

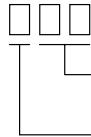
Weight : 0.08g(typ.)

Pin Assignment**Marking**

(●) on lower left of the marking indicates Pin1.

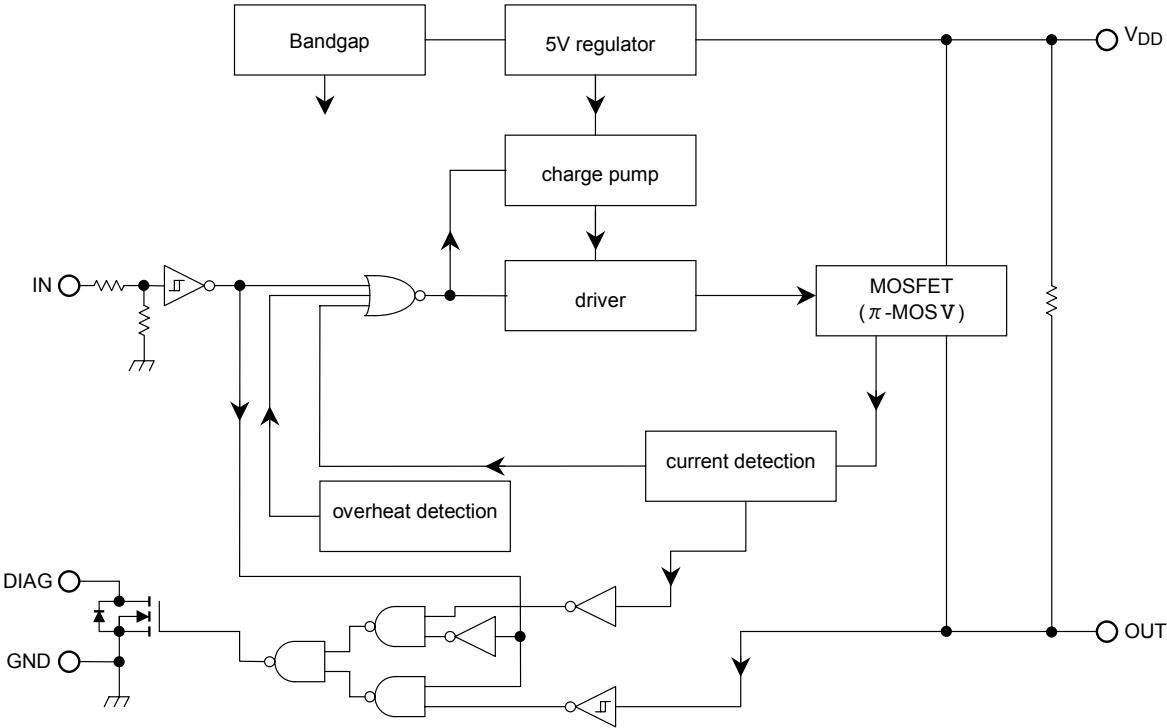
※Lot Number

Weekly code : (Three digits)

Week of manufacture
(01 for first of year, continues up to 52 or 53)Year of manufacture
(One low-order digits of calendar year)

That because of its MOS structure, this product is sensitive to static electricity.

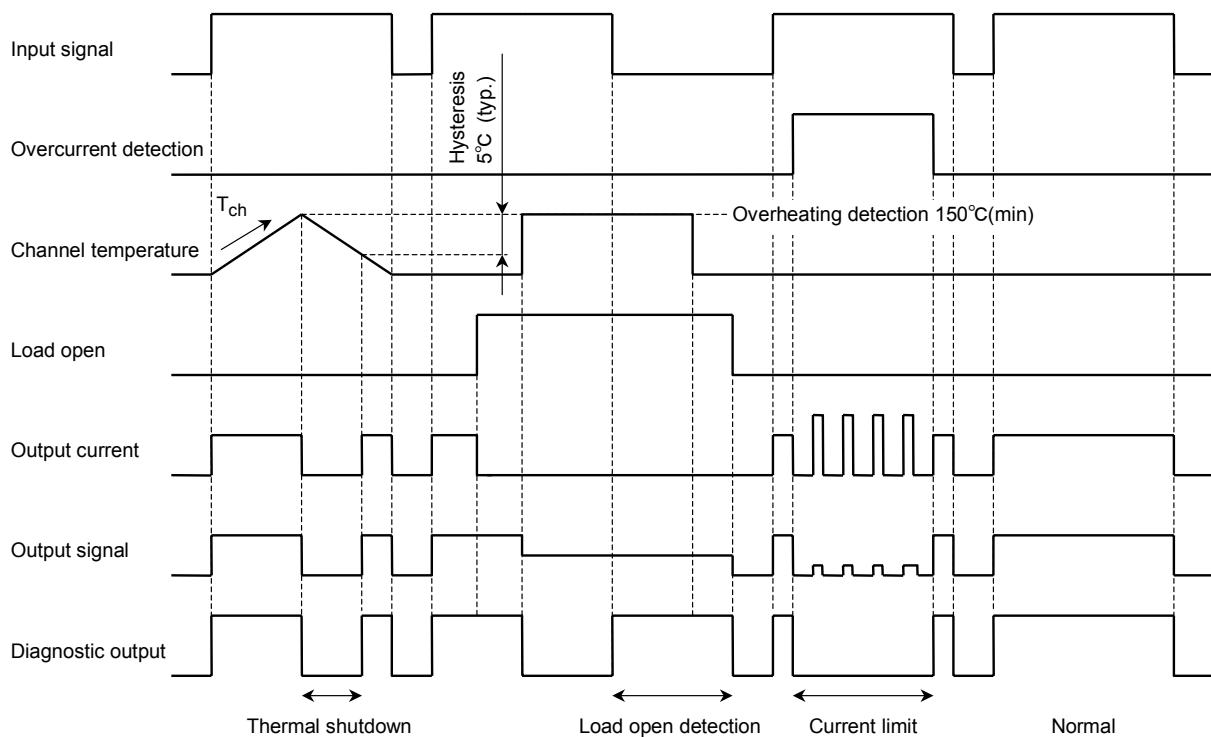
Block Diagram



Pin Description

Pin No.	Symbol	Function
1	OUT	Output pin. When the load is short circuited and current in excess of the detection current (3A min) flows to the output pin, the output automatically turns on or off.
2	GND	Ground pin.
3	DIAG	Self-diagnosis detection pin. Goes low when overheating is detected or when output is short circuit with input on (high). N-channel open drain.
4	IN	Input pin. Input is CMOS compatible, with pull down resistor connected. Even if the input is open, output will not accidentally turn on.
5,6,7,8	V _{DD}	Power pin.

Timing Chart



Truth Table

Input signal	Diagnosis output	Output signal	Output state	Operating state
H	H	H	on	Normal
L	L	L	off	
H	L	L	current limit (switching)	Load Short
L	L	L	off	
H	L	L	off	Overheating
L	H	H	off	
H	H	H	on	Load open
L	H	H	off	
H	L	L	off	Overheating and load open
L	H	H	off	

Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	60	V
Supply Voltage	DC	$V_{DD(1)}$	25	V
	Pulse	$V_{DD(2)}$	$60(R_S=1\Omega, \tau=250\text{ms})$	V
Input Voltage	DC	$V_{IN(1)}$	$-0.5\sim 12$	V
	Pulse	$V_{IN(2)}$	$V_{DD(1)}+1.5(t=100\text{ms})$	V
Diagnosis Output Voltage		V_{DIAG}	$-0.5\sim 25$	V
Output Current		I_O	Internally Limited	A
Input current		I_{IN}	± 10	mA
Diagnosis current		I_{DIAG}	5	mA
Power Dissipation (Note 1-a)		$P_D(1)$	1.1	W
Power Dissipation (Note 1-b)		$P_D(2)$	0.425	W
Operating temperature		T_{opr}	$-40\sim 110$	$^\circ\text{C}$
Channel temperature		T_{ch}	150	$^\circ\text{C}$
Storage temperature		T_{stg}	$-55\sim 150$	$^\circ\text{C}$

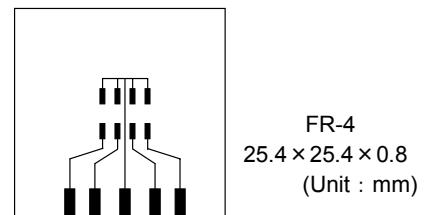
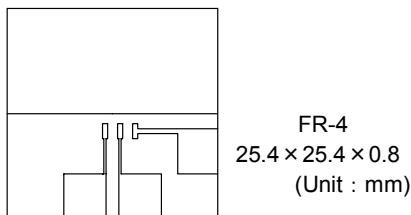
Thermal Resistance

Characteristic	Symbol	Rating	Unit
Thermal Resistance	$R_{th(ch-a)}$	113.5 (Note1-a)	$^\circ\text{C}/\text{W}$
		294.0 (Note1-b)	

Note 1:

1-a : Mounted on glass epoxy board (a)

1-b : Mounted on glass epoxy board (b)



Electrical characteristics ($T_a=25^\circ\text{C}$)

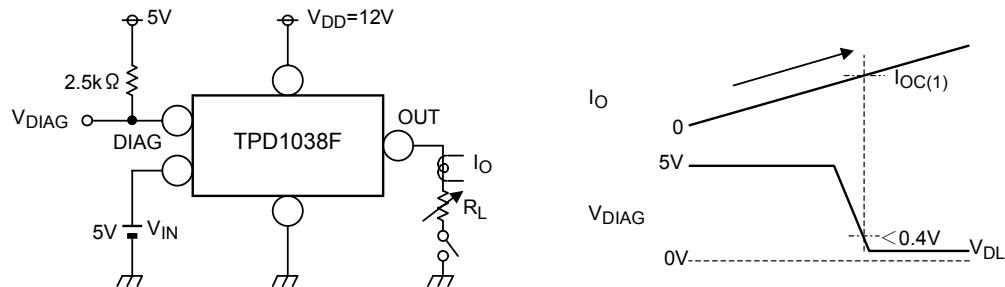
Characteristics	Symbol	Test circuit	Test condition	min	typ.	max	Unit
Operating supply voltage	$V_{DD(\text{OPR})}$	—	—	6	12	18	V
Current dissipation	I_{DD}	—	$V_{DD}=12\text{V}, V_{IN}=0\text{V}, R_L=10\Omega$	—	—	3	mA
H-level input voltage	V_{IH}	—	$V_{DD}=12\text{V}$	3.5	—	—	V
L-level input voltage	V_{IL}	—	$V_{DD}=12\text{V}$	—	—	1.5	V
H-level input current	I_{IH}	—	$V_{DD}=12\text{V}, V_{IN}=5\text{V}$	—	—	200	μA
On resistance	$R_{DS(\text{ON})}$	—	$V_{DD}=12\text{V}, I_O=2\text{A}$	—	—	0.12	Ω
Output leakage current	I_{OL}	—	$V_{DD}=12\text{V}$	—	—	1	mA
Diagnosis output voltage "L"-level	V_{DL}	—	$V_{DD}=12\text{V}, V_{IN}=0\text{V}, I_{DL}=1\text{mA}$ $R_L=10\Omega$	—	—	0.4	V
Diagnosis output current "H"-level	I_{DH}	—	$V_{DD}=12\text{V}, V_{IN}=5\text{V}, R_L=10\Omega, V_{DH}=12\text{V}$	—	—	10	μA
Over current detection	$I_{OC(1)}$ (Note2)	1.2	$V_{DD}=12\text{V}$	3	—	9	A
	$I_{OC(2)}$ (Note3)	3	$V_{DD}=12\text{V}, R_L=0.1\Omega$	—	—	10	A
Overheating detection	T_{OT}	—	$V_{DD}=12\text{V}$	150	—	200	$^\circ\text{C}$
Load open detection (Note4)	R_{op}	—	$V_{DD}=12\text{V}, V_{IN}=0\text{V}$	5	17	—	$k\Omega$
Switching time	t_{on}	4	$V_{DD}=12\text{V}, R_L=10\Omega$	—	—	100	μs
	t_{off}			—	—	40	μs
Diagnosis delay time	t_{DLH}	5	$V_{DD}=12\text{V}, R_L=10\Omega$	—	70	—	μs
	t_{DHL}			—	22	—	μs
Output clamp voltage	V_{clamp}	—	$V_{DD}=12\text{V}, V_{IN}=0\text{V}, I_O=1\text{A}, L=10\text{mH}$	—(60- V_{DD})	—	—(50- V_{DD})	V

(Note 2) Over current detection

(Note 3) Peak current @ current limit function

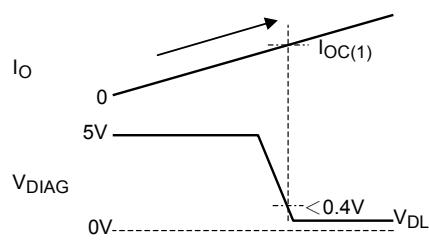
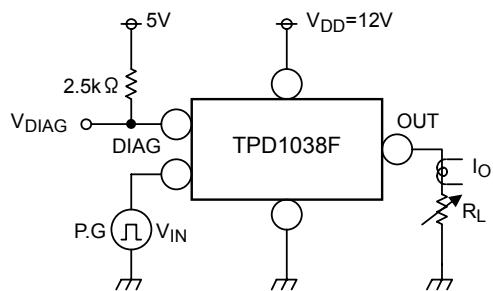
(Note 4) Load open detection function : $V_{DD} = 8 \sim 18\text{V}$

Test circuit 1

Over current detection $I_{OC(1)}$: Over current detection when load current is increased while V_{IN} = "H"

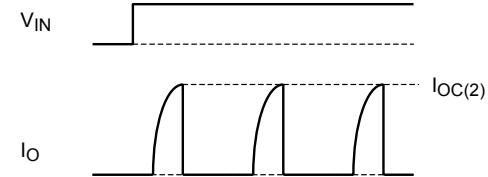
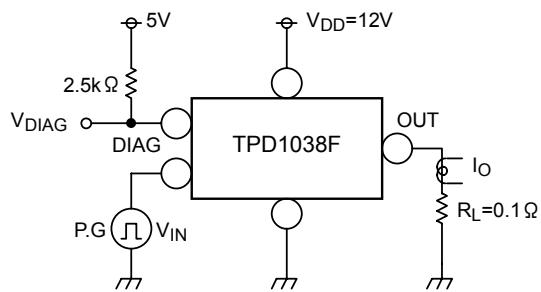
Test circuit 2

Over current detection loc(1) : Over current detection when load is short circuit and VIN = "L" → "H"



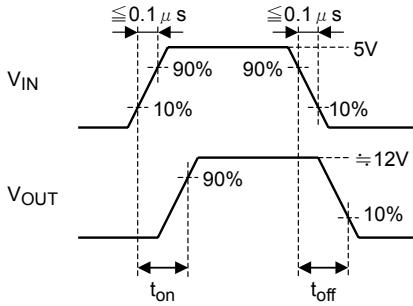
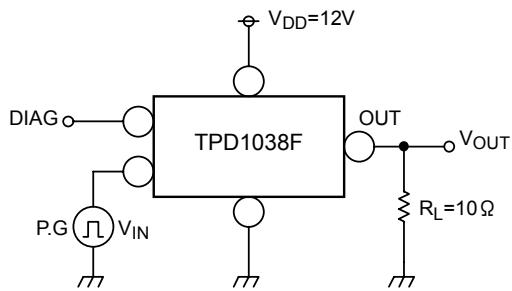
Test circuit 3

Over current detection loc(2)



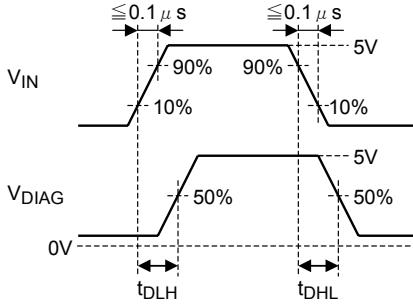
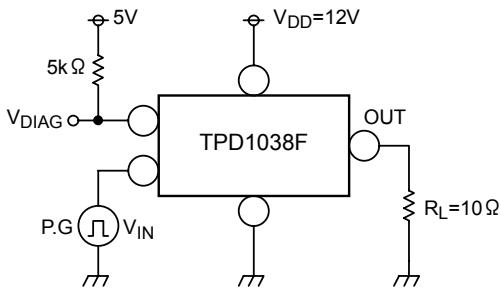
Test circuit 4

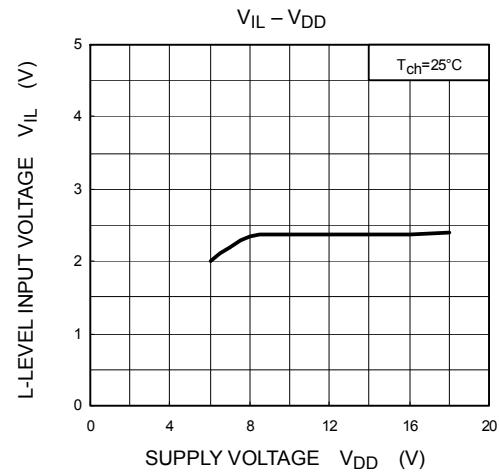
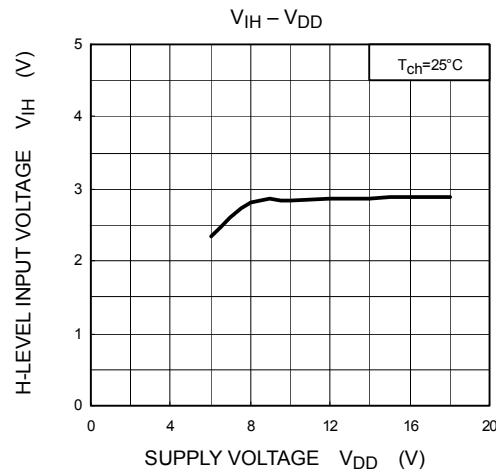
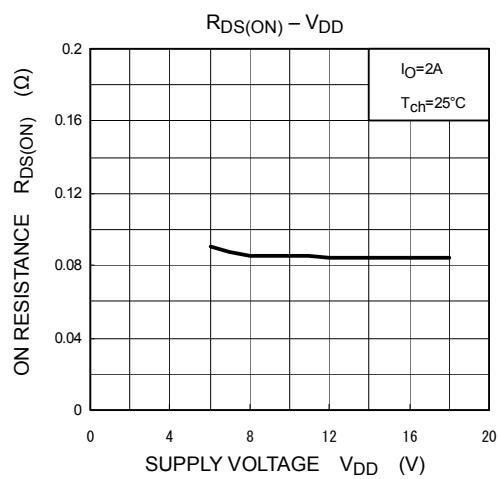
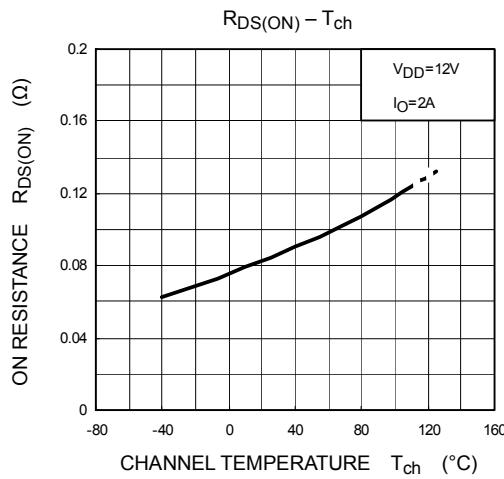
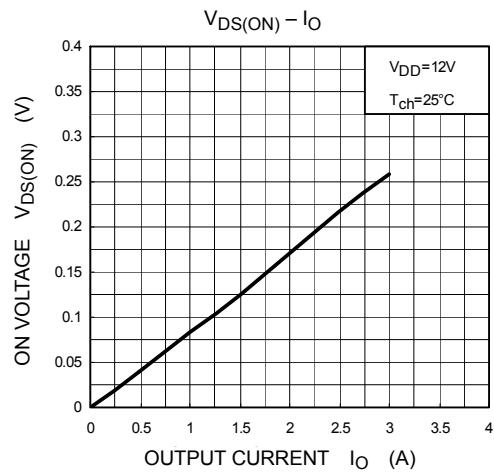
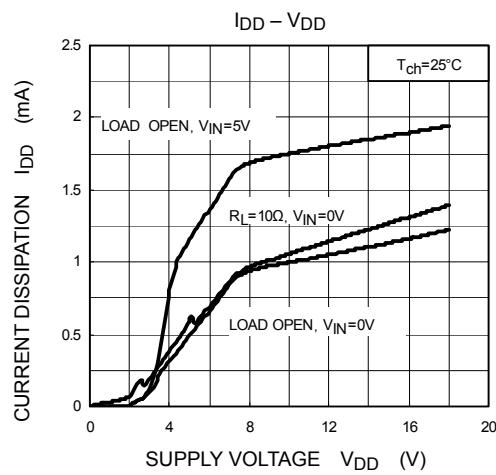
Switching time t_{on} , t_{off}

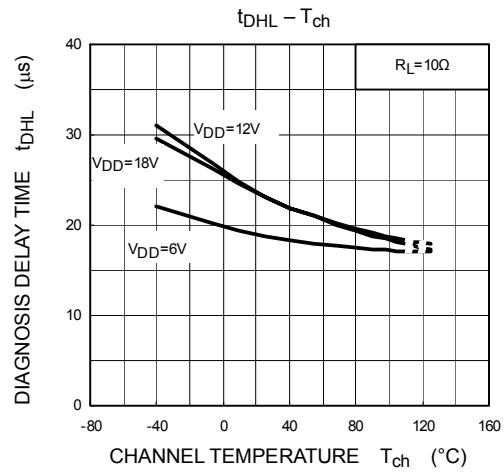
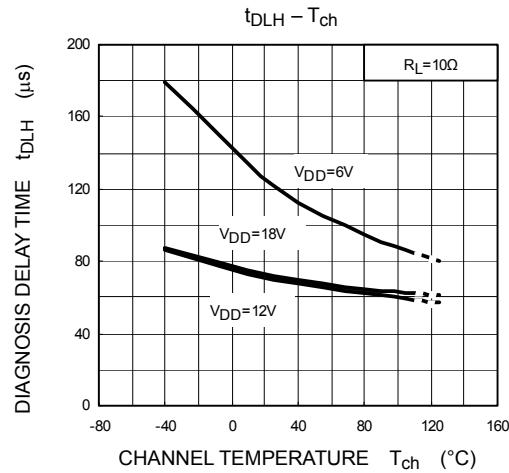
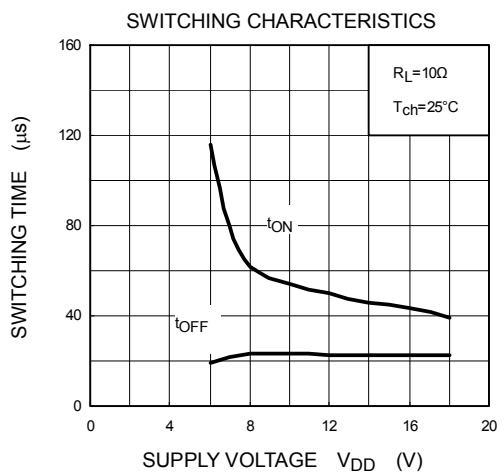
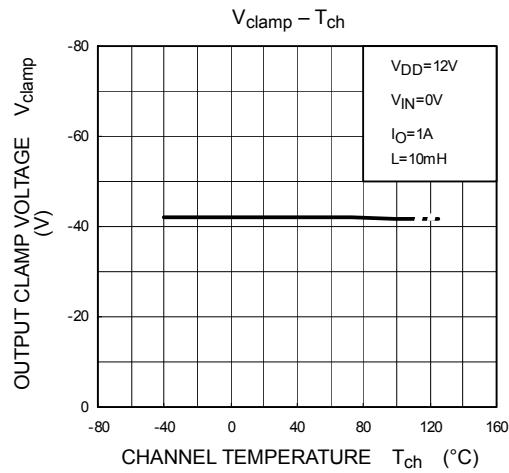
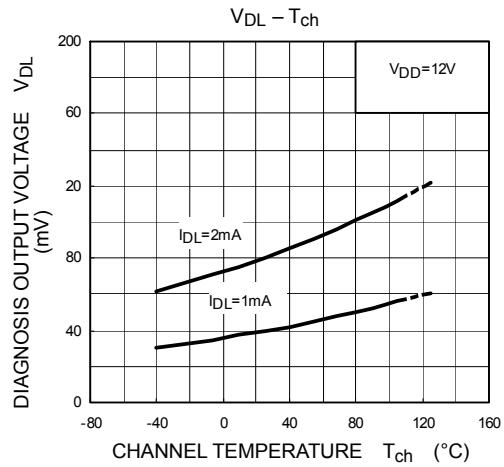
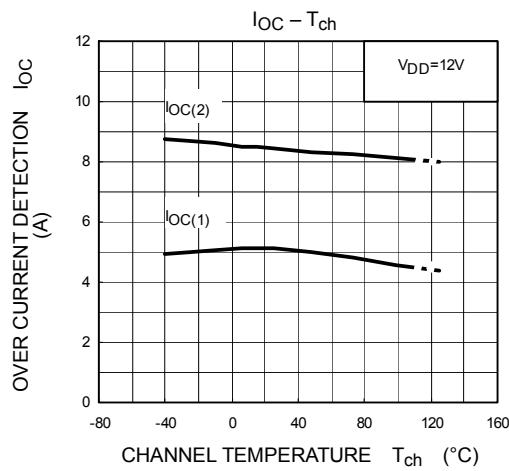


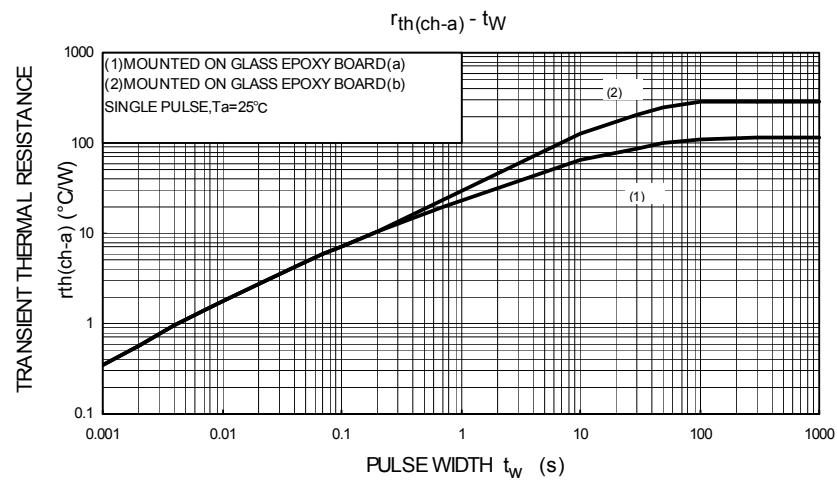
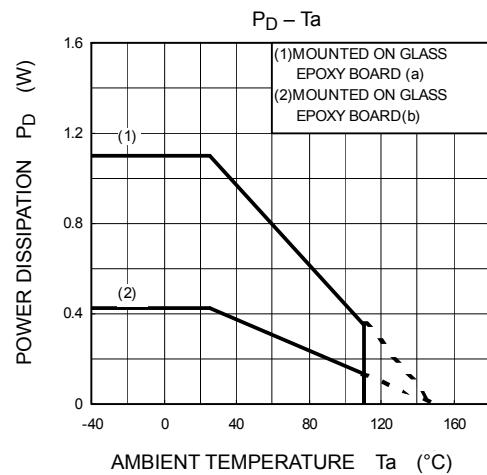
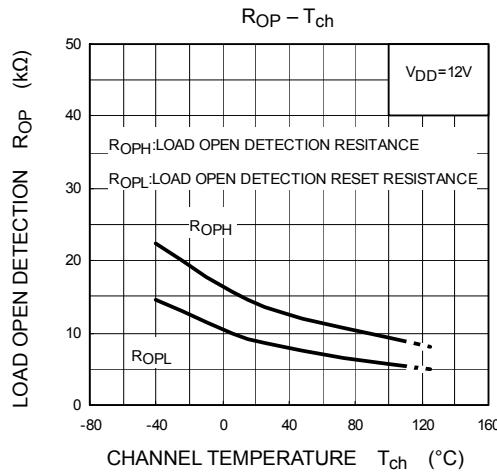
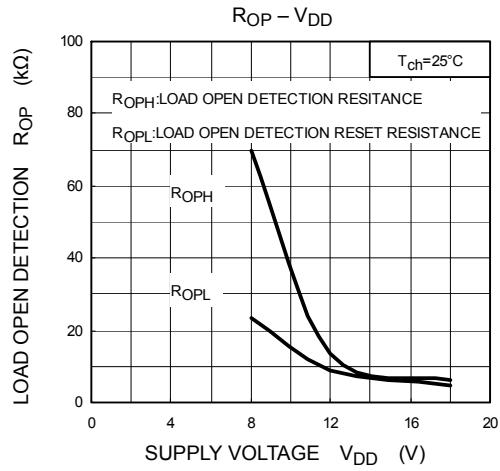
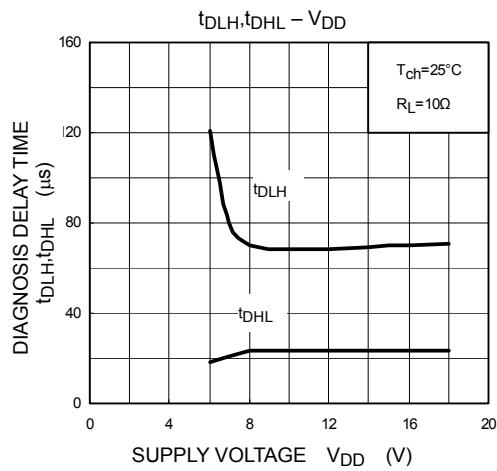
Test circuit 5

Diagnosis delay time t_{DLH} , t_{DHL}

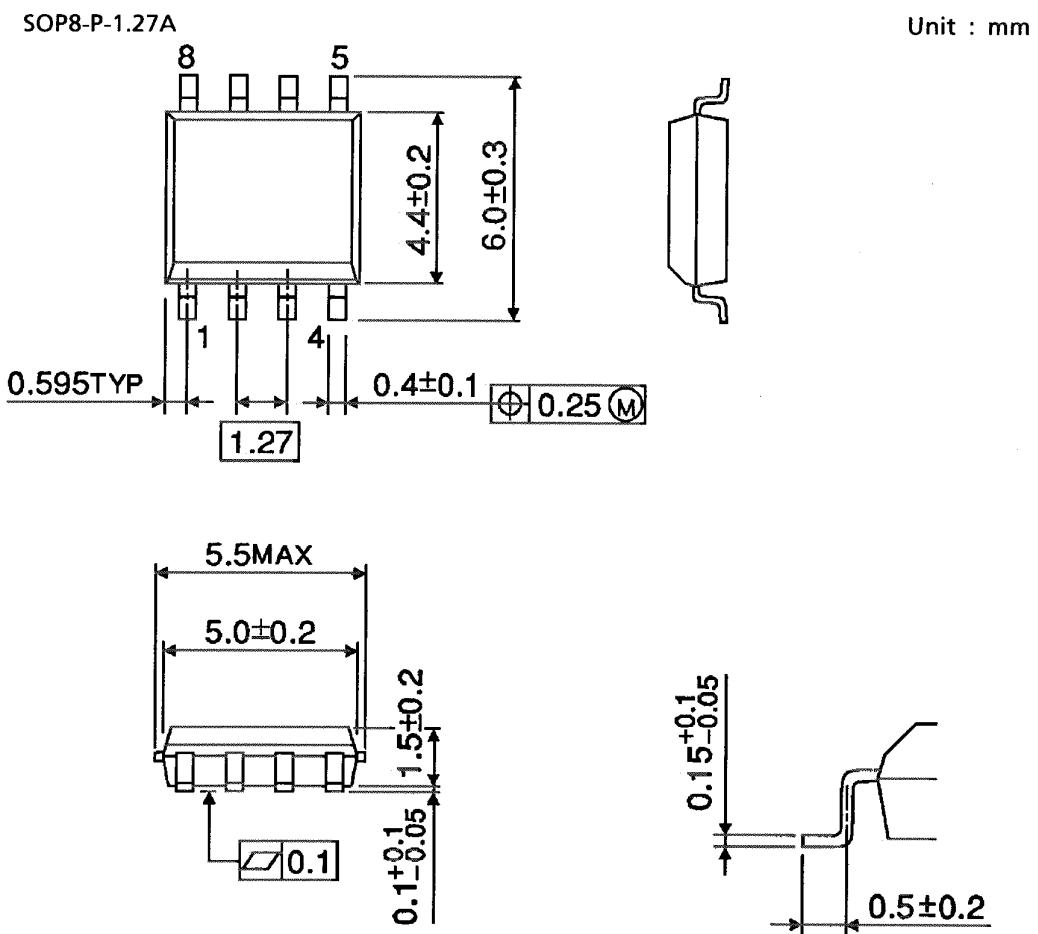








Package dimensions



Weight : 0.08g (typ.)

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