

VI TELEFILTER**Filter Specification****TFS 238****1/5****Measurement condition**

Ambient temperature T_A : 23 °C
 Input power level: 0 dBm
 Terminating impedances at f_C *: input: 450 Ω // -8,3 pF
 output: 430 Ω // -7,3 pF

Characteristics

Remark:

The reference level for the relative attenuation a_{rel} of TFS238 is the minimum of the pass band attenuation a_{min} . This value is defined as the insertion loss a_e . The centre frequency f_C is the arithmetic mean value of the upper and lower frequencies at the 1 dB filter attenuation level relative to the insertion loss a_e . The given values for the relative attenuation a_{rel} and the group delay ripple have to be reached at the frequencies given below, even if the centre frequency f_C is shifted due to the temperature coefficient of frequency TC_f in the operating temperature range and due to a production tolerance for the centre frequency f_C .

Data		typ. value	tolerance / limit
Insertion loss (reference level)	a_e	9,8 dB	max. 12 dB
Nominal frequency	f_N	-	238,5 MHz
Centre frequency	f_C	238,5 MHz	-
Relative attenuation	a_{rel}		
f_N	$f_N \pm 2,5$ MHz	0,4 dB	max. 1 dB
$f_N \pm 5$ MHz	$f_N \pm 7,5$ MHz	23 dB	min. 13 dB
$f_N \pm 7,5$ MHz	$f_N \pm 9$ MHz	38 dB	min. 23 dB
$f_N \pm 9$ MHz	$f_N \pm 10$ MHz	50 dB	min. 40 dB
$f_N \pm 10$ MHz	$f_N \pm 100$ MHz	60 dB	min. 47 dB
Phase variation	within $f_N \pm 2,5$ MHz	1,1 deg rms	max. 4 deg rms
Temperature coefficient of frequency (TC_f)**		-18 ppm/K	-
Frequency deviation of f_C over temperature T:		$\Delta f_C(\text{Hz}) = TC_f(\text{ppm/K}) \times (T - T_A) \times f_{CAT} (\text{MHz})$	
Operating temperature range		-	- 20 °C + 85 °C
Storage temperature range		-	- 40 °C + 85 °C
Input power		-	max. 20 dBm

*) The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions do not hesitate to ask for an application note or contact our design team.

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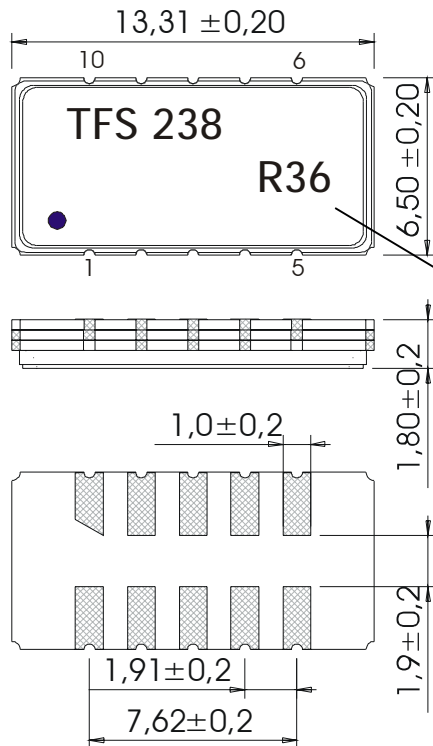
checked / approved: _____

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Construction, pin configuration and 50 Ω - matching network

(All dimensions in mm)

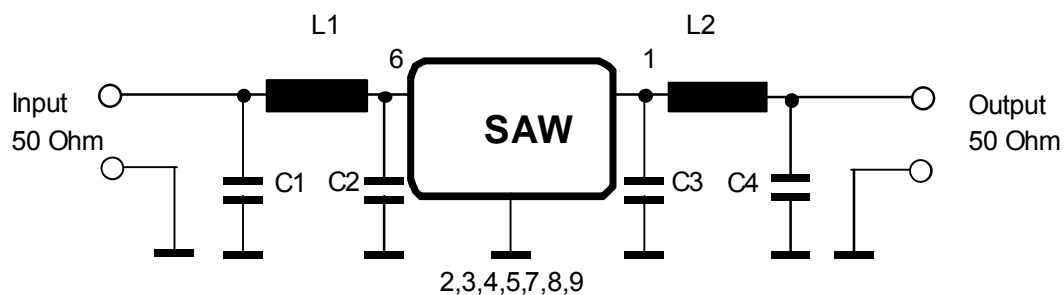


- 1 output
- 2 ground
- 3 ground
- 4 ground
- 5 input rf return
- 6 input
- 7 ground
- 8 ground
- 9 ground
- 10 output rf return

date code: year + week

- N 2001
- P 2002
- R 2003
-

50 Ω test circuit



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Air reflow temperature conditions

1st and 2nd air reflow profile

Name:	pre-heating periods	main-heating periods	peak temperature
Temperature:	150 °C - 170 °	Cover 200 °C	255 °C ± 5 °C
Time:		60 sec. - 90 sec.	20 sec. - 25 sec.

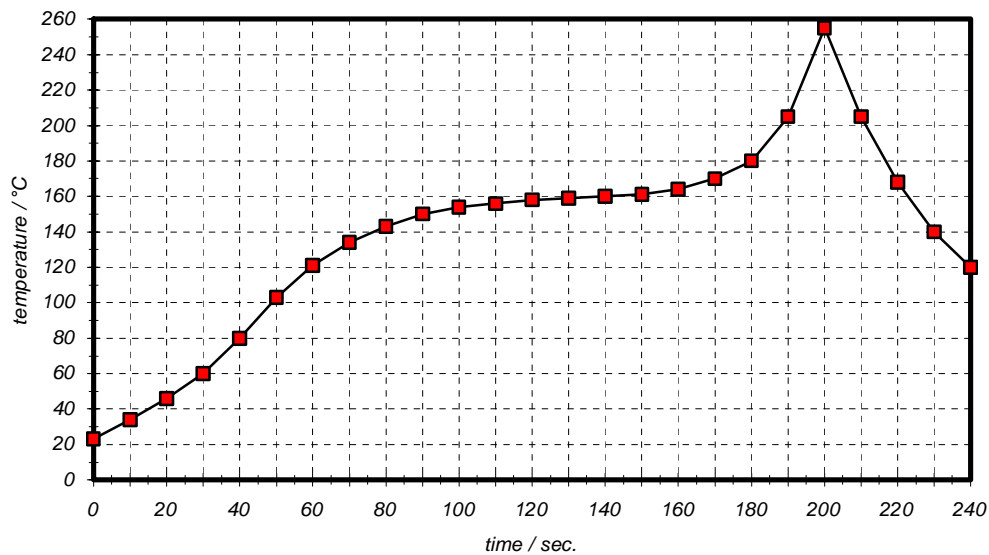
Air reflow profile

Table for temperature vs. time during the air reflow process

Tolerance of temperatures: ± 5 °C

time / sec.	temperature / °C	time / sec.	temperature / °C
0	23	140	160
10	34	150	161
20	46	160	164
30	60	170	170
40	80	180	180
50	103	190	205
60	121	195	230
70	134	200	255
80	143	205	230
90	150	210	205
100	154	215	180
110	156	220	165
120	158	230	140
130	159	240	120

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History

Version	Reason of Changes	Name	Date
1.0	generation of specification	Pfeiffer	24.10.2002
1.1	changing package and relative attenuation adding phase linearity over passband	Pfeiffer	29.11.2002
1.2	changing package and packing	Pfeiffer	13.02.2003
1.3	typical values, matching configuration and terminating impedances added	Pfeiffer	21.03.2003
2.0	typical values and terminating impedances changed	Pfeiffer	16.06.2003
2.1	typical values and terminating impedances changed relative attenuation at $f_N \pm 7,5 \text{ MHz}$... $f_N \pm 9 \text{ MHz}$ added	Pfeiffer	05.09.2003